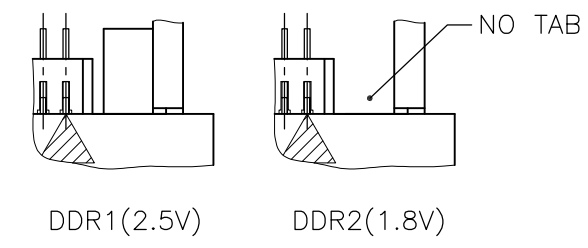
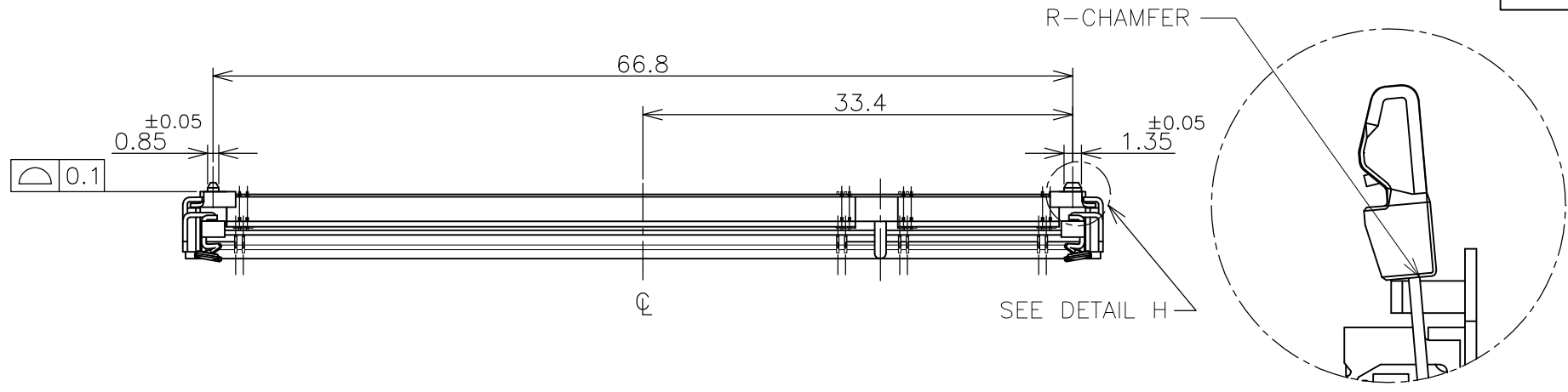


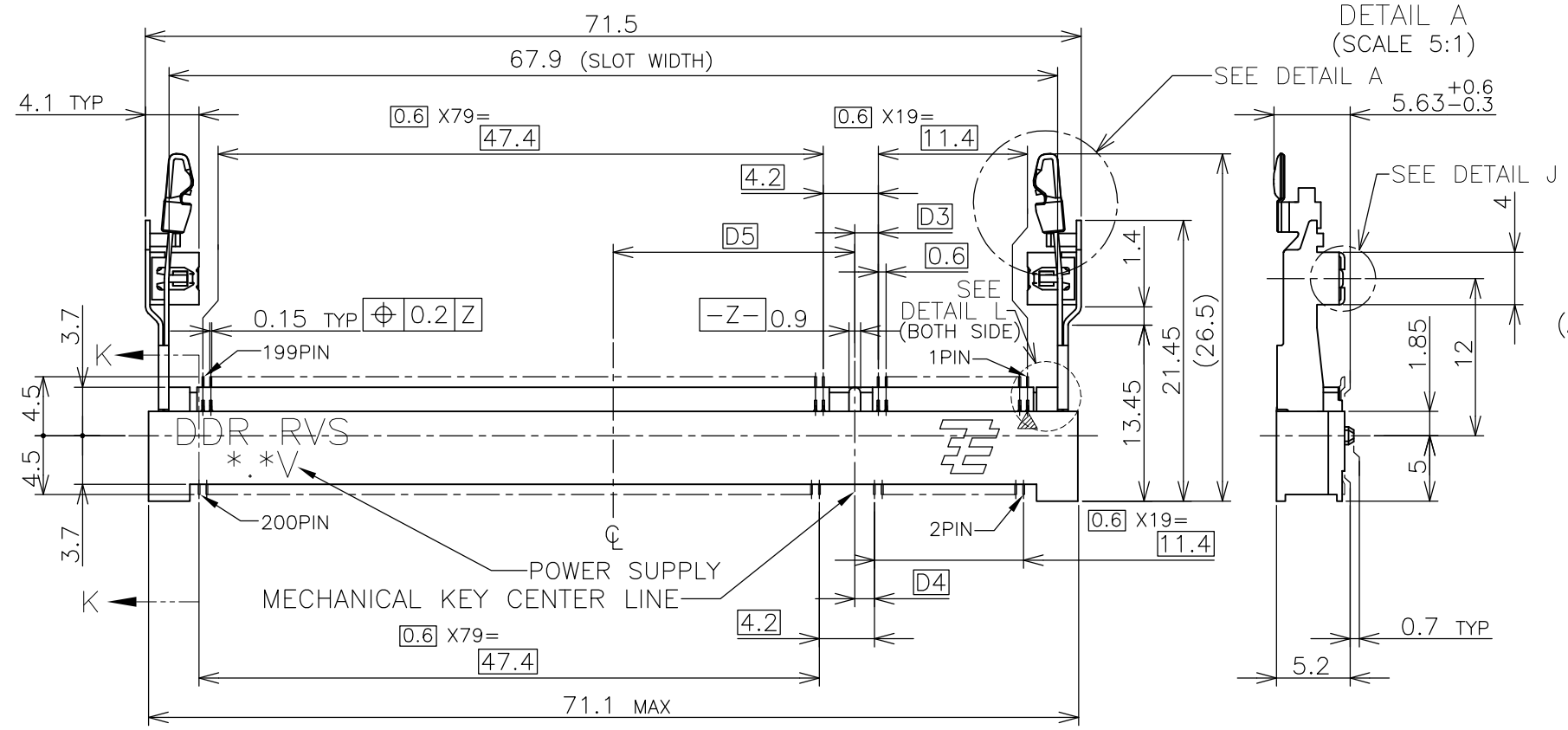
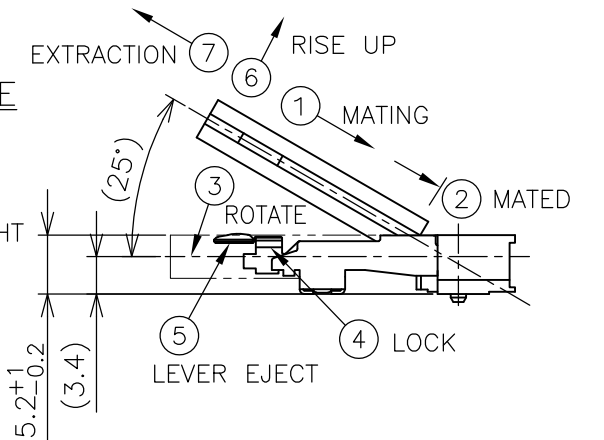
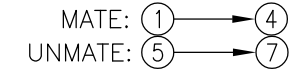
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LOC	DIST	REVISIONS					
ES		P	LTR	DESCRIPTION	DATE	DWN	APVD
			M2	REVISED PER ECO-11-005033	25MAR11	RK	HMR



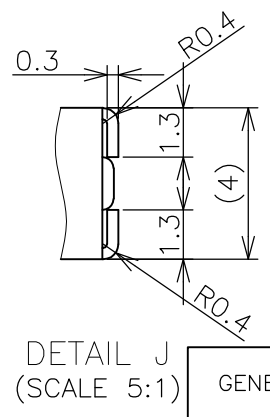
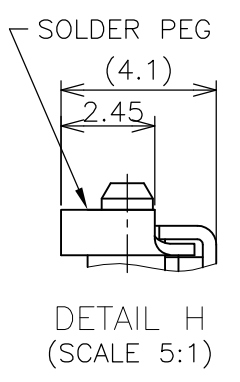
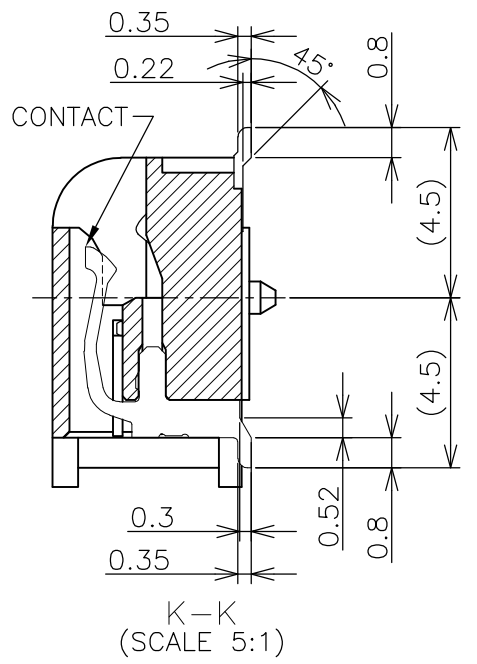
DETAIL L (SCALE 5:1)

HOW TO MATE AND UNMATE



(APPLIED TO SHEET 1-2)

- MATERIAL ; HOUSING: HIGH TEMPERATURE THERMO PLASTIC UL94V-0
CONTACT: COPPER ARROY
LOCK LEVER STAINLESS STEEL
SOLDER PEG: COPPER ARROY
- FINISH ; CONTACT AREA: GOLD FLASH ON 0.0013MIN ALL OVER NICKEL.
SOLDERING AREA: GOLD FLASH PLT.
SOLDER PEG: TIN PLATING.
- MECHANICAL KEY POSITION OF CONNECTOR SEE TABLE.
- TOLERANCES NON-CUMULATIVE.
- NOT TO SCALE.
- INSIDE MUST BE RESIST COAT EXCEPT SOLDER PATTERN.
- IF THE MODULE DOESN'T INSERT AND LOCK IN THE LATCH SMOOTHLY, OPEN WIDE BOTH LATCHES BY MANUAL. AND THE MODULE MUST BE INSERTED AND LOCKED IN THE LATCH.
- FINISH ; CONTACT AREA: 0.00076 GOLD ON 0.0013MIN ALL OVER NICKEL.
SOLDERING AREA: GOLD FLASH PLT.
SOLDER PEG: TIN PLATING.

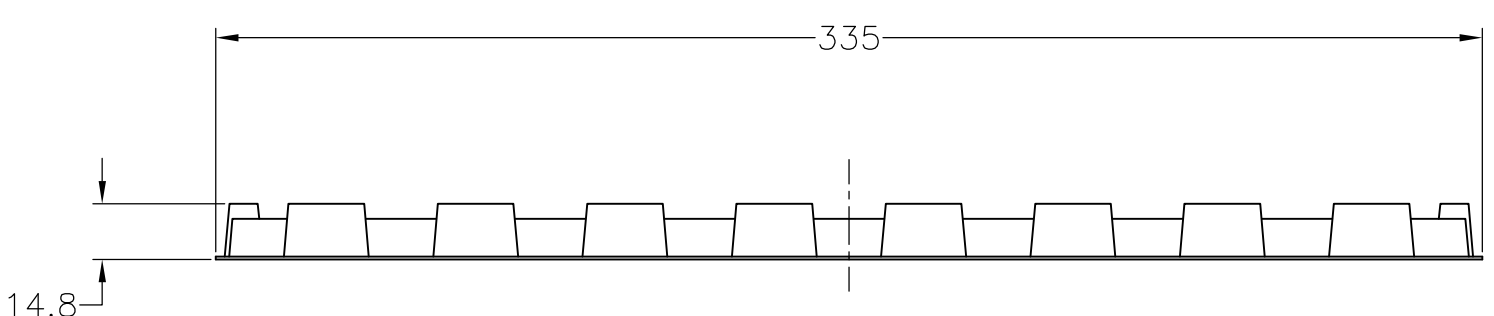
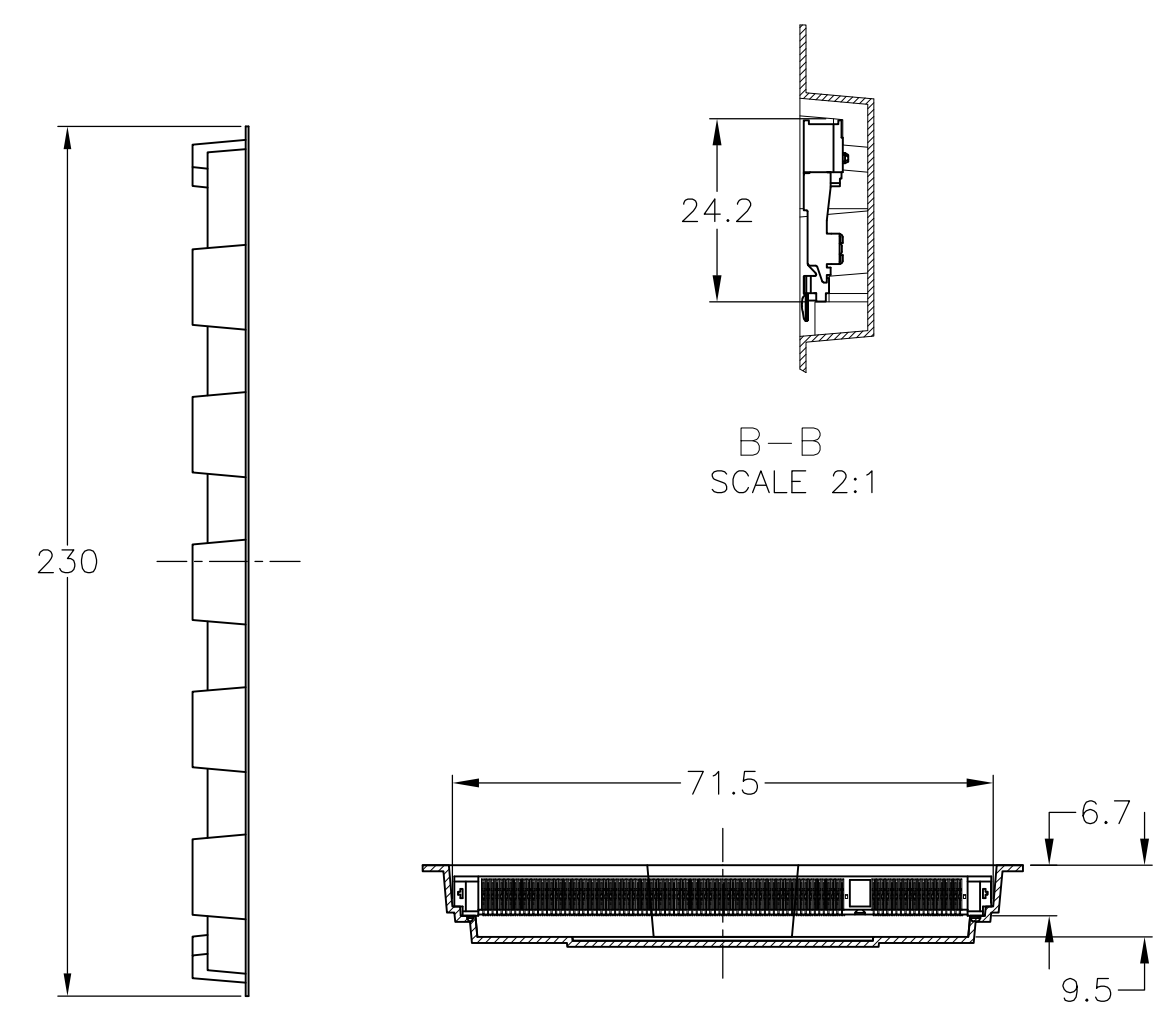
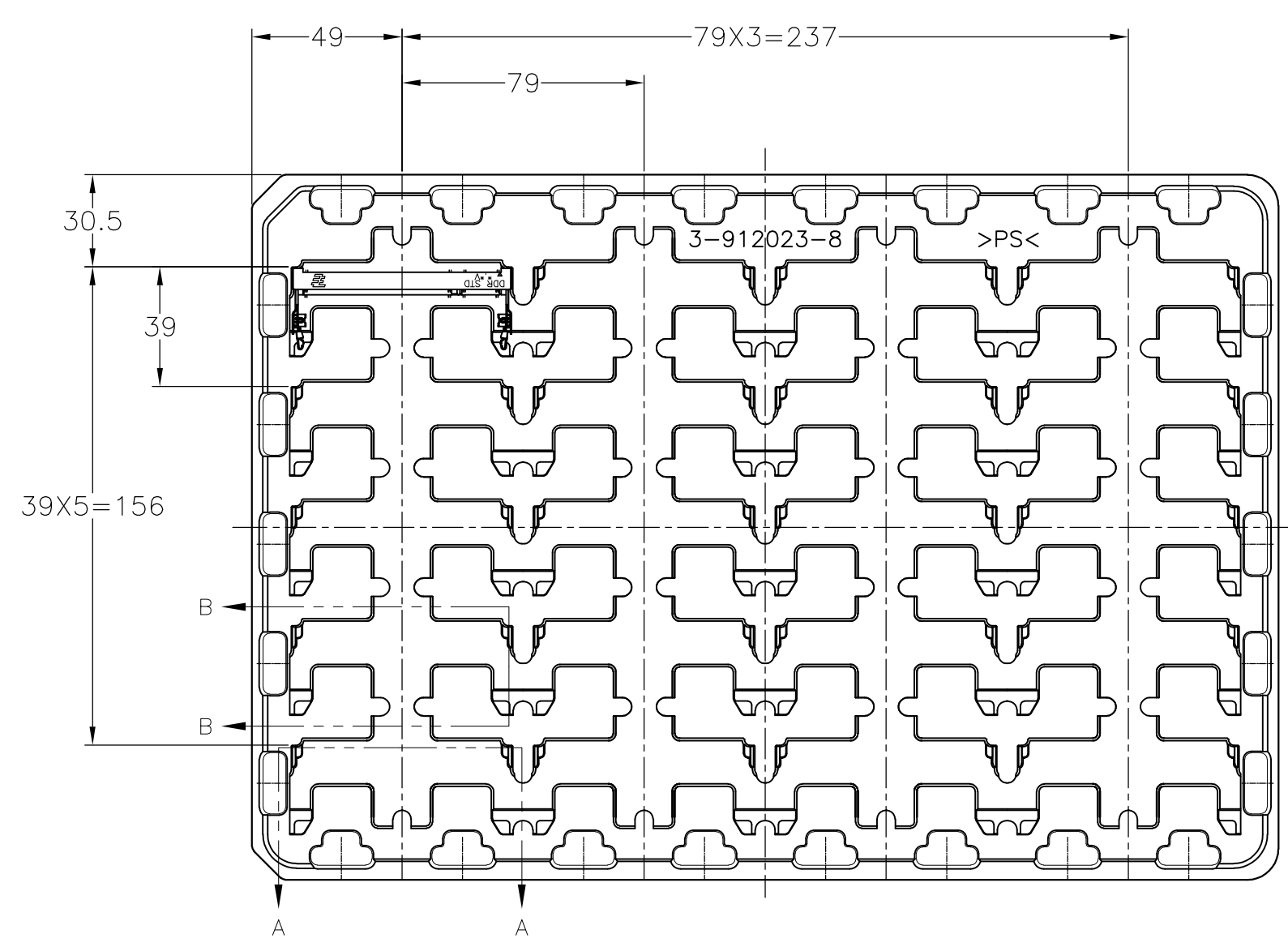


GENERAL TOLERANCE
 10 ≥ : ±0.2
 30 ≥ > 10 : ±0.25
 100 ≥ > 30 : ±0.3
 ANGLES : ±3°

△	AVAILABLE	1.8V (DDR2)	17.55	2.4	2.7	200	1473150-6
△	AVAILABLE	1.8V (DDR2)	17.55	2.4	2.7	200	1-1473150-4
△	AVAILABLE	1.8V (DDR2)	17.55	2.4	2.7	200	1473150-4
△	AVAILABLE	2.5V (DDR1)	18.45	1.5	1.8	200	1473150-1
△	TOOLING STATUS	POWER SUPPLY	D5	D4	D3	POS	PART NO.
THIS DRAWING IS A CONTROLLED DOCUMENT.			DWN B.WONG 20JUL10		TE Connectivity DDR1 & DDR2 SODIMM SOCKET 0.6mm PITCH 200POS STANDARD PROFILE REVERSE TYPE (NEW LATCH TYPE)		
DIMENSIONS: MM			CHK C.WANG 20JUL10				
TOLERANCES UNLESS OTHERWISE SPECIFIED:			APVD S.YAO 20JUL10				
0 PLC ± - 1 PLC ± - 2 PLC ± - 3 PLC ± - 4 PLC ± - ANGLES ± - FINISH ± -			NAME				
MATERIAL			PRODUCT SPEC 108-5701		SIZE		
			APPLICATION SPEC		CAGE CODE		
			WEIGHT 4.7g		DRAWING NO		
			CUSTOMER DRAWING		RESTRICTED TO		
			SCALE 2:1		SHEET 1 OF 4		
			REV M2				

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LOC		DIST		REVISIONS			
P	LTR	DESCRIPTION		DATE	DWN	APVD	
	-	SEE SHEET 1		-	-	-	



REMARK	QTY	P/N
DDR2	20EA/TRAY	1473150-6
DDR2	20EA/TRAY	1-1473150-4
DDR2	20EA/TRAY	1473150-4
DDR1	20EA/TRAY	1473150-1

- (APPLIED TO SHEET 4)
1. THE DIRECTION OF EACH CONNECTOR ON A TRAY IS REFERRED TO THIS DRAWING.
 2. GENERAL TOLERANCE: ±0.5

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN		STE TE Connectivity	
DIMENSIONS: MM		CHK			
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD		NAME	
0 PLC ±		PRODUCT SPEC		DDR1 & DDR2 SODIMM SOCKET	
1 PLC ±		APPLICATION SPEC		0.6mm PITCH 200POS	
2 PLC ±		WEIGHT		STANDARD PROFILE REVERSE TYPE (NEW LATCH TYPE)	
3 PLC ±		CUSTOMER DRAWING		SIZE CAGE CODE DRAWING NO RESTRICTED TO	
4 PLC ±		SCALE 1:2		A3 00779 C-1473150	
ANGLES ±		SHEET 4 OF 4		REV M2	
FINISH		REV M2			

单击下面可查看定价，库存，交付和生命周期等信息

[>>TE Connectivity\(泰科\)](#)