

800V Super-Junction Power MOSFET

FEATURES

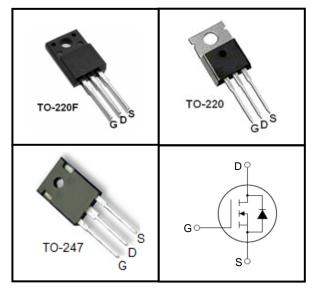
- $\bullet \quad \text{Very low FOM R}_{\text{DS(on)}} \times \text{Q}_{\text{g}} \\$
- 100% avalanche tested
- RoHS compliant



APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device	Package Marking			
TPA80R250A	TO-220F	80R250A		
TPP80R250A	TO-220	80R250A		
TPW80R250A	TO-247	80R250A		



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Parameter		Symbol	Value		11-14
		Syllibol	TO-220F	TO-220,TO-247	Unit
Drain-Source Voltage (V _{GS} =	0V)	V _{DSS}	800		V
Continuous Drain Current		-	18		- А
		I _D	10.8		
Pulsed Drain Current	(note1)	I _{DM}	5	54	А
Gate-Source Voltage		V_{GSS}	±30		V
Single Pulse Avalanche Energy (note2)		E _{AS}	280		mJ
Avalanche Current (note1)		I _{AR}	7.5		Α
Repetitive Avalanche Energy (note1)		E _{AR}	0.5		mJ
Power Dissipation (T _C = 25°C)		P _D	34	240	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150		°C

Thermal Resistance					
Parameter	Symbol		lue	Unit	
1 diameter	Oymbor	TO-220F	TO-220,TO-247	Oille	
Thermal Resistance, Junction-to-Case	R_{thJC}	3.67	0.52	00/14/	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	80	62	°C/W	

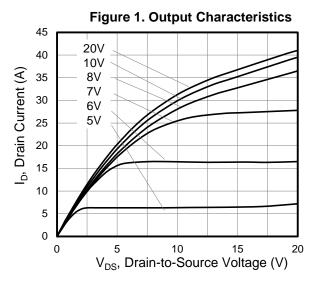


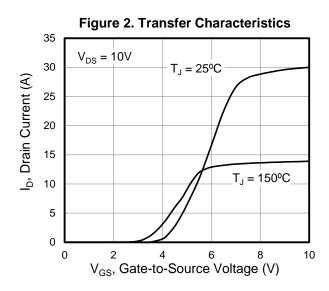
Specifications $T_J = 25^{\circ}C$, ur	less othe	rwise noted				
Parameter	Symbol	Test Conditions	Value			Unit
T di dillocol	arameter Symbol Test C		Min.	Тур.	Max.	Oille
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	800			V
Zoro Coto Voltago Proin Current		$V_{DS} = 800V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 800V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	μA
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 9A		0.24	0.28	Ω
Forward Transconductance (Note3)	g _{fs}	$V_{DS} = 10V, I_{D} = 9A$		10		S
Dynamic		•				
Input Capacitance	C _{iss}	V 9V		3871		pF
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 50V,$		217		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		17.1		
Total Gate Charge	Q_g			62.3		nC
Gate-Source Charge	Q_{gs}	$V_{DD} = 640 \text{V}, I_{D} = 18 \text{A}, $ $V_{GS} = 10 \text{V}$		14.5		
Gate-Drain Charge	Q_{gd}	65		23		
Turn-on Delay Time	t _{d(on)}			49		
Turn-on Rise Time	t _r	$V_{DD} = 400V, I_{D} = 9A,$		42.6		
Turn-off Delay Time	t _{d(off)}	$R_G = 25\Omega$		166		ns
Turn-off Fall Time	t _f			13		
Drain-Source Body Diode Characteris	stics					
Continuous Body Diode Current	I _s	T 0500			18	Α.
Pulsed Diode Forward Current	I _{SM}	T _C = 25°C			54	Α
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 18A$, $V_{GS} = 0V$		0.9	1.2	V
Reverse Recovery Time	t _{rr}			400		ns
Reverse Recovery Charge	Q _{rr}	$V_R = 400V, I_F = I_S,$ $di_F/dt = 100A/\mu s$		4		μC
Peak Reverse Recovery Current	I _{rrm}	3. _F , 3.1 1007 0 p 0		20		Α

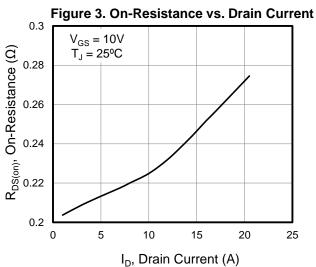
Notes

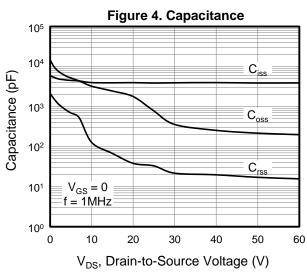
- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. I_{AS} = 7.5A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 1%

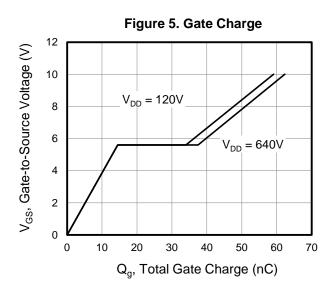
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

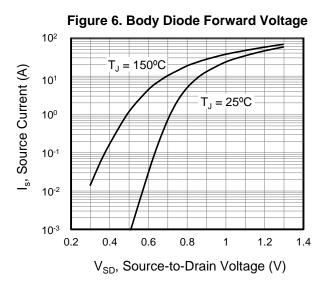












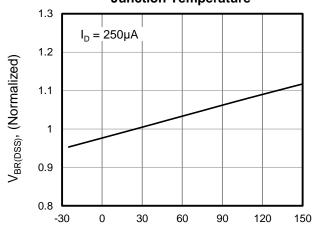


Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 7. On-Resistance vs. **Junction Temperature** 3 2.5 R_{DS(on)}, (Normalized) 2 $V_{GS} = 10V$ $I_{\rm D} = 7.5A$ 1.5 1 0.5 0 -100 -50 50 100 150 200 T_J, Junction Temperature (°C)

Figure 8. Threshold Voltage vs. **Junction Temperature** 0.4 $I_{D} = 250 \mu A$ $V_{GS(th)}$, (Variance) 0 -0.4 -0.8 -1.2 -100 -50 50 100 150 200 T_J, Junction Temperature (°C)

Figure 9. Breakdown voltage vs. Junction Temperature



T_J, Junction Temperature (°C)



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 9. Transient Thermal Impedance

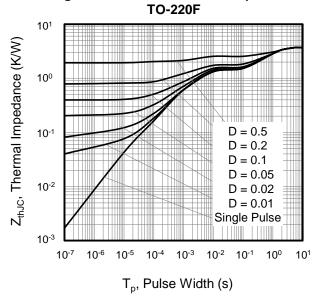


Figure 9. Transient Thermal Impedance TO-220,TO-247

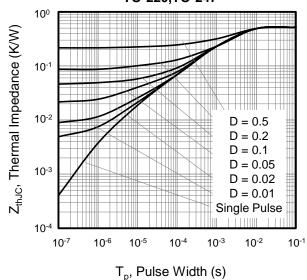
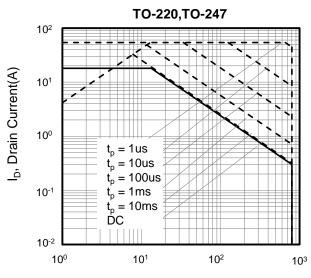


Figure 13. Safe operation area for

TO-220F 10^{2} 10¹ I_D, Drain Current(A) 100 = 1us = 10us = 100us 10-1 $t_p = 1 ms$ $t_p = 10ms$ ĎС 10-2 10¹ 10² 100 10³

 V_{DS} , Drain-Source Voltage(V)

Figure 12. Safe operation area for



V_{DS}, Drain-Source Voltage(V)



Figure A: Gate Charge Test Circuit and Waveform

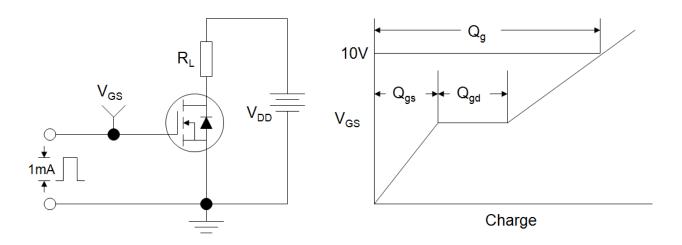


Figure B: Resistive Switching Test Circuit and Waveform

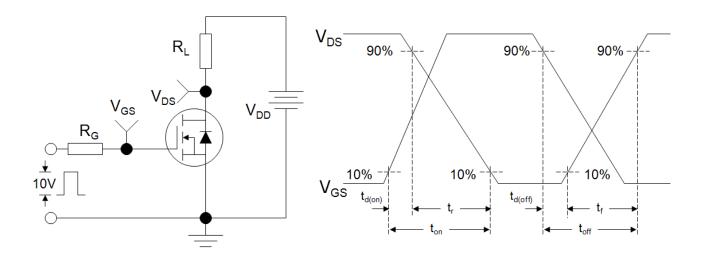
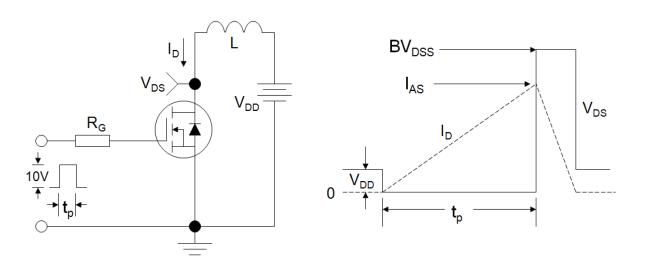
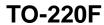
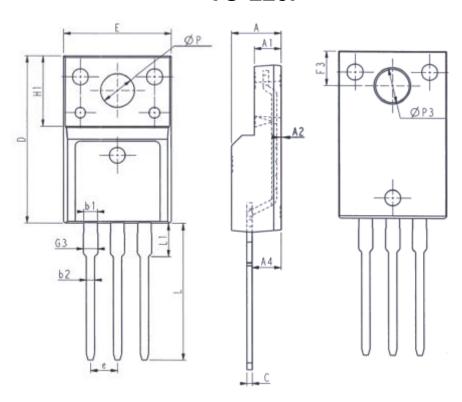


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





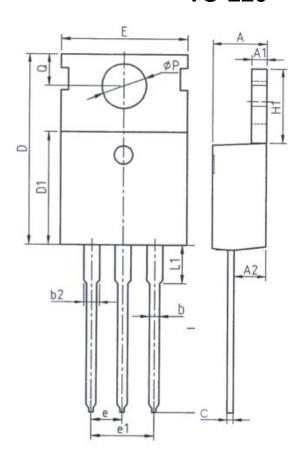


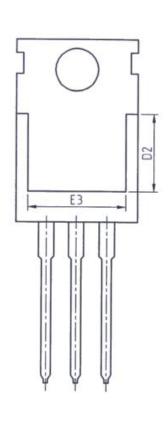


Unit: mm			Unit: mm		
Symbol	Min.	Max.	Symbol	Min.	Max.
E	9.96	10.36	L	12. 68	13. 28
Α	4. 50	4. 90	L1	2. 93	3. 13
A 1	2. 34	2. 74	Р	3. 03	3. 38
A2	0.30	0.60	P3	3. 15	3. 65
A4	2. 56	2. 96	F3	3. 15	3. 45
С	0.40	0. 65	G3	1. 25	1. 55
D	15. 57	16. 17	b1	1. 18	1. 43
H1	6. 70REF		b2	0. 70	0. 95
е	2. 54BSC				



TO-220



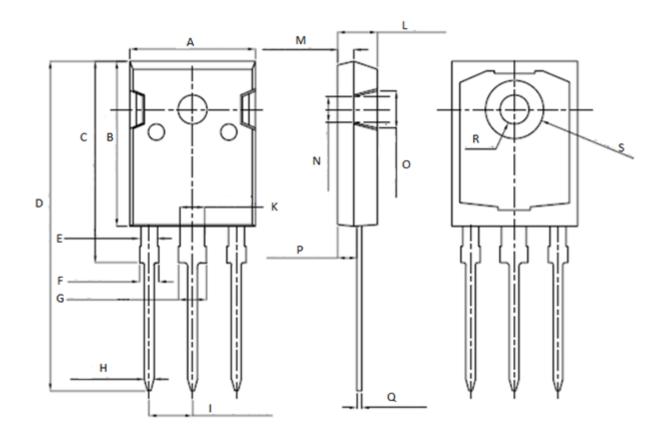


Unit: mm				
Symbol	Min.	Max.		
Α	4. 37	4. 77		
A1	1. 25	1. 45		
A2	2. 20	2. 60		
b	0. 70	0. 95		
b2	1. 17	1. 47		
С	0. 40	0. 65		
D	15. 10	16. 10		
D1	8. 80	9. 40		
D2	5. 50	_		

Unit: mm				
Symbol	Min. Max.			
E	9. 70	10. 30		
E3	7. 00 -			
е	2. 54BSC			
e1	5. 08BSC			
H1	6. 25	6. 85		
L	12. 75	13. 80		
L1	-	3. 40		
Р	3. 40	3. 80		
Q	2. 60	3. 00		



TO-247



Unit: mm				
Symbol	Min.	Max.		
Α	15. 95	16. 25		
В	20. 85	21. 25		
С	20. 95	21. 35		
D	40.5	40. 9		
Е	1. 9	2. 1		
F	2. 1	2. 25		
G	3. 1	3. 25		
Н	1.1	1.3		
I	5. 40	5. 50		

Unit: mm				
Symbol	Min.	Max.		
K	2. 90	3. 10		
L	4. 90	5. 30		
M	1. 90	2. 10		
N	4. 50	4. 70		
0	5. 40	5. 60		
Р	2. 29	2. 49		
Q	0. 51	0. 71		
R	ф 3. 5	ф 3. 7		
S	ф 7. 1	ф 7. 3		



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