P-channel MOSFET -30V, -1.5A

ETR11040-001

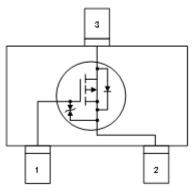
FEATURES

**On-State Resistance Driving voltage** 

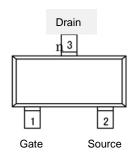
 $: R_{DS}(on) = 0.19 \Omega @V_{GS} = -10 V$ : -4.5V Environmentally Friendly : EU RoHS Compliant, Pb Free



## ■EQUIVALENT CIRCUIT



■ PIN CONFIGURATION ●SOT-23(TO-236)



### ■ PRODUCT NAME

PRODUCT NAME	PACKAGE	ORDER UNIT
XP233P1501TR-G *	SOT-23(TO-236)	3,000 pcs/ Reel

\* The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant

## ■ABSOLUTE MAXIMUM RATINGS

			Ta=25°C
PARAMETER	SYMBOL	RATINGS	UNITS
Drain-Source Voltage	VDSS	-30	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Drain Current (DC)	lD	-1.5	А
Drain Current(Pulse) (*1)	I <sub>DP</sub>	-3	А
Channel Power Dissipation (*2)	Pd	0.4	W
Junction Temperature	TJ	150	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C

 $^{(*1)}$ PW  $\leq 10\mu$ s,duty cycle  $\leq 1\%$ 

(\*2)When implemented on a PCB defined by JESD51-7

## ■ELECTRICAL CHARACTERISTICS

						Ta=25°C
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = -250µA, V <sub>GS</sub> =0V	-30	-	-	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V	-	-	-1	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS}$ = ±15V, $V_{DS}$ = 0V	-	-	±10	μA
Gate Threshold Voltage	V <sub>GS(off)</sub>	$I_D$ = -250uA, $V_{DS}$ = $V_{GS}$	-0.9	-1.4	-1.9	V
Drain-Source On Resistance	<b>D</b>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -1A	-	0.13	0.19	Ω
Drain-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -1A	-	0.22	0.33	Ω
Input Capacitance	Ciss		-	160	-	pF
Output Capacitance	Coss	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f=1MHz	-	50	-	pF
Reverse Transfer Capacitance	Crss		-	28	-	pF
Turn-on Delay Time	t <sub>d(on)</sub>		-	40	-	ns
Rise Time	tr	V <sub>DD</sub> = -10V, I <sub>D</sub> = -1A V <sub>GS</sub> = -10V	-	75	-	ns
Turn-off Delay Time	t <sub>d(off)</sub>		-	470	-	ns
Fall Time	t <sub>f</sub>		-	230	-	ns
Diode Forward Voltage	Vsd	Is= -1A, V <sub>GS</sub> = 0V	-	-0.7	-1.1	V

### ■NOTES ON USE

1. Please use this IC within the absolute maximum ratings.

Even within the ratings, in case of high load use continuously such as high temperature, high voltage, high current and thermal stress may

cause reliability degradation of the IC.

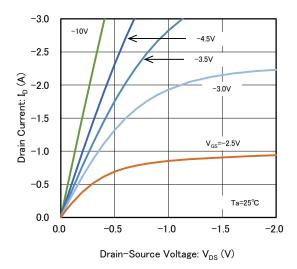
2. Torex places an importance on improving our products and their reliability.

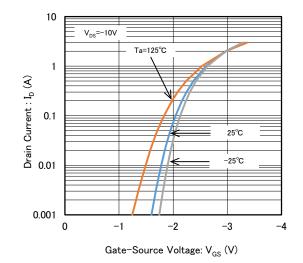
We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

### ■TYPICAL PERFORMANCE CHARACTERISTICS

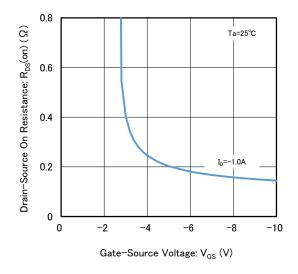
(1) Drain Current vs. Drain-Source Voltage

(2) Drain Current vs. Gate-Source Voltage

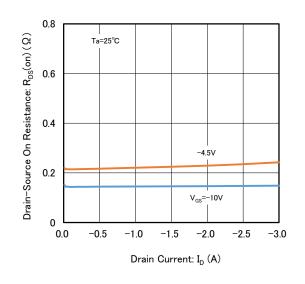


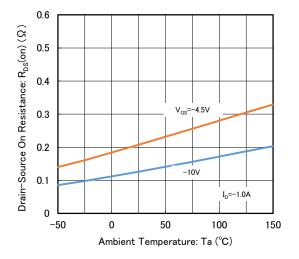


(3) Drain-Source On-State Resistance vs. Gate-Source Voltage (4) Drain-Source On-State Resistance vs. Ambient Temperature

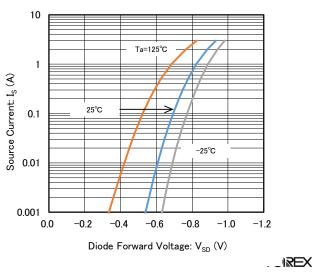


(5) Drain-Source On-State Resistance vs. Drain Current





(6) Source Current vs. Diode Forward Voltage

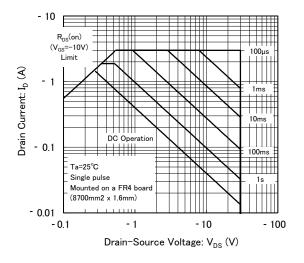


## ■TYPICAL PERFORMANCE CHARACTERISTICS

#### (7) Ciss, Coss, Crss vs. Drain-Source Voltage

#### 1000 f=1MHz, Ta=25°C Capacitance: Ciss, Coss, Crss (pF) Ciss 100 Coss Crss 10 1 0 -5 -10 -15 -20 -25 -30 Drain-Source Voltage: $V_{DS}$ (V)

(8) Area of Safe Operation



## ■ PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS	
SOT-23(TO-236)	<u>SOT-23(TO-236) PKG</u>	JESD51-7 Board	SOT-23(TO-236)PowerDissipation

## ■MARKING RULE

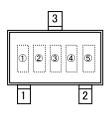
#### ●SOT-23(TO-236)

① ②, ③represents product series

MARK			PRODUCT SERIES		
1	2	3	PRODUCT SERIES		
3	3	Р	XP233P1501**-G		

④, ⑤ represents production lot number
01 to 09, 0A to 0Z, 11 to 9Z, A1 to A9, AA to AZ, B1 to ZZ repeated
(G, I, J, O, Q, W excluded)
\*No character inversion used

SOT-23(TO-236)



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