ETR0315_006a

1A Low Dropout Positive Voltage Regulator

■GENERAL DESCRIPTION

The XB1117 series is 1A Bi-polar transistor LDO voltage regulator. Output voltage of the XB1117P series is fixed to 1.8V, 2.5V, 3.3V, and 5.0V. The XB1117K series output voltage is adjustable by the external resistors. Please refer to the absolute maximum ratings for the difference between the rated input voltage of the XB1117P50 (Vout=5.0V) and XB1117P18, 25, 31, K12B. With the dropout voltage 1.2V (TYP.), output current can be generated up to 1A. The built-in overcurrent circuit and thermal protection circuit start to operate when either one of output put current reaches the current limit level or junction temperature reaches the temperature limit. The XB1117 series provide stable line and load regulation by using an input capacitor and an output capacitor (10 μ F, tantalum). Package is available in SOT-223.

■ APPLICATIONS

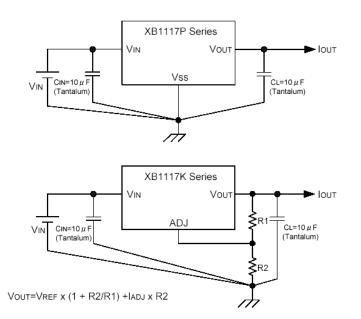
- Highly efficient linear regulators
- •5V ~ 3.3V DC / DC converter
- Battery charger
- Local power supply inside equipment
- Battery powered equipment

FEATURES

Maximum Output Current Output Voltages Output Voltage Accuracy Low Dropout Voltage Line Regulation (TYP.) Load Regulation (TYP.) Adjust Pin Current Protection Circuit

- Package Environmentally Friendly
- : 1A
 : 1.8V, 2.5V, 3.3V, 5.0V, ADJ
 : ±1%
 : 1.2V @ IOUT=1A
 : 0.04% (ADJ)
 : 0.1% (ADJ)
 : Less than 120 μ A (ADJ)
 : Over-current protection Thermal protection
 : SOT-223
 - : EU RoHS Compliant, Pb Free

TYPICAL APPLICATION CIRCUIT



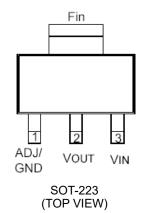
■ TYPICAL PERFORMANCE CHARACTERISTICS

Reference Voltage vs. Ambient Temperature

XB1117K12B VIN=5.0V, IOUT=1A, CIN=10 µ F (tantalum). CL=10 µ F (tantalum) 1.27 Reference Voltage: Vref (V) 1.26 1.25 1.24 1.23 1.22 0 10 20 30 40 50 60 70 Ambient Temperature (°C)

XB1117 Series

■ PIN CONFIGURATION



■ PIN ASSIGNMENT

PIN NUMBER	PIN NAME	FUNCTIONS
1	ADJ/GND	ADJ/Ground
2	Vout	Output
3	Vin	Input

* The electrical potential of the package fin is the same as the VOUT pin.

■ PRODUCT CLASSIFICATION

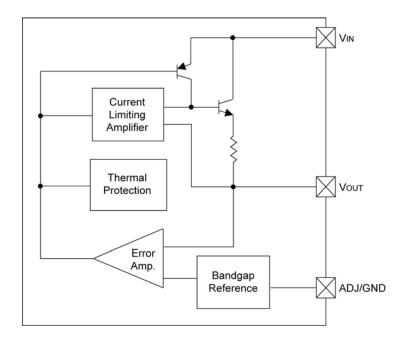
Ordering Information

XB1117123456-7

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION	
(1)		Р	Fixed Vout type	
Ū	Type of Regulators	К	Adjustable Vout type	
		181	Fixed Vout 1.80V (±1%)	
		251	Fixed Vout 2.50V (±1%)	
234	Output Voltage	& Accuracy	331	Fixed Vout 3.30V (±1%)
		501	Fixed Vout 5.00V (±1%)	
		12B	Adjustable Vout 1.25V (±1%)	
(5)6-7)(*1)	Package (Order Unit)	FR-G	SOT-223(1,000/Reel)	

 $\ensuremath{^{(*1)}}$ The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

■BLOCK DIAGRAM



■ABSOLUTE MAXIMUM RATINGS

XB1117P501

PARAMETER	SYMBOL	RATINGS	UNITS
Input Voltage	Vin	10.0	V
Thermal Resistance	θJc	15	
(Junction to Case)	000	15	°C/W
Thermal Resistance	θ Ја	160	C/VV
(Junction to Ambient)	0 JA	100	
Power Dissipation	PD	625	mW
(∆T=100°C)	FD	025	11100
Operating Ambient Temperature	Topr	0~+70	
Operating Junction Temperature Range	Tj	0~+125	⊃°
Storage Temperature Range	Tstg	- 65 ~+ 150	
Lead Temperature	Tlead	260	

*Stress above the listed absolute maximum rating may cause permanent damage to the device.

** The rated values of the XB1117P18 / 25 / 30 (VOUT=1.8V, 2.5V and 3.0V) and XB1117K type are different from that of the XB1117P50 (VOUT=5.0V).

■ELECTRICAL CHARACTERISTICS

XB1117P501	•						Ta=25°C
PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS
	Vout	VIN=7.0V		4.950	5.000	5.050	V
Output Voltage	VUUI	IOUT=0A	*Over Temp.	4.900	5.000	5.100	v
Line Regulation	ΔVout1	7.0V≦VIN≦9.0V Iouт=0A	*Over Temp.	-	1	6	
Lood Pogulation	Δ Vout2	VIN=7.0V		-	5.0	15.2	mV
Load Regulation		0A≦Iout≦1.0A	*Over Temp.	-	10.1	20.2	
Dropout Voltago	Vdif	Δ Vout=±1%		-	1.2	1.4	V
Dropout Voltage	Vali	0A≦Iout≦1.0A	*Over Temp.	-	1.3	-	v
Current Limit	ILIM	7.0V≦VIN≦10.0V	*Over Temp.	1.0	1.5	I	А
Supply Current	Iss	VIN=7.0V 0A≦Iout≦1.0A	*Over Temp.	-	6	13	mA
Temperature Coefficient	Тс	7.0V≦Vin≦10.0V 0A≦Iout≦1.0A		-	50	-	ppm/°C
Temperature Stability	Ts	VIN=7.0V IOUT=100mA	*Over Temp.	-	0.5	-	%

*Over Temp. = Over Temperature $(0 \sim +70^{\circ}C)$

■ABSOLUTE MAXIMUM RATINGS

XB1117P181, P251, P331, K12B

PARAMETER	SYMBOL	RATINGS	UNITS
Input Voltage	Vin	7.0	V
Thermal Resistance (Junction to Case)	θJc	15	°C/W
Thermal Resistance (Junction to Ambient)	θJA	160	0/11
Power Dissipation ($\Delta T=100^{\circ}C$)	PD	625	mW
Operating Ambient Temperature	TOPR	0 ~ 70	
Operating Temperature Range	TJ	0 ~ 125	⊃°
Storage Temperature Range	Tstg	-65 ~ 150	
Lead Temperature	TLEAD	260	

*Stress above the listed absolute maximum rating may cause permanent damage to the device.

■ELECTRICAL CHARACTERISTICS

XB1117P181						T	J=25°C
PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS
Output Voltage	Vout	Vin=5.0V Iout=0A	*Over Temp.	1.782	1.800	1.818	V
Line Regulation	ΔVout1	4.75V≦VIN≦7.0V Iout = 0A		-	1.0	5.5	mV
Load Regulation	ΔVout2	Vin=5.0V 0A≦Iout≦1.0A	*Over Temp.	-	1.80 3.70	18.2 22.0	mV
Dropout Voltage	Vdif	$\Delta Vout=\pm 1\%$ $0A \leq lout \leq 1.0A$	*Over Temp.	-	1.2 1.3	1.4	V
Current Limit	ILIM	4.75V≦VIN≦7.0V	*Over Temp.	1.0	1.5	-	Α
Supply Current	lss	Vin=5.0V 0A≦Iout≦1.0A	*Over Temp.	-	6	13	mA
Temperature Coefficient	Тс	4.75V≦VIN≦7.0V 0A≦Iout≦1.0A			50	-	ppm/ °C
Temperature Stability	Ts	VIN=5.0V Iout=100mA	*Over Temp.	-	0.5	-	%
*Over Temp. = Over Tempe	rature (0∼+7	(3°0)					•

XB1117P251

TJ =25°C

				OTAN			<u> </u>
PARAMETER	SYMBOL	CONDITIONS				-	UNITS
				MIN.	TYP.	MAX.	
Output Voltage	Vout	VIN=5.0V		2.475	2.500	2.525	v
Oulput voltage	V001	Iout=0A	*Over Temp.	2.450	2.500	2.550	v
Line Regulation	ΔVOUT1	4.75V≦VIN≦7.0V			1.0	6.8	mV
Line Regulation		Iout=0A		-	1.0	0.0	IIIV
Lood Dogulation		VIN=5.0V		-	2.5	25.3	m\/
Load Regulation	tion ΔVOUT2	0A≦Iout≦1.0A	*Over Temp.	-	5.1	30.3	mV
Due new t Malte ne	\ / al:f	ΔVout=±1%		-	1.2	1.4	V
Dropout Voltage	Vdif	0A≦Iout≦1.0A	*Over Temp.	-	1.3	-	v
Current Limit	ILIM	4.75V≦VIN≦7.0V	*Over Temp.	1.0	1.5	-	Α
Supply Current	loo	VIN=5.0V	*Over Temp		6	13	
Supply Current	lss	0A≦Iout≦1.0A	*Over Temp.	-	6	13	mA
Temperature	То	4.75V≦VIN≦7.0V			50		ppm/
Coefficient	Тс	0A≦Iout≦1.0A		-	50	-	°C
Tomporatura Stability	То	VIN=5.0V	*Over Temr		0.5		0/
Temperature Stability	Ts	IOUT=100mA	*Over Temp.	-	0.5	-	%
Ner Temp = Over Temperature $(0 \sim \pm 70^{\circ}\text{C})$							

■ ELECTRICAL CHARACTERISTICS (Continued)

XB1117P331

XB1117P331							TJ =25℃
PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS
Quitaut Voltage	Vout	VIN=5.0V		3.267	3.300	3.333	V
Output Voltage	V001	IOUT=0A	*Over Temp.	3.234	3.300	3.366	V
Line Regulation	Δ Vout1	4.75V≦VIN≦7.0V			1.0	4.5	
	Δ νουτι	IOUT=0A		-	1.0	4.5	mV
Load Regulation	Δ Vout2	VIN=5.0V		-	3.4	10.0	IIIV
	Δ 00012	0A≦Iout≦1.0V	*Over Temp.	-	6.7	13.4	
Dropout Voltage	Vdif	Δ Vout=±1%		-	1.2	1.4	V
Diopout voltage	vuii	0A≦Iout≦1.0A	*Over Temp.	-	1.3	-	v
Current Limit	ILIM	4.75V≦VIN≦7.0V	*Over Temp.	1.0	1.5	-	А
Supply Current	Iss	VIN=5.0V	*Over Temp.	_	6	13	mA
	100	0A≦Iout≦1.0A	over remp.		0	10	ША
Temperature	Тс	4.75V≦VIN≦7.0V		_	50	_	ppm/°C
Coefficient	10	0A≦Iout≦1.0A			50	-	ppin/ C
Temperature Stability	Ts	VIN=5.0V	*Over Temp.	_	0.5	_	%
	13	Iout=100mA	•Over temp.	-	0.0	-	/0

*Over Temp. = Over Temperature $(0 \sim +70^{\circ}C)$

XB1117K12B							TJ=25℃
PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS
Deference veltage	Vref	VIN=5.0V		1.238	1.250	1.262	V
Reference voltage	viei	IOUT=10mA	*Over Temp.	1.225	1.250	1.275	v
Line Regulation	Δ Vout1	4.75V≦VIN≦7.0V Iout=0A		-	0.04	0.20	%
Lood Dogulation		VIN=5.0V		-	0.1	0.3	%
Load Regulation	Δ Vout2	10mA≦Io∪τ≦1.0A	*Over Temp.	-	0.2	0.4	%
Dranautivaltara	\/_l:f	ΔVout=±1%		-	1.2	1.4	V
Dropout voltage	Dropout Voltage Vdif	10mA≦Iouт≦1.0A	*Over Temp.	-	1.3	-	v
Current Limit	I _{LIM}	2.75A≦VIN≦7.0V	*Over Temp.	1.0	1.5	-	А
Temperature Coefficient	Тс	2.75V≦VIN≦7.0V 10mA≦Iout≦1.0A		-	50	-	ppm/ °C
Adjust Din Cument	Lun I	2.75V≦VIN≦7.0V			55	-	
Adjust Pin Current	Iadj	10mA≦Io∪τ≦1.0A	*Over Temp.	-	-	120	μA
Adjust Pin Current Change	Δ Iadj	2.75V≦VIN≦7.0V 10mA≦Iouт≦1.0A	*Over Temp.	-	0.2	5.0	μA
Temperature Stability	Ts	VIN=5.0V Iout=100mA	*Over Temp.	-	0.5	-	%
Minimum Load Current	Ιουτ	Vоит=5.0V		-	-	10	mA

*Over Temp. = Over Temperature ($0 \sim +70^{\circ}$ C)

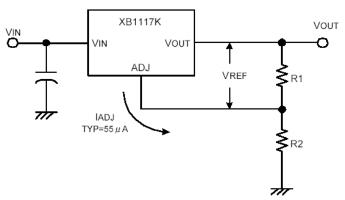
■ OPERATIONAL EXPLANATION

1. Output voltage adjustment

The XB1117 series provide a stable output by comparing the output voltage to an internal reference voltage. With the adjustable XB1117K type, a 1.25V reference voltage (VREF) is fixed between the VOUT pin and the ADJ pin and the external resistors R1 and R2 are used to set the output voltage. The resistance values of R1 and R2 should be set so as to provide a minimum load current of 10mA. The output voltage is given by the following equation.

VOUT=VREF(1+R2/R1)+IADJ x R2

The output voltage of the XB1117P type is internally fixed to 1.8V, 2.5V, 3.3V, and 5.0V so external resistors are not necessary.



2. Stability and load regulation

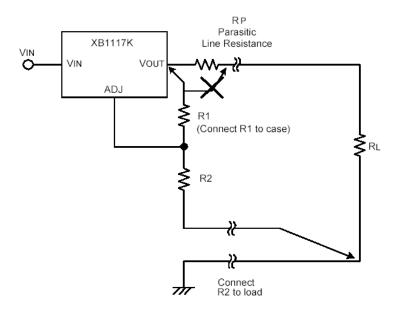
The XB1117 series requires a load capacitor between the VOUT pin and the GND pin to provide phase compensation thereby ensuring stability of the output voltage. Either a tantalum capacitor of more than $10 \,\mu$ F (TYP.) or an aluminum electrolytic capacitor of more than $50 \,\mu$ F (TYP.) should be connected.

(Note : The capacitor's ESR value should not exceed 0.5Ω .)

The output capacitor does not have a theoretical upper limit so increasing its value will increase stability. $CL=100 \mu F$ or more is typical for high current regulator design.

In order to avoid any reductions in output voltage accuracy with the XB1117K type, we recommend not to place a parasitic resistor (Rp) between the Vout pin and the divider resistor R1. The parasitic resistor (Rp) does not influence the output however if the divider resistor R1 is directly connected to the Vout pin.

With the XB1117P type, although external resistor (R1) is internally connected to the Vout pin, stability can be maintained by not wiring a parasitic resistor to the GND pin.



XB1117 Series

■ OPERATIONAL EXPLANATION (Continued)

3. Thermal protection

XB1117 series has thermal protection which limits junction temperature to 150°C. However, device functionally is only guaranteed to a maximum junction temperature of + 125°C. The power dissipation and junction temperature for the XB1117 series are given by;

 $PD=(VIN-VOUT) \times IOUT$ $TJ=TA+(PD \times \partial JA)$ NOTE : TJ must not exceed 125°C.

4. Current limit protection

XB1117 series is protected against overload conditions. Current protection is triggered at 1.5A (TYP.).

5. Thermal consideration

The XB1117 series contain thermal limiting circuitry designed to protect itself from over-temperature conditions. Even for normal load conditions, maximum junction temperature ratings must not be exceeded. As mentioned in thermal protection section, we need to consider all sources of thermal resistance between junction and ambient. It includes junction-to-case, case-to-heat-sink interface and heat sink thermal resistance itself.

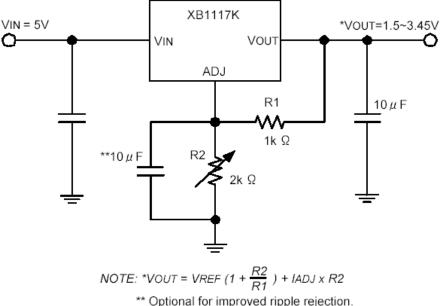
Junction-to-case thermal resistance is specified from the IC junction to the bottom of the case directly below the die. Proper mounting is required to ensure the best possible thermal flow from this area of the package to the heat sink. The case of all devices in this product series is electrically connected to the output. Therefore, if the case of the device is not electrically isolated, a thermally conductive spacer is recommended.

■NOTES ON USE

- 1. For the phenomenon of temporal and transitional voltage decrease or voltage increase, the IC may be damaged or deteriorated if IC is used beyond the absolute MAX. specifications.
- Torex places an importance on improving our products and their reliability.
 We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

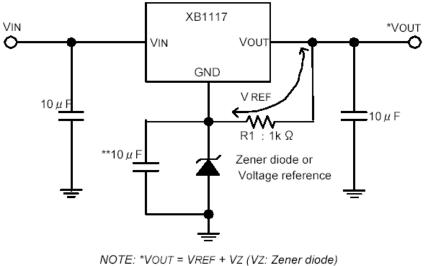
■APPLICATION CIRCUITS

Adjustable Output Voltage



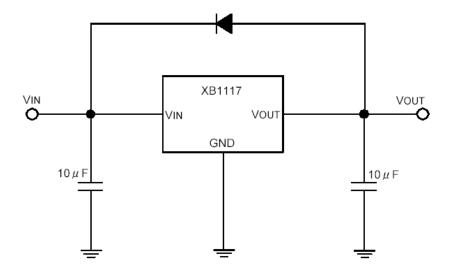
■APPLICATION CIRCUITS (Continued)

Regulator with reference voltage



** Optional for improved ripple rejection.

Regulator with reverse diode protection

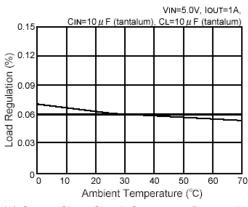


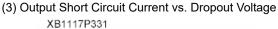
TYPICAL PERFORMANCE CHARACTERISTICS

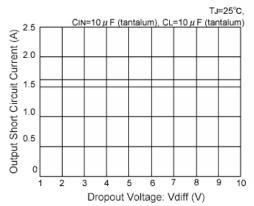
(1) Load Regulation vs. Ambient Temperature

XB1117K12B

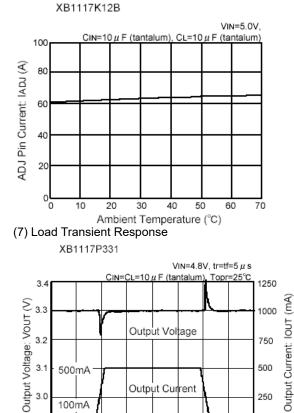
XB1117 Series



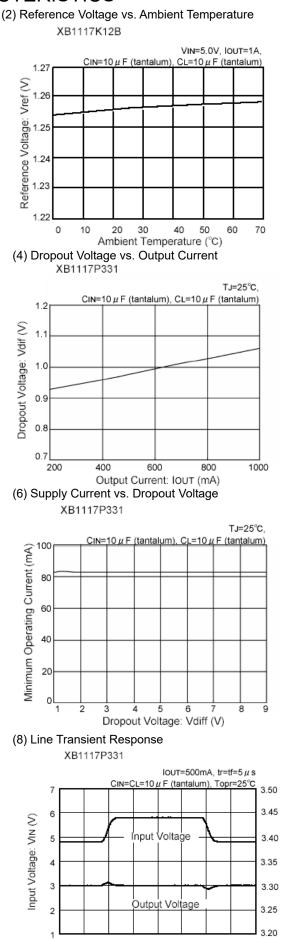




(5) Adjust Pin Current vs. Ambient Temperature



Time (20 µ s/div.)



Time (20 µ s/div.)

Output Voltage: VOUT (V)

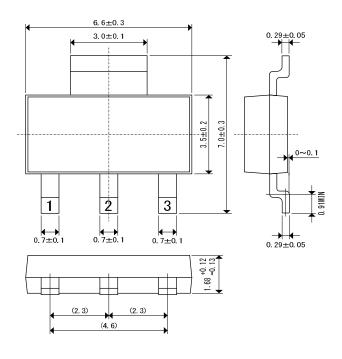
2.9

100mA

0

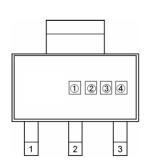
■ PACKAGING INFORMATION

●SOT-223



■MARKING RULE

●SOT-223



SOT-223 (TOP VIEW) 1 represents product series

MARK	PRODUCT SERIES
В	XB1117xxxxFx

② represents fixed or adjustable output voltage

MARK	PRODUCT SERIES
К	XB1117KxxxFx
Р	XB1117PxxxFx

③ represents output voltage

· · ·	•	
MARK	OUTPUT VOLTAGE (V)	PRODUCT SERIES
В	ADJ	XB1117K12BFx
K	1.8	XB1117P181Fx
Т	2.5	XB1117P251Fx
2	3.3	XB1117P331Fx
М	5.0	XB1117P501Fx

④ represents production lot number

0 to 9, A to Z repeated. (G, I, J, O, Q, W are excepted.)

XB1117 series is Discontinued.

XB1117 Series

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