

MOSFETs Silicon N-Channel MOS (DTMOSIV)

# **TK7E80W**

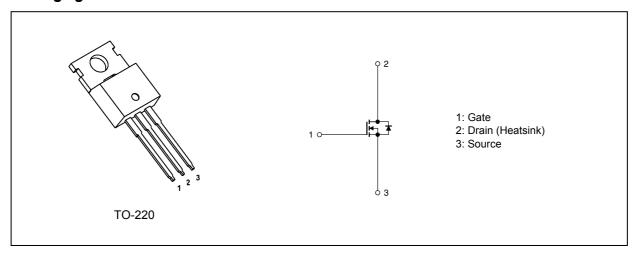
#### 1. Applications

· Switching Voltage Regulators

#### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)}$  = 0.795  $\Omega$  (typ.) by using Super Junction Structure : DTMOS
- (2) Easy to control Gate switching
- (3) Enhancement mode:  $V_{th}$  = 3 to 4 V ( $V_{DS}$  = 10 V,  $I_{D}$  = 0.28 mA)

#### 3. Packaging and Internal Circuit



## 4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	800	V
Gate-source voltage	'	$V_{GSS}$	±20	
Drain current (DC)	(Note 1)	I <sub>D</sub>	6.5	Α
Drain current (pulsed)	(Note 1)	I <sub>DP</sub>	26	
Power dissipation (	T <sub>c</sub> = 25°C)	$P_{D}$	110	W
Single-pulse avalanche energy	(Note 2)	E <sub>AS</sub>	242	mJ
Single-pulse avalanche current		I <sub>AS</sub>	1.3	Α
Reverse drain current (DC)	(Note 1)	I <sub>DR</sub>	6.5	
Reverse drain current (pulsed)	(Note 1)	I <sub>DRP</sub>	26	
Channel temperature	'	T <sub>ch</sub>	150	ů
Storage temperature		T <sub>stg</sub>	-55 to 150	
Mounting torque		TOR	0.6	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production

2016-11



#### 5. Thermal Characteristics

Characteristics		Max	Unit
Channel-to-case thermal resistance		1.13	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	83.3	

Note 1: Ensure that the channel temperature does not exceed 150 °C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25 °C (initial), L = 260 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AS}$  = 1.3 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



#### 6. Electrical Characteristics

#### 6.1. Static Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	800	_	_	V
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.28 mA	3	_	4	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.3 A	_	0.795	0.95	Ω

#### 6.2. Dynamic Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 300 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	700	_	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	1.2	_	
Output capacitance	C <sub>oss</sub>		_	20	_	
Effective output capacitance	C <sub>o(er)</sub>	V <sub>DS</sub> = 640 V, V <sub>GS</sub> = 0 V	_	22	_	
Gate resistance	r <sub>g</sub>	V <sub>DS</sub> = OPEN , f = 1 MHz	_	30	_	Ω
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1	_	18	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	45	_	
Switching time (fall time)	t <sub>f</sub>		_	8	_	
Switching time (turn-off time)	t <sub>off</sub>		_	70	_	
MOSFET dv/dt ruggedness	dv/dt	$V_{DS} \le V_{(BR)DSS}$ , $I_D \le 6.5$ A	50		_	V/ns

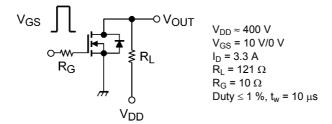


Fig. 6.2.1 Switching Time Test Circuit

## 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	$Q_g$	V <sub>DD</sub> ≈640 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A		13		nC
Gate-source charge 1	Q <sub>gs1</sub>		_	4.5	_	
Gate-drain charge	$Q_{gd}$			4		

#### 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	$V_{DSF}$	I <sub>DR</sub> = 6.5 A, V <sub>GS</sub> = 0 V	1	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	V <sub>DD</sub> ≈640 V		330	_	ns
Reverse recovery charge	$Q_{rr}$	$I_{DR} = 3.3 \text{ A}, V_{GS} = 0 \text{ V}$ - $dI_{DR}/dt = 100 \text{ A/}\mu\text{s}$	_	2.9	_	μС
Peak reverse recovery current	I <sub>rr</sub>	-αι <sub>DR</sub> /αι – 100 Α/μο	1	16	_	Α
Diode dv/dt ruggedness	dv/dt	$V_{DS} \le 640 \text{ V}, I_{DR} = 3.3 \text{ A}, V_{GS} = 0 \text{ V}$	4.5	_	_	V/ns



## 7. Marking

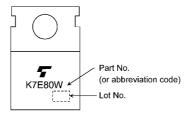


Fig. 7.1 Marking

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#### 8. Characteristics Curves (Note)

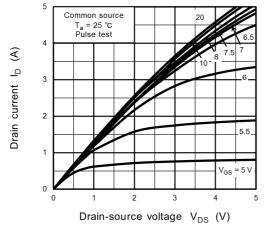


Fig. 8.1 I<sub>D</sub> - V<sub>DS</sub>

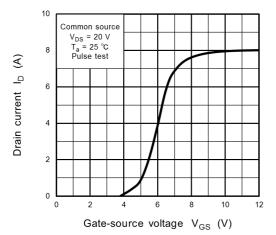


Fig. 8.3  $I_D - V_{GS}$ 

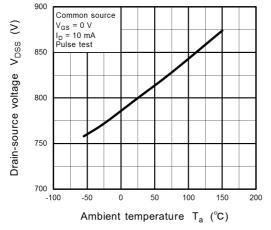


Fig. 8.5 V<sub>DSS</sub> - T<sub>a</sub>

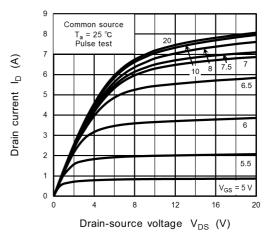


Fig. 8.2 I<sub>D</sub> - V<sub>DS</sub>

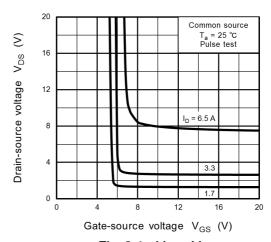


Fig. 8.4 V<sub>DS</sub> - V<sub>GS</sub>

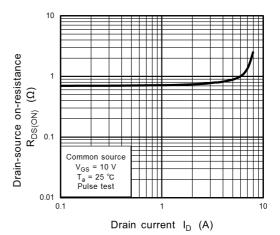


Fig. 8.6 R<sub>DS(ON)</sub> - I<sub>D</sub>

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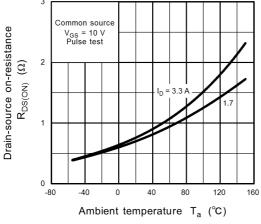


Fig. 8.7 R<sub>DS(ON)</sub> - T<sub>a</sub>

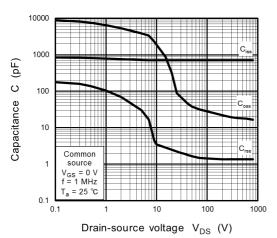


Fig. 8.9 C - V<sub>DS</sub>

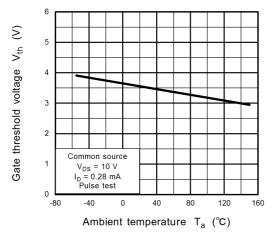


Fig. 8.11 V<sub>th</sub> - T<sub>a</sub>

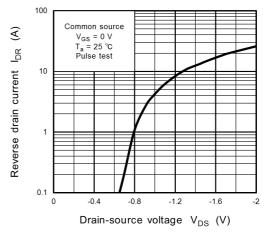


Fig. 8.8 IDR - VDS

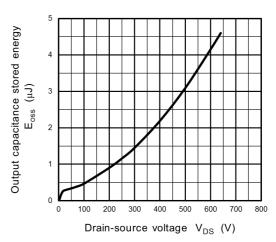


Fig. 8.10 E<sub>oss</sub> - V<sub>DS</sub>

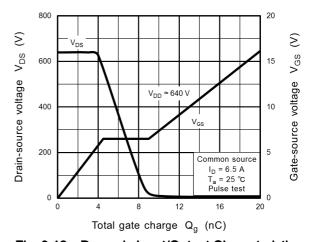


Fig. 8.12 Dynamic Input/Output Characteristics

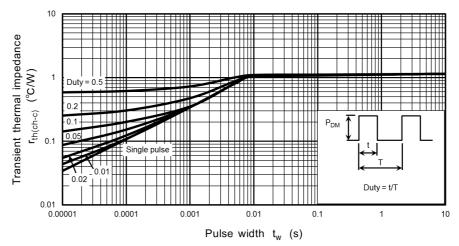


Fig. 8.13 r<sub>th</sub> - t<sub>w</sub> (Guaranteed Maximum)

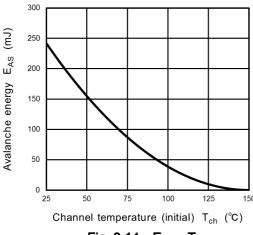


Fig. 8.14 E<sub>AS</sub> - T<sub>ch</sub> (Guaranteed Maximum)

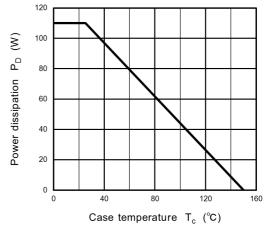
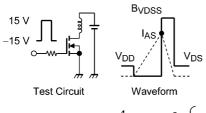


Fig. 8.15 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)



 $R_G = 25 \Omega$ ,  $V_{DD} = 90 V$   $E_{AS} = \frac{1}{2} \cdot L \cdot I_{AS}^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}}\right)$ 

Fig. 8.16 Test Circuit/Waveform

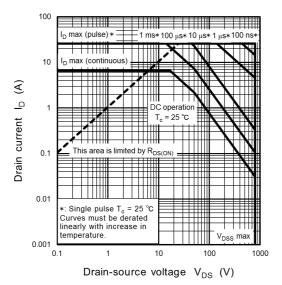


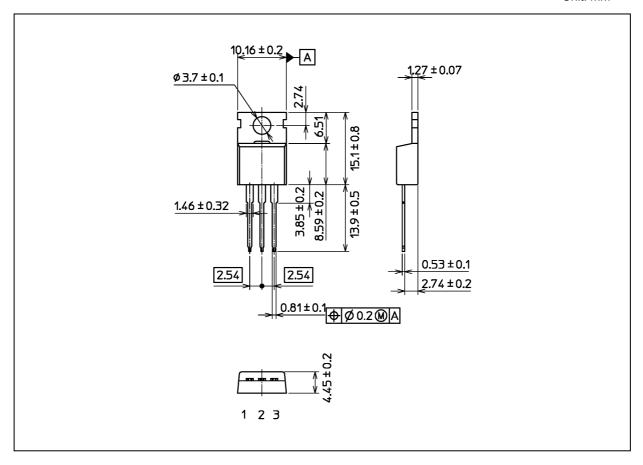
Fig. 8.17 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 1.93 g (typ.)

Package Name(s)
TOSHIBA: 2-10X1A
Nickname: TO-220

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