TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7W14FU, TC7W14FK

Schmitt Inverter

The TC7W14 is high speed C^2MOS Schmitt Inverter fabricated with silicon gate C^2MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the C^2MOS low power dissipation.

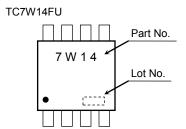
Pin configuration and function are the same as the TC7WU04 but the inputs have 25% VCC hysteresis and with its Schmitt trigger function, the TC7W14 can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

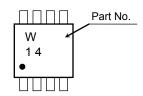
Features

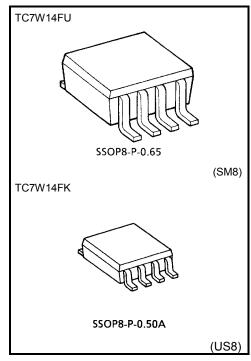
- High speed: t_{pd} = 11 ns (typ.) at V_{CC} = 5 V
- Low power dissipation: $I_{CC} = 1\mu A$ (max) at Ta = 25°C
- High noise immunity: V_H = 1.1 V at V_{CC} = 5V
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4mA (min)
- Balanced propagation delays: t_{pLH} ≃ t_{pHL}
- Wide operating voltage range: V_{CC} (opr) = 2 to 6V

Marking



TC7W14FK





Weight SSOP8-P-0.65: 0.02 g (typ.) SSOP8-P-0.50A: 0.01 g (typ.)

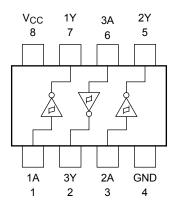
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V_{CC} + 0.5	٧
Input diode current	I _{IK}	±20	mA
Output diode current	I _{OK}	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±25	mA
Douge dissination	D-	300 (SM8)	mW
Power dissipation	P _D	200 (US8)	IIIVV
Storage temperature range	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

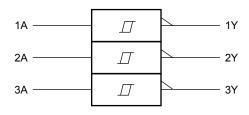
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Pin Configuration (top view)



Logic Diagram



Truth Table

А	Y
L	Н
Н	L

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Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature range	T _{opr}	-40 to 85	°C

Electrical Characteristics

DC Electrical Characteristics

Characteristics Symbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit			
		.,			V _{CC} (V)	Min	Тур.	Max	Min	Max	0
			_		2.0	1.0	1.25	1.5	1.0	1.5	
Threshold voltage Low level	V_P	4.5			2.3	2.7	3.15	2.3	3.15		
		6.0			3.0	3.5	4.2	3.0	4.2	V	
				2.0	0.3	0.65	0.9	0.3	0.9	V	
	V _N	_		4.5	1.13	1.6	2.0	1.13	2.0		
					6.0	1.5	2.3	2.6	1.5	2.6	
						0.3	0.6	1.0	0.3	1.0	
Hysteresis vo	oltage	V _H	V _H	_	4.5	0.6	1.1	1.4	0.6	1.4	V
					6.0	0.8	1.2	1.7	0.8	1.7	
		rel V _{OH}	$V_{IN} = V_{IL}$	I _{OH} = -20 μA	2.0	1.9	2.0		1.9	_	
					4.5	4.4	4.5	_	4.4	_	
	High level				6.0	5.9	6.0	_	5.9	_	
				$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	7
Output				$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	V
voltage Low level	vel V _{OL} V _{IN} =			2.0	_	0	0.1	_	0.1	V	
			$I_{OL} = 20 \ \mu A$	4.5	_	0	0.1	_	0.1		
		$V_{IN} = V_{IH}$	$V_{IN} = V_{IH}$	6.0	_	0	0.1	_	0.1		
				I _{OL} = 4 mA	4.5	_	0.17	0.26	_	0.33	
				I _{OL} = 5.2 mA	6.0		0.18	0.26	_	0.33	
Input leakage	current	I _{IN}	V _{IN} = V _{CC} or GND		6.0		_	±0.1	_	±1.0	μА
Quiescent supply current I _{CC} V _{IN} = V _{CC} or GND		6.0	_	_	1.0	_	10.0	μА			



AC Electrical Characteristics ($C_L = 15 \text{ pF}, V_{CC} = 5 \text{ V}, Ta = 25^{\circ}\text{C}$)

Characteristics	Symbol	Took Condition	-	l lmit		
		Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH} t _{THL}	ı		4	8	ns
Propagation delay time	t _{pLH} t _{pHL}	_	_	11	21	ns

AC Electrical Characteristics ($C_L = 50 \text{ pF}$, input $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
2.13.13.13.10.100			V _{CC} (V)	Min	Тур.	Max	Min	Max	
Output transition time		_	2.0		30	75		95	
	t _{TLH} t _{THL}		4.5		8	15	_	19	ns
	TIFIL		6.0	_	7	13	_	16	
Propagation delay time	t _{pLH} t _{pHL}	_	2.0		42	125		155	
			4.5		14	25		31	ns
			6.0		12	21		26	
Input capacitance	C _{IN}				5	10		10	pF
Power dissipation capacitance	C _{PD}		(Note)	_	28	_	_	_	pF

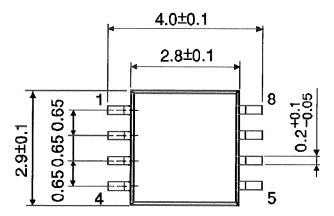
Note: C_{PD} is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load.

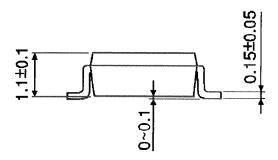
Average operating current can be obtained by the equation hereunder.

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3 \text{ (per gate)}$

Package Dimensions

SSOP8-P-0.65 Unit: mm



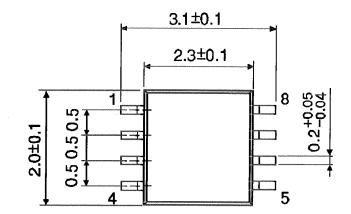


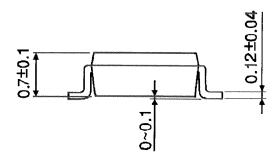
Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A







Weight: 0.01 g (typ.)

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