TOSHIBA Schottky Barrier Diode

CRS05

Secondary Rectification in Switching Regulators Reverse-Current Protection in Mobile Devices

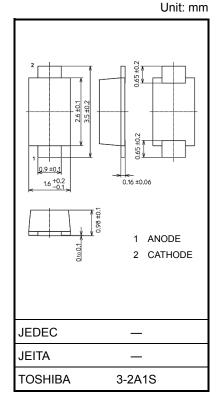
- Repetitive peak reverse voltage : VRRM = 30 V
- Average forward current : IF (AV) = 1.0 A
- Peak forward voltage : $V_{FM} = 0.45 \text{ V} (\text{max}) @I_{FM} = 1.0 \text{ A}$
- Repetitive peak reverse current $: I_{RRM} = 5 \mu A (max) @V_{RRM} = 5 V$
- The use of small, thin surface-mount package is optimum way for high-density mounting. Nickname: S-FLAT[™]

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Repetitive peak reverse voltage	V _{RRM}	30	V	
Average forward current		1.0 (Note 1)	A	
Average lorward current	lf(AV)	1.0 (Note 2)		
Non-repetitive peak forward surge current	IFSM	20 (50 Hz)	А	
Junction temperature	Тј	-40 to 150	°C	
Storage temperature	T _{stg}	Γ _{stg} –40 to 150		

Note 1: $Ta = 102^{\circ}C$ Device mounted on a ceramic board

NOLC 1.	1a = 102 C Device mounted on a certainic board
	board size : 50 mm × 50 mm
	Soldering land size : 2 mm × 2 mm
	board thickness : 0.64 mm
	Rectangular waveform $: \alpha = 180^{\circ}, V_{R} = 15 V$
Note 2:	Ta = 54.7°C Device mounted on a glass-epoxy board
	board size : 50 mm × 50 mm
	Soldering land size : 6 mm × 6 mm
	board thickness : 1.6 mm
	Rectangular waveform $: \alpha = 180^{\circ}, VR = 15 V$
Noto ·	Lising continuously under heavy loads (e.g. the application



Weight: 0.013 g (typ.)

Note : Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

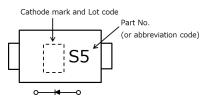
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
	V _{FM (1)}	I _{FM} = 0.1 A (pulse test)	_	0.33	_	v	
Peak forward voltage	VFM (2)	IFM = 0.7 A (pulse test)	_	0.40	_		
	VFM (3)	I _{FM} = 1.0 A (pulse test)	—	0.42	0.45		
Repetitive peak reverse current	I _{RRM (1)}	V _{RRM} = 5 V (pulse test)		2.0	5.0		
	IRRM (2)	V _{RRM} = 30 V (pulse test)		20	200	μA	
Junction capacitance	Cj	V _R = 10 V, f = 1.0 MHz	—	60	_	pF	
Thermal resistance (junction to ambient)	During	$\begin{array}{llllllllllllllllllllllllllllllllllll$	_	_	70	°CAN	
	R _{th (j-a)}	Device mounted on a glass-epoxy board board size 50 mm × 50 mm soldering land size 6 mm × 6 mm board thickness 1.6 mm	_	_	140	°C/W	

Start of commercial production

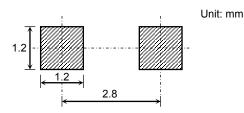
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Marking

Abbreviation Code	Part No.
S5	CRS05



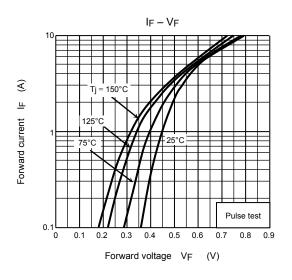
Land pattern dimensions for reference only

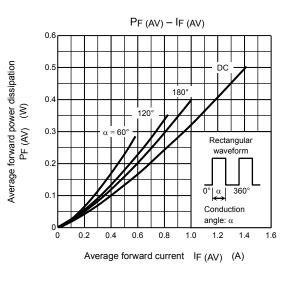


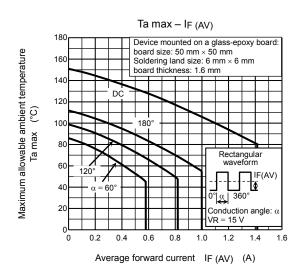
Usage Considerations

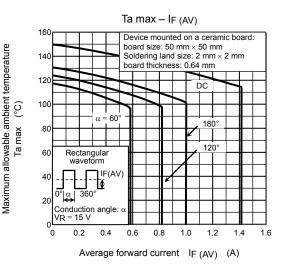
- Schottky barrier diodes have reverse current characteristics compared to other diodes. There is a possibility SBD may cause thermal runaway when it is used under high temperature or high voltage. Please take forward and reverse loss into consideration during design.
- 2) The absolute maximum ratings are rated values that must not be exceeded for a moment to have you use an element safely. Please refer to each following absolute maximum ratings on the occasion of use and design. VRRM: VRRM has a temperature coefficient of 0.1 %/°C.
 - Please take this coefficient into account when designing a circuit board that will be operated in a low-temperature environment.
 - IF(AV): We recommend that the current be in less than 80 % of rating and the junction temperature (Tj) be in less than 80 % of absolute maximum rating under the worst condition.
 This rating is based on the premise that the device is radiating heat enough.
 Therefore, when enough heat radiation is not expected, please consider the margin to the permission curve of Ta(max) IF(AV) for using the device.
 - IFSM: This rating specifies a non-repetitive limit value. This only applies to an abnormal operation, which seldom occurs during the lifespan of a device.
 - T_j : Derate device parameters in proportion to this rating in order to ensure high reliability. We recommend that the junction temperature (T_j) of a device be kept below 80 %.
- 3) Thermal resistance (junction-to-ambient) varies with the mounting conditions of the device on the circuit board. An appropriate thermal resistance value that should be used, must be considering the circuit board design and soldering land size.
- 4) For other design considerations, see the Toshiba website.

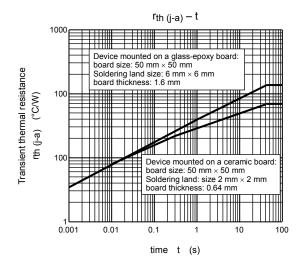
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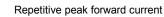


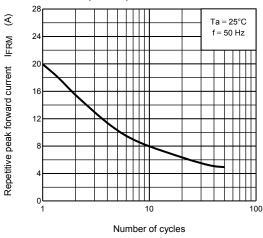




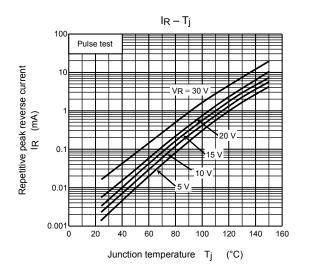


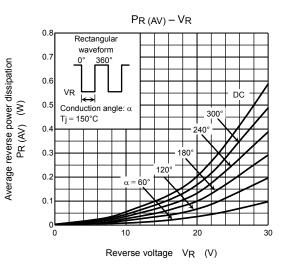


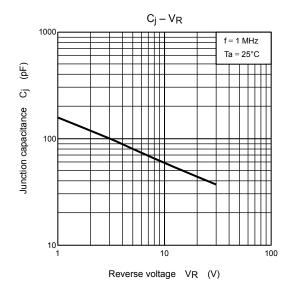




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