TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

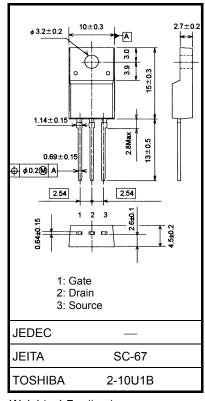
# **TK18A50D**

#### Switching Regulator Applications

- Low drain-source ON resistance:  $RDS(ON) = 0.22 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 8.5 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement-mode:  $V_{th} = 2.0$  to 4.0 V ( $V_{DS} = 10 \text{ V}$ ,  $I_D = 1 \text{ mA}$ )

| Characteristics                               |       |          | Symbol           | Rating     | Unit |  |
|---|-------|----------|------------------|------------|------|--|
| Drain-source voltage                          |       |          | V <sub>DSS</sub> | 500        | V    |  |
| Gate-source voltage                           |       |          | V <sub>GSS</sub> | ±30        | V    |  |
| Drain current                                 | DC    | (Note 1) | I <sub>D</sub>   | 18         | ^    |  |
|   | Pulse | (Note 1) | I <sub>DP</sub>  | 72         | A    |  |
| Drain power dissipation (Tc = $25^{\circ}$ C) |       |          | PD               | 50         | W    |  |
| Single pulse avalanche energy<br>(Note 2)     |       |          | E <sub>AS</sub>  | 533        | mJ   |  |
| Avalanche current                             |       |          | I <sub>AR</sub>  | 18         | A    |  |
| Repetitive avalanche energy (Note 3)          |       |          | E <sub>AR</sub>  | 5.0        | mJ   |  |
| Channel temperature                           |       |          | T <sub>ch</sub>  | 150        | °C   |  |
| Storage temperature range                     |       |          | T <sub>stg</sub> | -55 to 150 | °C   |  |

#### Absolute Maximum Ratings (Ta = 25°C)



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

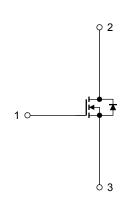
#### **Thermal Characteristics**

| Characteristics                        | Symbol                 | Max  | Unit |  |
|--|------------------------|------|------|--|
| Thermal resistance, channel to case    | R <sub>th (ch-c)</sub> | 2.5  | °C/W |  |
| Thermal resistance, channel to ambient | R <sub>th (ch-a)</sub> | 62.5 | °C/W |  |

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 2.8 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 18 \text{ A}$ Note 3: Repetitive rating: pulse width limited by maximum channel temperature This transistor is an electrostatic sensitive device. Please handle with caution.

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Internal Connection



Start of commercial production 2009-01

Unit: mm

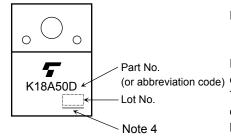
**Electrical Characteristics (Ta = 25°C)** 

| Characteristics              |                | Symbol               | Test Condition  | Min | Тур. | Max  | Unit |
|------------------------------|----------------|----------------------|---|-----|------|------|------|
| Gate leakage current         |                | I <sub>GSS</sub>     | $V_{GS}=\pm 30~V,~V_{DS}=0~V$   | _   |      | ±1   | μA   |
| Drain cut-off curr           | rent           | I <sub>DSS</sub>     | $V_{DS} = 500 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$  | _   |      | 10   | μA   |
| Drain-source bre             | akdown voltage | V (BR) DSS           | $I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$   | 500 |      | _    | V    |
| Gate threshold voltage       |                | V <sub>th</sub>      | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$   | 2.0 |      | 4.0  | V    |
| Drain-source ON resistance   |                | R <sub>DS (ON)</sub> | $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 9 \text{ A}$  | _   | 0.22 | 0.27 | Ω    |
| Forward transfer admittance  |                | Y <sub>fs</sub>      | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 9 \text{ A}$  | 2.4 | 8.5  | _    | S    |
| Input capacitance            |                | C <sub>iss</sub>     |   |     | 2600 | _    |      |
| Reverse transfer capacitance |                | C <sub>rss</sub>     | $V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$  |     | 11   | _    | pF   |
| Output capacitance           |                | C <sub>oss</sub>     |   |     | 280  |      |      |
| Switching time               | Rise time      | tr                   | $10 V$ $I_D = 9 A$ $V_{OUT}$  |     | 50   |      | - ns |
|                              | Turn-on time   | t <sub>on</sub>      | $0 \lor - 1 \lor $ | _   | 100  | _    |      |
|                              | Fall time      | t <sub>f</sub>       | <br>ערט ≈ 200 V   |     | 25   | _    |      |
|                              | Turn-off time  | t <sub>off</sub>     | Duty $\leq$ 1%, t <sub>w</sub> = 10 $\mu$ s   | —   | 150  | _    |      |
| Total gate charge            |                | Qg                   |   | _   | 45   |      |      |
| Gate-source charge           |                | Q <sub>gs</sub>      | $V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 18 \text{ A}$   |     | 28   |      | nC   |
| Gate-drain charge            |                | Q <sub>gd</sub>      | ]   | _   | 17   | —    |      |

## Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics                              | Symbol           | Test Condition                                 | Min | Тур. | Max  | Unit |
|--|------------------|--|-----|------|------|------|
| Continuous drain reverse current<br>(Note 1) | I <sub>DR</sub>  | —  | _   | _    | 18   | А    |
| Pulse drain reverse current (Note 1)         | I <sub>DRP</sub> | _  | _   | _    | 72   | А    |
| Forward voltage (diode)                      | V <sub>DSF</sub> | $I_{DR} = 18 \text{ A}, V_{GS} = 0 \text{ V}$  | _   | _    | -1.7 | V    |
| Reverse recovery time                        | t <sub>rr</sub>  | $I_{DR} = 18 \text{ A}, V_{GS} = 0 \text{ V},$ | _   | 1700 | _    | ns   |
| Reverse recovery charge                      | Qrr              | dI <sub>DR</sub> /dt = 100 A/μs                | _   | 26   | _    | μC   |

### Marking

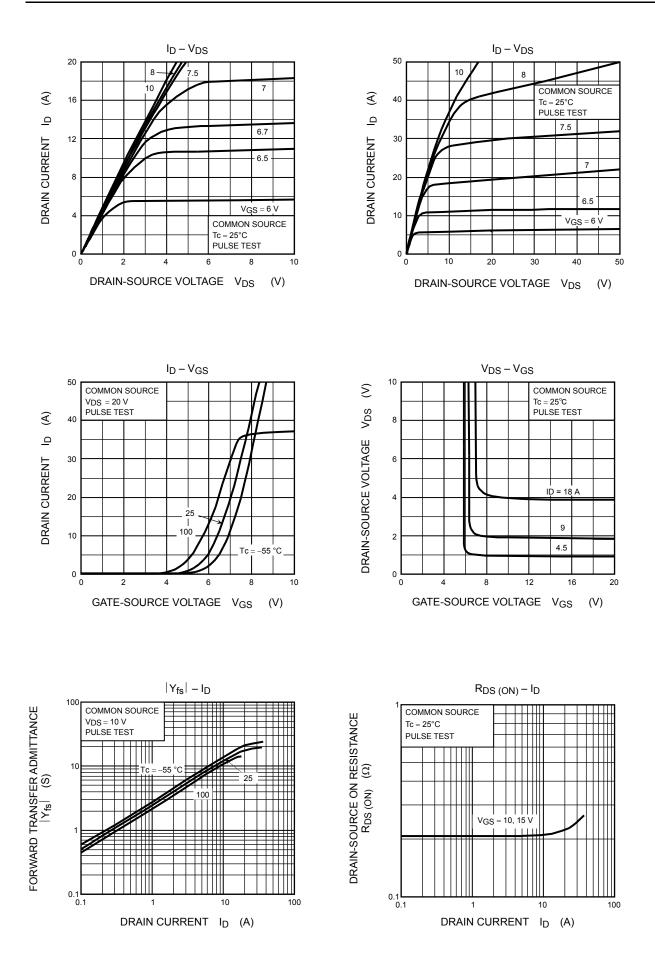


Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Part No.Please contact your TOSHIBA sales representative for details as to<br/>environmental matters such as the RoHS compatibility of Product.Lot No.The RoHS is Directive 2011/65/EU of the European Parliament and<br/>of the Council of 8 June 2011 on the restriction of the use of certain<br/>hazardous substances in electrical and electronic equipment

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-1.5

160

20

16

12

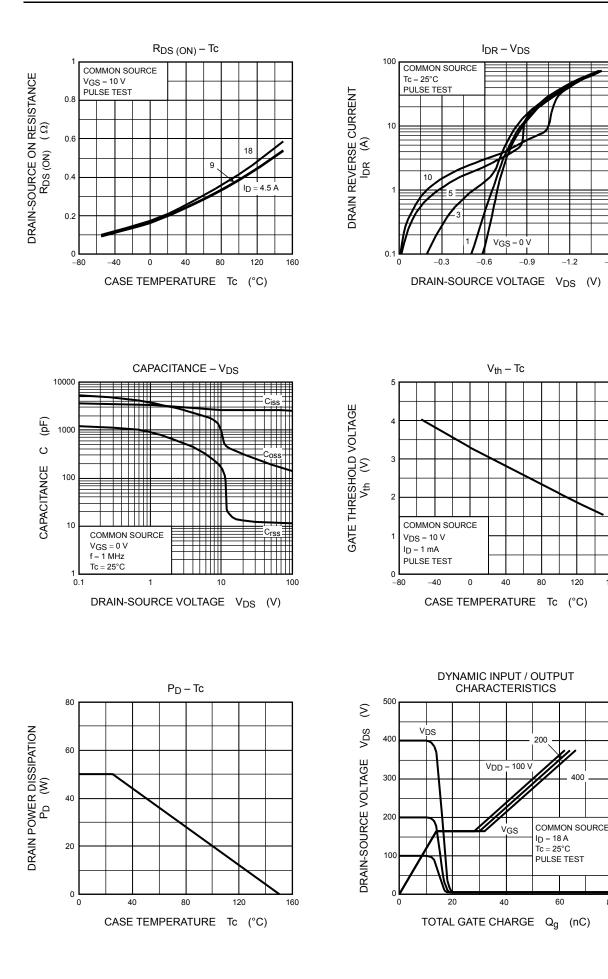
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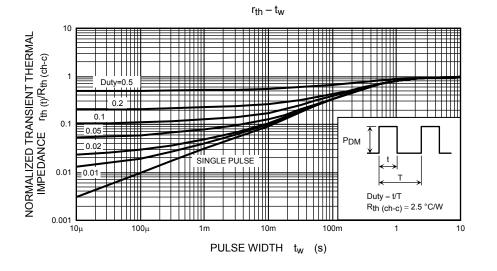
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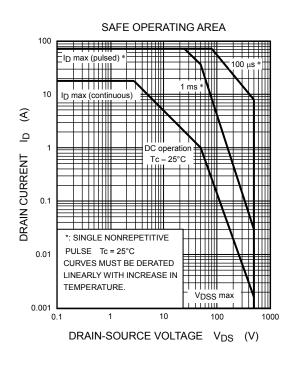
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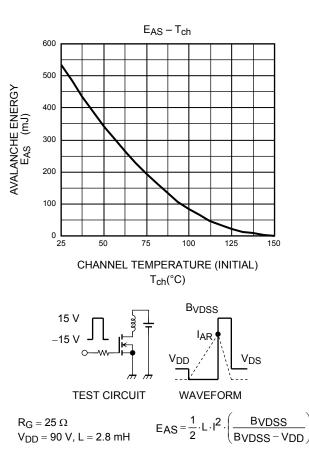
VgS

GATE-SOURCE VOLTAGE









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