

TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

# TCK22xxxG, TCK2065G, TCK1024G

# Load Switch IC with Over current limited function

The TCK22xxxG, TCK2065G and TCK1024G are Load Switch ICs for power management with Over Current Limited function featuring low switch on resistance, ultra low quiescent current, high output current and wide input voltage range. Typical switch ON resistance is only 31 m $\Omega$  at  $V_{\text{IN}}=5.0$  V,  $I_{\text{OUT}}=-0.15$  A load conditions. And these feature a thermal shut down function and output auto-discharge function.

These devices are available in 0.4 mm pitch ultra small package WCSP6E (0.8 mm x 1.2 mm, t: 0.55 mm). So these devices are ideal for portable applications that require high-density board assembly such as cellular phone.

### **Feature**

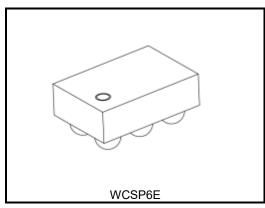
- Over current limit function
   I<sub>CL</sub> = 400/740/1110/1540 mA (Option)
- Thermal shutdown function
- Inrush current reduction
- Output auto-discharge function
- True reverse current blocking function(Option)
- Under voltage lockout function(Option)
- Low ON resistance :

 $R_{ON}$  = 31 m $\Omega$  (typ.) at  $V_{IN}$  = 5.0 V,  $I_{OUT}$  = -0.15 A

 $R_{ON}$  = 40 m $\Omega$  (typ.) at  $V_{IN}$  = 3.3 V,  $I_{OUT}$  = -0.15 A

 $R_{ON} = 70 \text{ m}\Omega$  (typ.) at  $V_{IN} = 1.8 \text{ V}$ ,  $I_{OUT} = -0.15 \text{ A}$ 

- Low quiescent current: IQ = 25 μA (typ.) at V<sub>IN</sub> = 5.5 V, I<sub>OUT</sub> = 0 mA
- Pull down connection between CONTROL and GND
- Ultra small package: WCSP6E (0.8 mm x 1.2 mm, t: 0.55 mm)



Weight: 1 mg (typ.)

Start of commercial production 2016-02

2022-06-09



# **Function Table**

	Function						
Part number	Output current limit	True Reverse current blocking	Output auto- discharge	Under voltage lock out	Thermal shut down	Control pin polarity	Marking
TCK22946G	400 mA	Built in	Built in	Built in	Built in	Active High	1T
TCK22951G	740 mA	Built in	Built in	Built in	Built in	Active High	2T
TCK2065G	1110 mA	Built in	Built in	Built in	Built in	Active High	3T
TCK1024G	1540 mA	Built in	Built in	Built in	Built in	Active High	4T
TCK22891G	400 mA	N/A	Built in	N/A	Built in	Active High	5T
TCK22892G	740 mA	N/A	Built in	N/A	Built in	Active High	6T
TCK22893G	1110 mA	N/A	Built in	N/A	Built in	Active High	7T
TCK22894G	1540 mA	N/A	Built in	N/A	Built in	Active High	8T



# **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Input voltage	VIN	-0.3 to 6.0	V
Control voltage	Vст	-0.3 to 6.0	V
Output voltage	Vout	-0.3 to 6.0	V
Output current	lout	Internally limited	-
Power dissipation	PD	800 (Note 1)	mW
Operating temperature range	T <sub>opr</sub>	−40 to 85	°C
Junction temeperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	−55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Rating at mounting on a board

Board material: Glass epoxy (FR4)

Board dimension: 40mm x 40mm (both sides of board), t=1.6mm

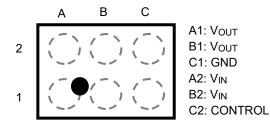
Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

Through hole: diameter 0.5mm x 28

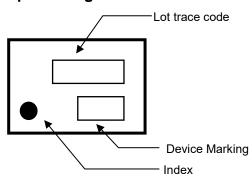
# **Operating conditions**

Characteristics	Symbol	Condition		Min	Max	Unit	
Input voltage	V <sub>IN</sub>	TCK22946G TCK22891G	-	1.1	5.5	V	
		Others		1.4	5.5		
Output voltage	Vout	_		-	VIN	V	
CONTROL High-level input voltage	\/	1.2V < V <sub>IN</sub> ≤ 5.5 V		1.0		V	
CONTROL High-level input voltage	VIH	1.1V ≤V <sub>IN</sub> ≤1.2 V		0.9	_		
CONTROL Low-level input voltage	VIL	_		_	0.4	V	

# Pin Assignment(Top view)



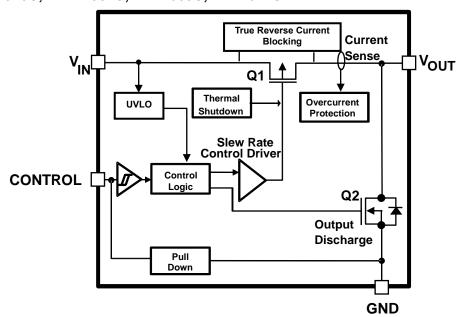
# Top marking



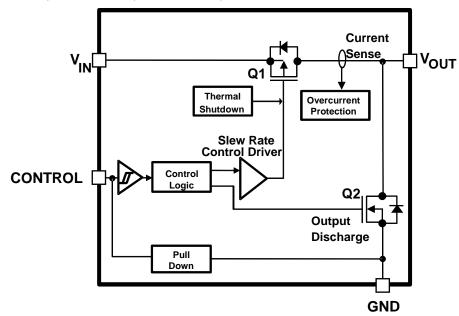


# **Block Diagram**

# TCK22946G, TCK22951G, TCK2065G, TCK1024G



# TCK22891G, TCK22892G, TCK22893G, TCK22894G



# Operation logic table

		TCK22946G, TCK22951G TCK2065G, TCK1024G	TCK22891G, TCK22892G TCK22893G, TCK22894G
0 1 1	Output Q₁	ON	ON
Control "High"	Discharge Q <sub>2</sub>	OFF	OFF
Reverse current blocking		Active	
O a vatural	Output Q₁	OFF	OFF
Control "Low"	Discharge Q <sub>2</sub>	ON	ON
LOW	Reverse current blocking	Active	_



# **Electrical Characteristics**

# DC Characteristics (Ta = -40 to 85°C)

21		Test Condition		Ta = 25°C			Ta = -40 to 85°C		
Characteristics	Symbol			Min	Тур.	Max	Min	Max	Unit
Quiescent current ( ON state)	lo	IOUT = 0 mA	V <sub>IN</sub> = 1.1 V	_	16	_	_	_	μА
Quiescent current ( ON state)	lQ	1001 = 01114	V <sub>IN</sub> = 5.5 V	_	25	_	_	50	μА
Quiescent current ( OFF state)	IQ(OFF)	VIN = 5.5 V, VOL	JT = OPEN,		0.6	_	_	2.5	μА
Reverse blocking current	I <sub>RB</sub>	Vout = 5.0 V, V <sub>IN</sub> = 0 V, RCB	active (Note 2)	_	0.01	_	_	2	μА
Reverse blocking voltage threshold	V <sub>RB</sub>	Vout – Vin	(Note 2)	_	35	_	_	_	mV
Reverse blocking release voltage threshold	VRBR	Vout – Vin	(Note 2)	_	-15	_	_	_	mV
Under Voltage Lock Out (UVLO) rising threshold	V <sub>UVL_RI</sub>	— (Note 2)		_	0.82	_	_	1.1	٧
Under Voltage Lock Out (UVLO) falling threshold	VUVL_FA	_	(Note 2)		0.77	_	_	_	V
			V <sub>IN</sub> = 5.0 V		31	_	_	85	
			V <sub>IN</sub> = 3.3 V		40	_	_	95	
On resistance	Ron	IOUT = -0.15 A	V <sub>IN</sub> = 1.8 V	_	70	_	_	140	$m\Omega$
			V <sub>IN</sub> = 1.2 V (Note 3)	_	141	_	_	_	
			V <sub>IN</sub> = 1.1 V (Note 3)		179	_	_	_	
			TCK22946G TCK22891G	_	400	_	_	_	
	lcL	V <sub>IN</sub> = 5.5 V	TCK22951G TCK22892G	_	740	_	_	_	
Output Limited Current			TCK2065G TCK22893G	_	1110	_	_	_	mA
			TCK1024G TCK22894G	_	1540	_	_	_	
Output discharge on resistance	R <sub>SD</sub>	_	•	_	100	_	_	_	Ω

Note 2: Only applies to the TCK22946G, TCK22951G, TCK2065G and TCK1024G

Note 3: Only applies to the TCK22946G and TCK22891G



# AC Characteristics (Ta = 25°C)

 $V_{IN} = 5.0 V$ 

Characteristics	Symbol	Test Condition(Figure 1)	Min	Тур.	Max	Unit
Vout rise time	tr	$V_{\text{IN}\text{=}}$ 5.0 V , RL = 500 $\Omega$ , CL=0.1 $\mu\text{F},$	_	50	_	μS
Vout fall time	tf	$V_{\text{IN}\text{=}}$ 5.0 V , RL = 500 $\Omega$ , CL=0.1 $\mu\text{F},$	_	50	_	μS
Turn on delay	ton	$V_{\text{IN}\text{=}}$ 5.0 V , RL = 500 $\Omega$ , CL=0.1 $\mu\text{F},$	_	40	_	μS
Turn off delay	tOFF	$V_{\text{IN}}\text{=}~5.0~\text{V}$ , $R_{\text{L}}\text{=}~500~\Omega$ , $C_{\text{L}}\text{=}0.1~\mu\text{F},$	_	10	_	μS

# **AC Waveform**

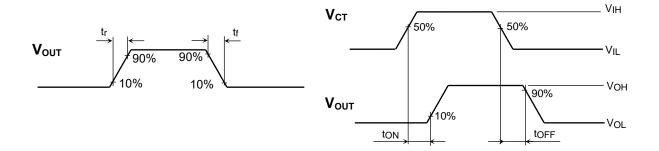


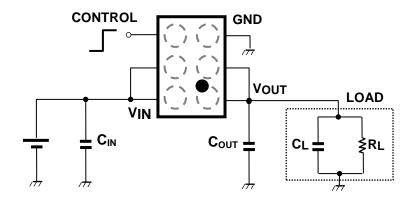
Figure 1 t<sub>r</sub>, t<sub>f</sub>, t<sub>ON</sub>, t<sub>OFF</sub> Waveforms



# **Application Note**

#### 1. Application circuit example (top view)

The figure below shows the example of configuration.



Control Voltage	Output Voltage
HIGH	ON
LOW	OFF
OPEN	OFF

### 1) Input and Output capacitor

An input capacitor (CIN) and an output capacitor (COUT) are necessary for the stable operation. And they are effective to reduce voltage overshoot or undershoot due to sharp changes in output current and also for improved stability of the power supply. When used, place C<sub>IN</sub> and C<sub>OUT</sub> more than 1.0μF as close to V<sub>IN</sub> pin and V<sub>OUT</sub> pin to improve stability of the power supply.

### 2) Control pin

The CONTROL pin controls state of the switch, operated by the control voltage. Control pin is equipped with Schmitt trigger. Also, pull down resistance equivalent to a few M $\Omega$  is connected between CONTROL and GND, thus the load switch IC is in OFF state even when CONTROL pin is OPEN.

## Over current limit function

This device has a built-in fold-back type of current-limiting circuit. Around 15% or more derating against typical values is recommended for system design with enough margin.

# Thermal shutdown function

Each device has a built-in thermal shutdown circuit. If the junction temperature goes beyond 170°C (Typ.), thermal shutdown circuit operates and turns off power switch. When the junction temperature decreases lower than 150°C, the power switch is turned on due to hysteresis. This operation is repeated as long as the junction temperature continues increasing.

# True reverse current blocking function(Option)

Some of these devices have built-in true reverse current blocking circuit (TRCB) to block reverse current from Vout to VIN regardless of output MOSFET ON/OFF condition. (Full-Time Reverse Current Protection)

# **Under-voltage Lockout function(Option)**

Some of these devices have a built-in under-voltage lockout circuit to turn off switch if VIN drops below UVLO. This circuit has hysteresis and UVLO is released when V<sub>IN</sub> exceeds threshold.

### Instructions and directions for use

Each device has several built-in protection functions, but these do not assure for the suppression of uprising device operation. In use of these products, please read through and understand dissipation idea for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommends inserting failsafe system into the design.



# 7. Power Dissipation

Power dissipation is measured on the board condition shown below.

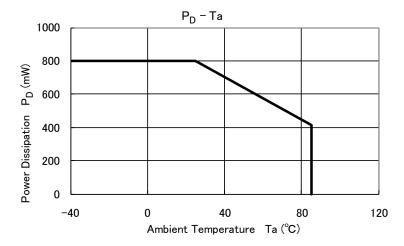
[The Board Condition]

Board material: Glass epoxy (FR4)

Board dimension: 40mm x 40mm (both sides of board), t=1.6mm

Metal pattern ratio: a surface approximately 50%, the reverse side approximately 50%

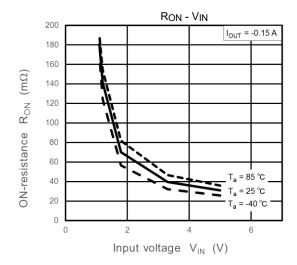
Through hole: diameter 0.5mm x 28

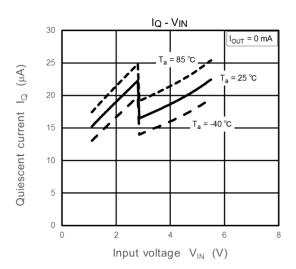


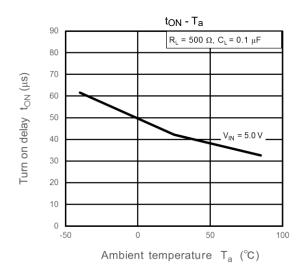
Please allow sufficient margin when designing a board pattern to fit the expected power dissipation. Also take into consideration the ambient temperature, input voltage, output current etc. and applying the appropriate derating for allowable power dissipation during operation.

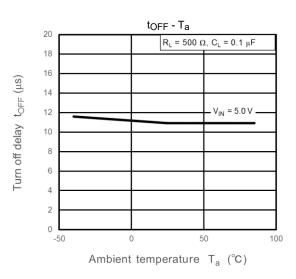


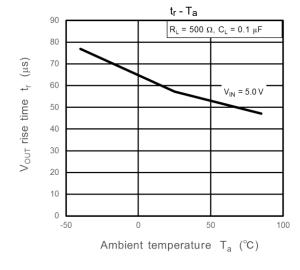
# TCK22xxxG/TCK2065G/TCK1024G Representative Typical Characteristics

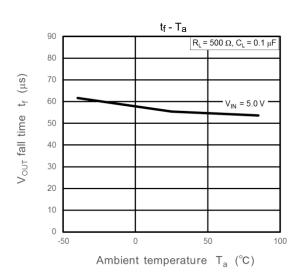




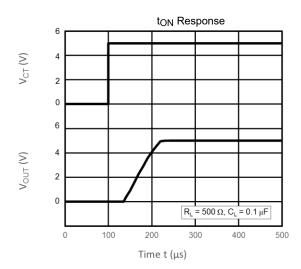


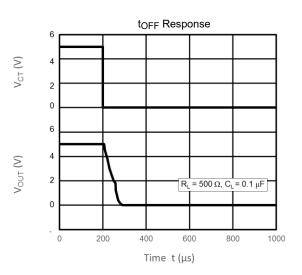


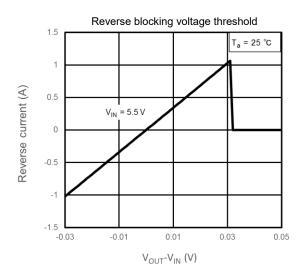


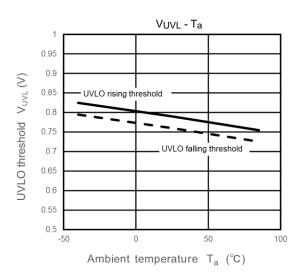


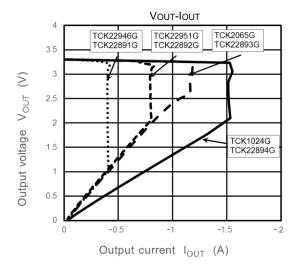










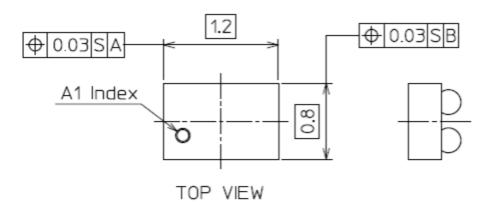


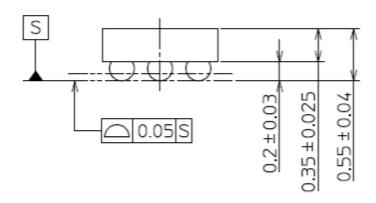
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

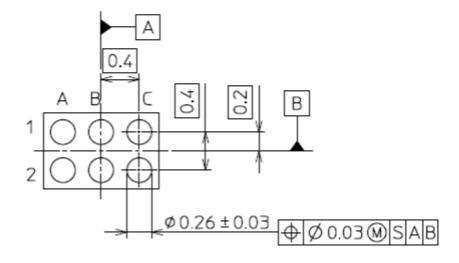


# Package dimension

Unit: mm







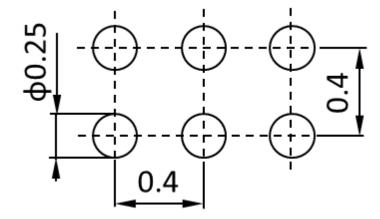
BOTTOM VIEW

Weight: 1 mg (typ.)



Land pattern dimensions (for reference only)

Unit: mm





### RESTRICTIONS ON PRODUCT USE

Toshiba Corporation and its subsidiaries and affiliates are collectively referred to as "TOSHIBA". Hardware, software and systems described in this document are collectively referred to as "Product".

- TOSHIBA reserves the right to make changes to the information in this document and related Product without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE
  EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY
  CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT
  ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation,
  equipment used in nuclear facilities, equipment used in the aerospace industry, lifesaving and/or life supporting medical equipment,
  equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or
  explosions, safety devices, elevators and escalators, and devices related to power plant. IF YOU USE PRODUCT FOR UNINTENDED USE,
  TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your TOSHIBA sales representative or contact us via our
  website.
- · Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any
  applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR
  PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER,
  INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING
  WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2)
  DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR
  INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE,
  ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please
  use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including
  without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT
  OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

# TOSHIBA ELECTRONIC DEVICES & STORAGE CORPORATION

https://toshiba.semicon-storage.com/

# 单击下面可查看定价,库存,交付和生命周期等信息

>>Toshiba(东芝)