TOSHIBA Field Effect Transistor Silicon N Channel Junction Type

# 2SK3320

For Low Noise Audio Amplifier Applications

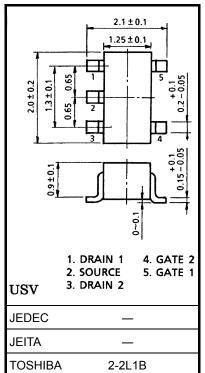
- Two devices in a ultra super mini (five pins) package
- High  $|Y_{fs}|$ :  $|Y_{fs}| = 15 \text{ mS}$  (typ.) (V<sub>DS</sub> = 10 V, V<sub>GS</sub> = 0)
- High breakdown voltage:  $V_{GDS} = -50 V$
- Super low noise: NF = 1.0dB (typ.)

 $(V_{DS} = 10 \text{ V}, \text{ I}_D = 0.5 \text{ mA}, \text{ f} = 1 \text{ kHz}, \text{ R}_G = 1 \text{ k}\Omega)$ 

• High input impedance:  $I_{GSS} = -1 nA (max) (V_{GS} = -30 V)$ 

#### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Gate-drain voltage	V <sub>GDS</sub>	-50	V
Gate current	IG	10	mA
Drain power dissipation	P <sub>D</sub> (Note 1)	200	mW
Junction temperature	Tj	125	°C
Storage temperature range	T <sub>stg</sub>	–55 to 125	°C



Weight: 6.2 mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of

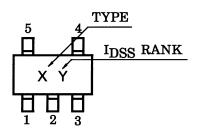
high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating

temperature/current/voltage, etc.) are within the absolute maximum ratings.

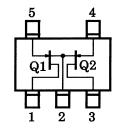
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

#### Marking



### Pin Assignment (top view)



Start of commercial production 1999-01

Unit: mm

## Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

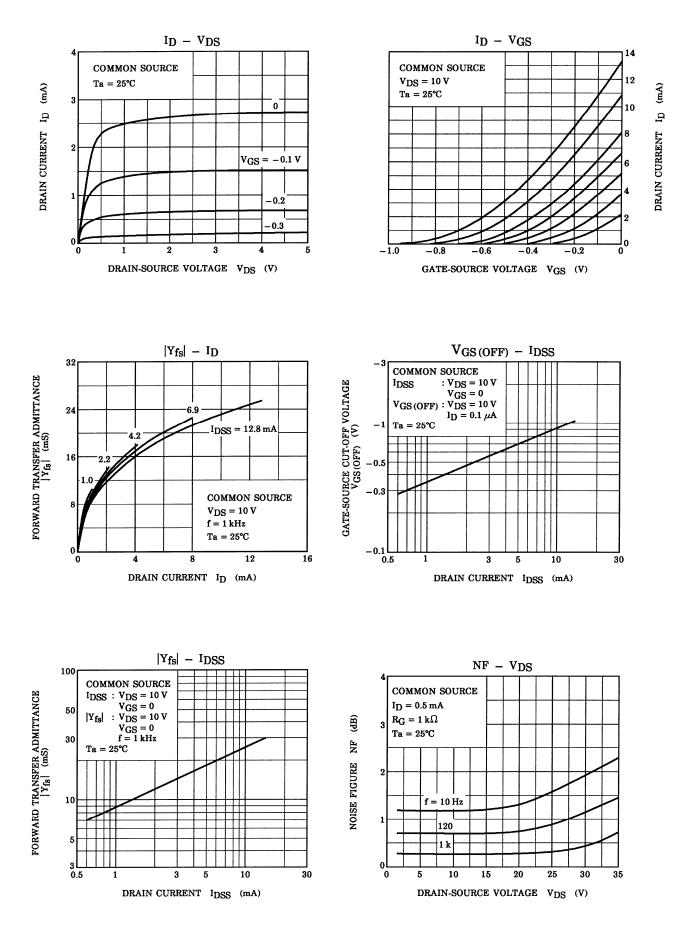
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate cut-off current	I <sub>GSS</sub>	$V_{GS} = -30 V, V_{DS} = 0$	_	_	-1.0	nA
Gate-drain breakdown voltage	V (BR) GDS	$V_{DS} = 0, I_G = -100 \ \mu A$	-50	—	_	V
Drain current	I <sub>DSS</sub> (Note 2)	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0$	1.2	_	14.0	mA
Gate-source cut-off voltage	V <sub>GS (OFF)</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.1 \mu\text{A}$	-0.2	_	-1.5	V
Forward transfer admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ kHz}$	4.0	15	_	mS
Input capacitance	C <sub>iss</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	_	13	_	pF
Reverse transfer capacitance	C <sub>rss</sub>	$V_{DG} = 10 \text{ V}, \text{ I}_{D} = 0, \text{ f} = 1 \text{ MHz}$	_	3	_	pF
Noise figure	NF (1)	$V_{DS}$ = 10 V, $R_G$ = 1 k $\Omega,~I_D$ = 0.5 mA, $f$ = 10 Hz		5		dB
	NF (2)	$V_{DS}$ = 10 V, $R_G$ = 1 k $\Omega,~I_D$ = 0.5 mA, $f$ = 1 kHz	_	1	_	

Note 2: I<sub>DSS</sub> classification Y (Y): 1.2 to 3.0 mA, GR (G): 2.6 to 6.5 mA, BL (L): 6.0 to 14.0 mA

( ).....I<sub>DSS</sub> rank marking

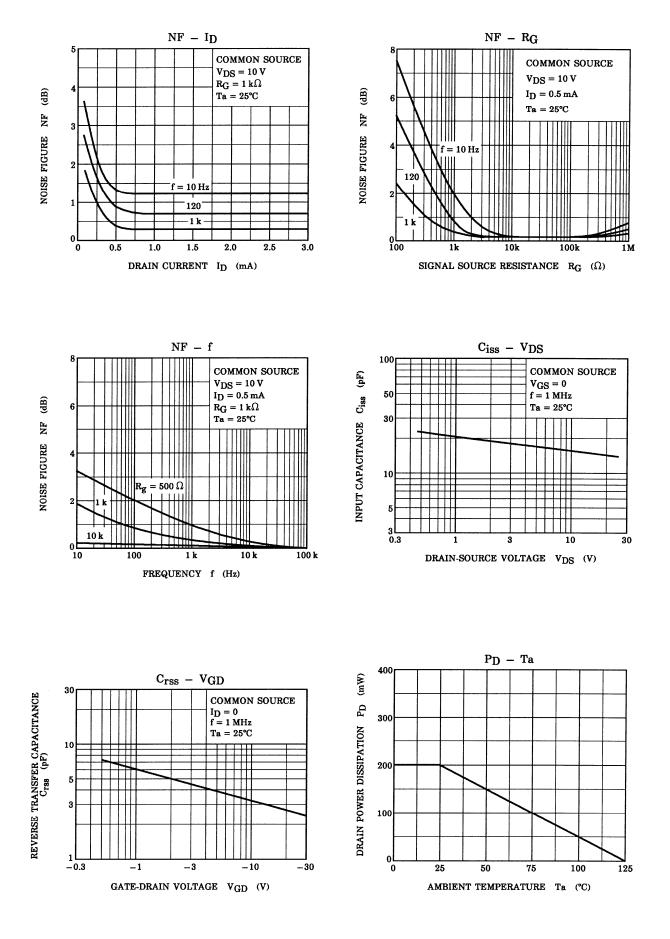
# **TOSHIBA**

### (Q1, Q2 common)



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### (Q1, Q2 common)



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