MOSFETs Silicon N-Channel MOS

# SSM6N7002CFU

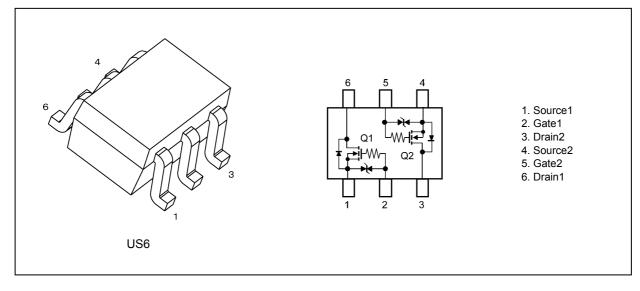
### 1. Applications

High-Speed Switching

#### 2. Features

- (1) Gate-Source diode for protection
- (2) Low drain-source on-resistance
  - $\begin{array}{l} : \mathrm{R}_{\mathrm{DS(ON)}} = 2.8 \ \Omega \ (\mathrm{typ.}) \ (@\mathrm{V}_{\mathrm{GS}} = 10 \ \mathrm{V}, \ \mathrm{I}_{\mathrm{D}} = 100 \ \mathrm{mA}) \\ \mathrm{R}_{\mathrm{DS(ON)}} = 3.1 \ \Omega \ (\mathrm{typ.}) \ (@\mathrm{V}_{\mathrm{GS}} = 5 \ \mathrm{V}, \ \mathrm{I}_{\mathrm{D}} = 100 \ \mathrm{mA}) \\ \mathrm{R}_{\mathrm{DS(ON)}} = 3.2 \ \Omega \ (\mathrm{typ.}) \ (@\mathrm{V}_{\mathrm{GS}} = 4.5 \ \mathrm{V}, \ \mathrm{I}_{\mathrm{D}} = 100 \ \mathrm{mA}) \end{array}$

### 3. Packaging and Pin Assignment



### Absolute Maximum Ratings (Note) (Unless otherwise specified, T<sub>a</sub> = 25 °C) (Q1,Q2 Common)

| Characteristics        |                    | Symbol           | Rating     | Unit |
|------------------------|--------------------|------------------|------------|------|
| Drain-source voltage   |                    | V <sub>DSS</sub> | 60         | V    |
| Gate-source voltage    |                    | V <sub>GSS</sub> | ±20        |      |
| Drain current (DC)     | (Note 1)           | I <sub>D</sub>   | 170        | mA   |
| Drain current (pulsed) | (Note 1), (Note 2) | I <sub>DP</sub>  | 680        |      |
| Power dissipation      | (Note 3)           | PD               | 285        | mW   |
| Channel temperature    |                    | T <sub>ch</sub>  | 150        | °C   |
| Storage temperature    |                    | T <sub>stg</sub> | -55 to 150 | °C   |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Ensure that the channel temperature does not exceed 150 °C.
- Note 2: Pulse width (PW)  $\leq$  10  $\mu s,\,duty \leq$  1%
- Note 3: Device mounted on an FR-4 board.(total dissipation) (25.4 mm  $\times$  25.4 mm  $\times$  1.6 mm ,Cu pad: 645 mm<sup>2</sup>)
- Note: The MOSFETs in this device are sensitive to electrostatic discharge. When handling this device, the worktables, operators, soldering irons and other objects should be protected against anti-static discharge.
- Note: The channel-to-ambient thermal resistance, R<sub>th(ch-a)</sub>, and the drain power dissipation, P<sub>D</sub>, vary according to the board material, board area, board thickness and pad area. When using this device, be sure to take heat dissipation fully into account.

### 5. Electrical Characteristics

### 5.1. Static Characteristics (Unless otherwise specified, Ta = 25 °C)(Q1,Q2 Common)

| Characteristics                |          | Symbol               | Test Condition   | Min | Тур. | Max  | Unit |
|--------------------------------|----------|----------------------|--|-----|------|------|------|
| Gate leakage current           |          | I <sub>GSS</sub>     | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±16 V                             | _   | —    | ±2   | μA   |
|                                |          |                      | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±10 V                             | —   |      | ±0.5 |      |
|                                |          |                      | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±5 V                              | _   | _    | ±0.1 |      |
| Drain cut-off current          |          | I <sub>DSS</sub>     | V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V                              | —   |      | 1    | μA   |
|                                |          |                      | V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V,<br>T <sub>j</sub> = 150 °C  | —   | —    | 200  |      |
| Drain-source breakdown voltage |          | V <sub>(BR)DSS</sub> | I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0 V                             | 60  | _    | _    | V    |
| Gate threshold voltage         | (Note 1) | V <sub>th</sub>      | I <sub>D</sub> = 250 μA, V <sub>DS</sub> = V <sub>GS</sub>                 | 1.1 |      | 2.1  | V    |
| Drain-source on-resistance     | (Note 2) | R <sub>DS(ON)</sub>  | I <sub>D</sub> = 100 mA, V <sub>GS</sub> = 10 V                            | _   | 2.8  | 3.9  | Ω    |
|                                |          |                      | I <sub>D</sub> = 100 mA, V <sub>GS</sub> = 10 V,<br>T <sub>j</sub> = 150 ℃ | —   | 5.4  | 8.1  |      |
|                                |          |                      | I <sub>D</sub> = 100 mA, V <sub>GS</sub> = 5 V                             | _   | 3.1  | 4.4  |      |
|                                |          |                      | I <sub>D</sub> = 100 mA, V <sub>GS</sub> = 4.5 V                           | _   | 3.2  | 4.7  |      |
| Forward transfer admittance    | (Note 2) | Y <sub>fs</sub>      | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 200 mA                            | _   | 450  | _    | mS   |

Note 1: Let V<sub>th</sub> be the voltage applied between gate and source that causes the drain current (I<sub>D</sub>) to below (250  $\mu$ A for this device). Then, for normal switching operation, V<sub>GS(ON)</sub> must be higher than V<sub>th</sub>, and V<sub>GS(OFF)</sub> must be lower than V<sub>th</sub>. This relationship can be expressed as: V<sub>GS(OFF)</sub> < V<sub>th</sub> < V<sub>GS(ON)</sub>. Take this into consideration when using the device.

Note 2: Pulse measurement.

### 5.2. Dynamic Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)(Q1,Q2 Common)

| Characteristics                      | Symbol              | Test Condition                                   | Min | Тур. | Max | Unit |
|--------------------------------------|---------------------|--|-----|------|-----|------|
| Input capacitance                    | C <sub>iss</sub>    | V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V,   | _   | 11   | 17  | pF   |
| Reverse transfer capacitance         | C <sub>rss</sub>    | f = 1 MHz  | -   | 0.7  | _   |      |
| Output capacitance                   | C <sub>oss</sub>    |  | —   | 3    | _   |      |
| Switching time (turn-on delay time)  | t <sub>d(on)</sub>  | V <sub>DD</sub> = 40 V, I <sub>D</sub> = 160 mA, | _   | 2    | 4   | ns   |
| Switching time (rise time)           | t <sub>r</sub>      | $V_{GS}$ = 0 to 10 V, $R_{G}$ = 50 $\Omega$      | _   | 3    | _   |      |
| Switching time (turn-off delay time) | t <sub>d(off)</sub> |  | _   | 7    | 14  |      |
| Switching time (fall time)           | t <sub>f</sub>      |  |     | 24   | _   |      |

### 5.3. Switching Time Test Circuit

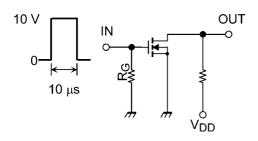


Fig. 5.3.1 Switching Time Test Circuit

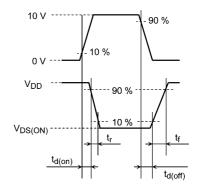


Fig. 5.3.2 Input Waveform/Output Waveform

## 5.4. Gate Charge Characteristics (Unless otherwise specified, $T_a = 25$ °C) (Q1,Q2 Common)

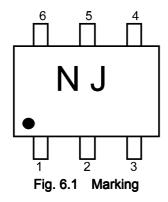
| Characteristics                                 | Symbol          | Test Condition                                   | Min | Тур. | Max  | Unit |
|---|-----------------|--|-----|------|------|------|
| Total gate charge (gate-source plus gate-drain) | Qg              | V <sub>DS</sub> = 30 V, I <sub>D</sub> = 200 mA, | _   | 0.27 | 0.35 | nC   |
| Gate-source charge                              | Q <sub>gs</sub> | V <sub>GS</sub> = 4.5 V                          | _   | 0.08 | —    |      |
| Gate-drain charge                               | Q <sub>gd</sub> |  | _   | 0.08 | _    |      |

## 5.5. Source-Drain Characteristics (Unless otherwise specified, $T_a = 25$ °C) (Q1,Q2 Common)

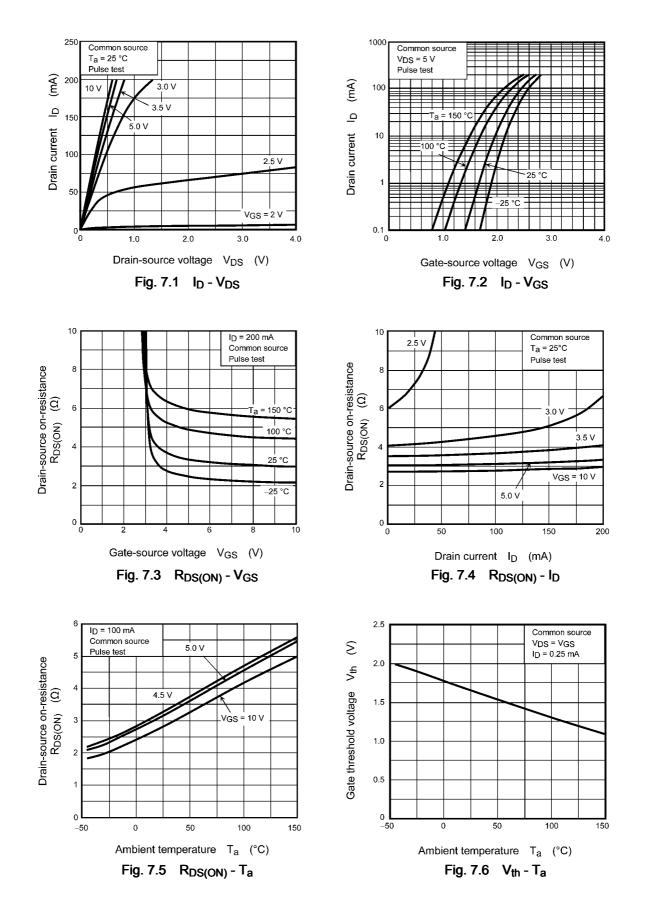
| Characteristics       |          | Symbol           | Test Condition                                  | Min | Тур.  | Max  | Unit |
|-----------------------|----------|------------------|---|-----|-------|------|------|
| Diode forward voltage | (Note 1) | $V_{\text{DSF}}$ | I <sub>D</sub> = -115 mA, V <sub>GS</sub> = 0 V | _   | -0.87 | -1.2 | V    |

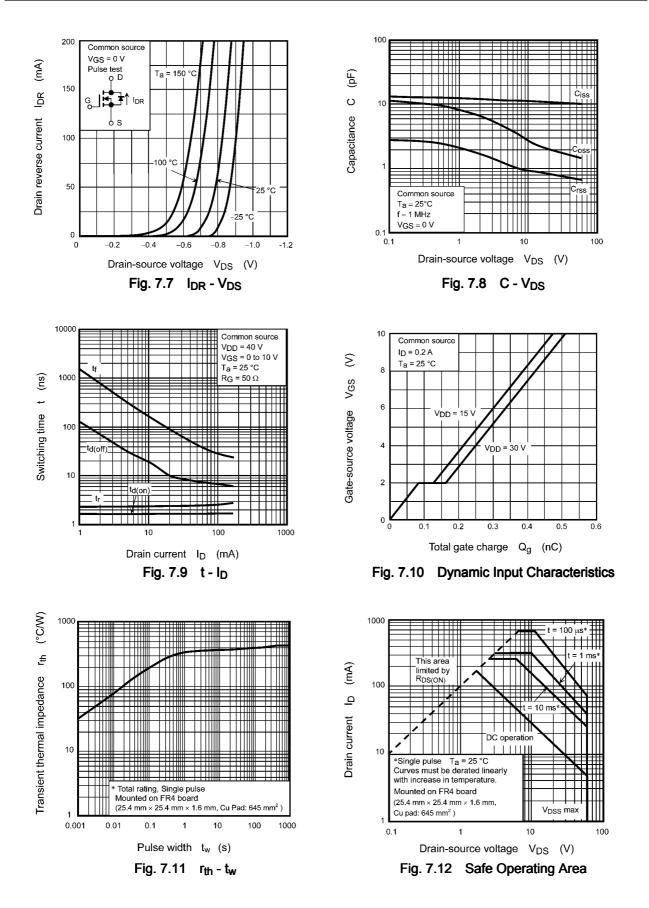
Note 1: Pulse measurement.

### 6. Marking



### 7. Characteristics Curves (Q1,Q2 Common) (Note)

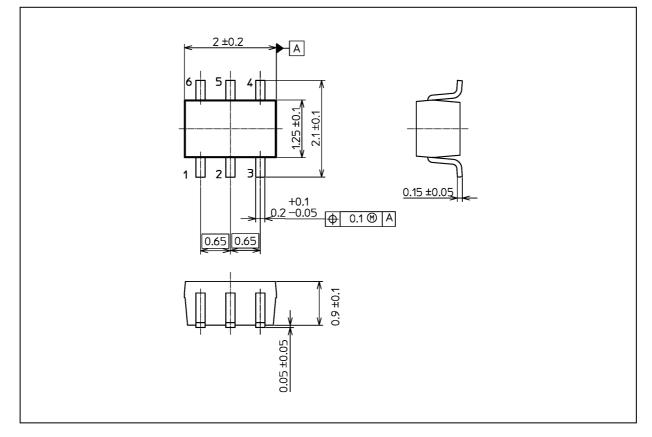




Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### Package Dimensions

Unit: mm



#### Weight: 6.8 mg (typ.)

|               | Package Name(s) |  |
|---------------|-----------------|--|
| Nickname: US6 |                 |  |

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