TOSHIBA Photocoupler

IRED & Photo-Diode Array

# TLP590B

Telecommunications
Programmable Controllers
MOS Gate Drivers
MOSFET Gate Drivers

The TOSHIBA TLP590B consists of an infrared emitting diode optically coupled to a series-connected photo-diode array in a six-lead plastic DIP package.

The TLP590B is suitable for MOSFET gate drivers.

• UL-recognized: UL 1577, File No.E67349

### **Short Current**

Type Name	Classification	Short (	Current	Classification		
Name	Classification	(min)	lF	Marking		
TLP590B	C20	20 μΑ	10 mA	20		
ILP590B	Standard	12 µA	IUIIIA	20, blank		

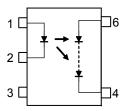
Note: When applying for a safety standard approval, use the type name of the standard device.

TLP590B(C20): TLP590B

# Unit: mm 6 4 7.12±0.25 7.12±0.25 7.85 to 8.80 TOSHIBA 11-7A9S

Weight: 0.39 g (typ.)

### **Pin Configuration (Top View)**



- 1: Anode(LED)
- 2: Cathode(LED)
- 3: N.C.
- 4: Cathode
- 6: Anode

Start of commercial production 1989-02

### **Absolute Maximum Ratings (Ta = 25°C)**

	Characteristics	Symbol	Rating	Unit
	Forward current	lF	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔIF/°C	-0.5	mA / °C
	Pulse forward current (100 µs pulse, 100 pps)	IFP	1	А
LED	Reverse voltage	VR	3	<b>V</b>
	Diode power dissipation	PD	100	mW
	Diode power dissipation derating (Ta ≥ 25°C)	ΔP <sub>D</sub> /°C	-1.0	mW/°C
	Junction temperature	Tj	125	°C
	Forward current	lFD	50	μA
Detector	Reverse voltage	VRD	10	٧
Dete	Output power dissipation	Po	0.5	mW
	Junction temperature	Tj	125	°C
Sto	rage temperature range	T <sub>stg</sub>	-55 to 125	°C
Оре	erating temperature range	Topr	-40 to 85	°C
Lea (10	d soldering temperature s)	T <sub>sol</sub>	260	°C
	ation voltage , 60 s, R.H. ≤ 60 %) (Note 1)	BVs	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal device: Pins 1, 2 and 3 shorted together, and pins 4 and 6 shorted together.

### **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Forward current	lF	_	20	25	mA
Operating temperature	T <sub>opr</sub>	-25	-	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



## **Electrical Characteristics (Ta = 25°C)**

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10 mA	1.2	1.4	1.7	٧
LED	Reverse current	IR	V <sub>R</sub> = 3 V	_	_	10	μA
	Capacitance	Ст	V = 0V, f = 1 MHz	_	30	60	pF
ctor	Forward voltage	V <sub>FD</sub>	I <sub>FD</sub> = 10 μA	_	7	_	V
Detector	Reverse current	I <sub>RD</sub>	V <sub>RD</sub> = 10 V	_	1	_	nA

# **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Open voltage	Voc	I <sub>F</sub> = 10 mA	7.0	8.0	_	V
Short current	Isc	IF = 10 mA	12	20	_	μA

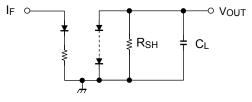
# Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	8.0	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5×10 <sup>10</sup>	10 <sup>14</sup>	-	Ω
Isolation voltage	BVS	AC, 60 s	2500		_	Vrms

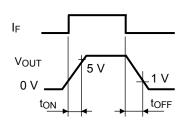
# **Switching Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	ton	I <sub>F</sub> = 20 mA, R <sub>SH</sub> = 510 kΩ	_	0.2	_	ms
Turn-off time	toff	C <sub>L</sub> =1000 pF (Note 2)	-	1	_	ms

Note 2: Switching time test circuit

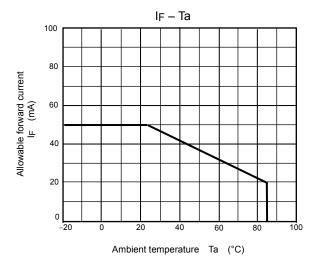


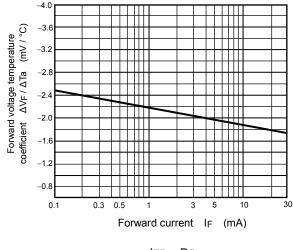
R<sub>SH</sub>: External shunt resistance



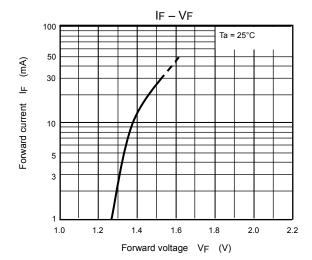
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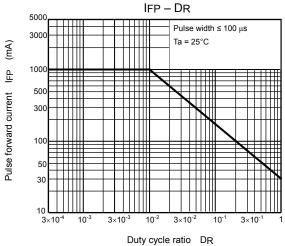
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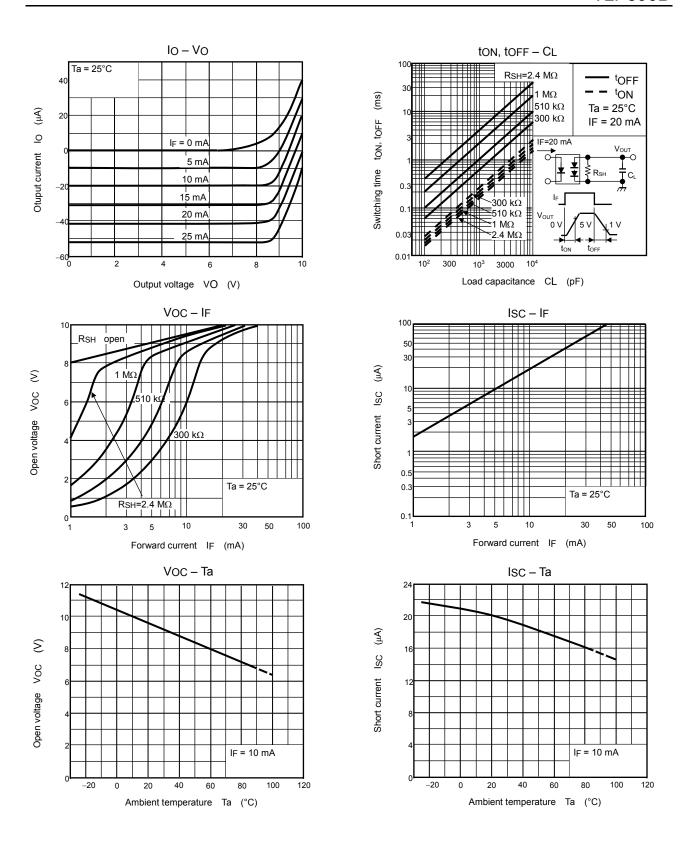
ΔVF / ΔTa - IF





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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