

CMOS Digital Integrated Circuits Silicon Monolithic

TC7WZU04FU

1. Functional Description

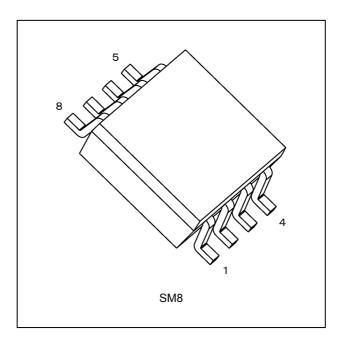
• Triple Inverter (Unbuffer)

2. Features

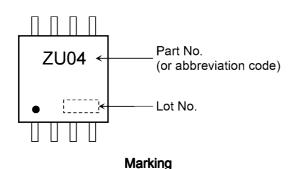
- (1) Wide operating temperature range: $T_{opr} = -40$ to 125 °C (Note 1)
- (2) High output current: ± 16 mA (min) at $V_{CC} = 4.5$ V
- (3) Low power dissipation: $I_{CC} = 1 \mu A \text{ (max) (V}_{CC} = 5.5 \text{ V}, T_a = 25 \text{ °C})$
- (4) Operating supply voltage range: $V_{CC} = 1.65 \text{ V}$ to 5.5 V
- (5) 5.5 V tolerant inputs

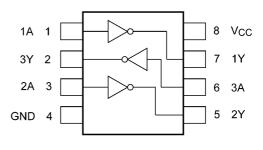
Note 1: For devices with the ordering part number ending in J(CT. T_{opr} = -40 to 85 °C for the other devices.

3. Packaging



4. Marking and Pin Assignment





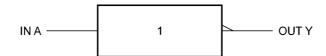
Pin Assignment (Top view)

Start of commercial production

2020-01



5. IEC Logic Symbol



6. Truth Table

A	Y
L	Н
Н	L

7. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V_{CC}		-0.5 to 6.0	V
Input voltage	V _{IN}		-0.5 to 6.0	V
DC output voltage	V _{OUT}		-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}		-20	mA
Output diode current	I _{OK}	(Note 1)	±20	mA
DC output current	l _{out}		±50	mA
V _{CC} /ground current	I _{CC}		±50	mA
Power dissipation	P _D		300	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

8. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V _{CC}		_	1.65 to 5.5	V
		(Note 1)	_	1.5 to 5.5	
Input voltage	V _{IN}		_	0 to 5.5	V
Output voltage	V _{OUT}		_	0 to V _{CC}	V
Operating temperature	T _{opr}	(Note 2)	_	-40 to 125	°C
		(Note 3)	_	-40 to 85	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 1: Data retention only

Note 2: For devices with the ordering part number ending in J(CT.

Note 3: For devices except those with the ordering part number ending in J(CT.

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9. Electrical Characteristics

9.1. DC Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Unit
High-level input voltage	V _{IH}	_		1.65 to 2.7	$V_{CC} \times 0.85$	_	_	V
				3.0 to 5.5	V _{CC} × 0.80	_	_	
Low-level input voltage	V _{IL}	_		1.65 to 2.7	_	_	V _{CC} × 0.15	V
				3.0 to 5.5	_	_	$V_{CC} \times 0.20$	
High-level output voltage	V _{OH}	$V_{IN} = V_{IL}$	I _{OH} = -100 μA	1.65	1.45	1.64	_	٧
				2.3	2.1	2.29	_	
				3.0	2.7	2.99	_	
				4.5	4.0	4.48	_	
		V _{IN} = GND	I _{OH} = -2 mA	1.65	1.29	1.52	_	
			I _{OH} = -4 mA	2.3	1.9	2.19	_	
			I _{OH} = -8 mA	3.0	2.4	2.82	_	
			I _{OH} = -12 mA	3.0	2.3	2.73	_	
			I _{OH} = -16 mA	4.5	3.8	4.24	_	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 100 μA	1.65	_	0.01	0.2	٧
				2.3		0.01	0.2	
				3.0		0.01	0.3	
				4.5	_	0.01	0.5	
		V _{IN} = V _{CC}	I _{OL} = 2 mA	1.65	_	0.08	0.24	
			I _{OL} = 4 mA	2.3		0.12	0.3	
			I _{OL} = 8 mA	3.0	_	0.19	0.4	
			I _{OL} = 12 mA	3.0		0.29	0.55	
			I _{OL} = 16 mA	4.5	_	0.29	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±1	μА
Quiescent supply current	I _{CC}	V _{IN} = 5.5 V or GND		1.65 to 5.5	_	_	1	μΑ



9.2. DC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_	1.65 to 2.7	$V_{CC} \times 0.85$	_	V	
				3.0 to 5.5	$V_{CC} \times 0.80$	_	
Low-level input voltage	V _{IL}	_		1.65 to 2.7	_	V _{CC} × 0.15	V
				3.0 to 5.5	_	V _{CC} × 0.20	
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -100 μA	1.65	1.45	_	V
				2.3	2.1	_	
				3.0	2.7	_	
				4.5	4.0	_	
		V _{IN} = GND	I _{OH} = -2 mA	1.65	1.29	_	
			I _{OH} = -4 mA	2.3	1.9	_	
			I _{OH} = -8 mA	3.0	2.4	_	
			I _{OH} = -12 mA		2.3	_	
			I _{OH} = -16 mA	4.5	3.8	_	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 100 μA	1.65	_	0.2	V
				2.3	_	0.2	
				3.0	_	0.3	
				4.5	_	0.5	
		V _{IN} = V _{CC}	I _{OL} = 2 mA	1.65	_	0.24	
			I _{OL} = 4 mA	2.3	_	0.3	
			I _{OL} = 8 mA	3.0	_	0.4	
			I _{OL} = 12 mA	3.0	_	0.55	
			I _{OL} = 16 mA	4.5	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	±10	μА
Quiescent supply current	Icc	V _{IN} = 5.5 V or GND		1.65 to 5.5	_	10	μА



9.3. DC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_	1.65 to 2.7	$V_{CC} \times 0.85$	_	V	
				3.0 to 5.5	$V_{CC} \times 0.80$	_	
Low-level input voltage	V _{IL}	_		1.65 to 2.7	_	V _{CC} × 0.15	V
				3.0 to 5.5	_	V _{CC} × 0.20	
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -100 μA	1.65	1.45	_	V
				2.3	2.1	_	
				3.0	2.7	_	
				4.5	4.0	_	
		V _{IN} = GND	I _{OH} = -2 mA	1.65	0.95	_	
			I _{OH} = -4 mA	2.3	1.7	_	
			I _{OH} = -8 mA	3.0	2.2	_	
			I _{OH} = -12 mA	3.0	2.0	_	
			I _{OH} = -16 mA	4.5	3.4	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 100 μA	1.65	_	0.2	V
				2.3	_	0.2	
				3.0	_	0.3	
				4.5	_	0.5	
		V _{IN} = V _{CC}	I _{OL} = 2 mA	1.65	_	0.7	
			I _{OL} = 4 mA	2.3	_	0.45	
			I _{OL} = 8 mA	3.0	_	0.6	
			I _{OL} = 12 mA	3.0	_	0.8	
			I _{OL} = 16 mA	4.5	_	0.8	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND	•	0 to 5.5	_	±20	μΑ
Quiescent supply current	Icc	V _{IN} = 5.5 V or GND		1.65 to 5.5	_	100	μА

Note: For devices with the ordering part number ending in J(CT.



9.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Unit				
Propagation delay time	t _{PLH} ,t _{PHL}		$R_L = 1 M\Omega$	1.8 ± 0.15	15	1.5	4.6	8.1	ns				
				2.5 ± 0.2		1.2	3.3	5.7					
				3.3 ± 0.3		0.8	2.7	4.1					
								5.0 ± 0.5		0.5	2.2	3.3	
			R _L = 500 Ω	3.3 ± 0.3	50	1.2	4.0	6.4	ns				
				5.0 ± 0.5		0.8	3.4	5.6					
Input capacitance	C _{IN}		_	0 to 5.5	_	_	5.4	_	pF				
Power dissipation	C _{PD}	(Note 1)	_	3.3	_	_	9.8	_	pF				
capacitance				5.5			22						

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$ (per 1 gate)

9.5. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	1.8 ± 0.15	15	1.5	8.9	ns
			2.5 ± 0.2		1.2	6.3	
			3.3 ± 0.3		0.8	4.5	
			5.0 ± 0.5		0.5	3.6	
		R_L = 500 Ω	3.3 ± 0.3	50	1.2	7.0	ns
			5.0 ± 0.5		0.8	6.2	

9.6. AC Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to 125 °C, Input: $t_f = t_f = 3$ ns)

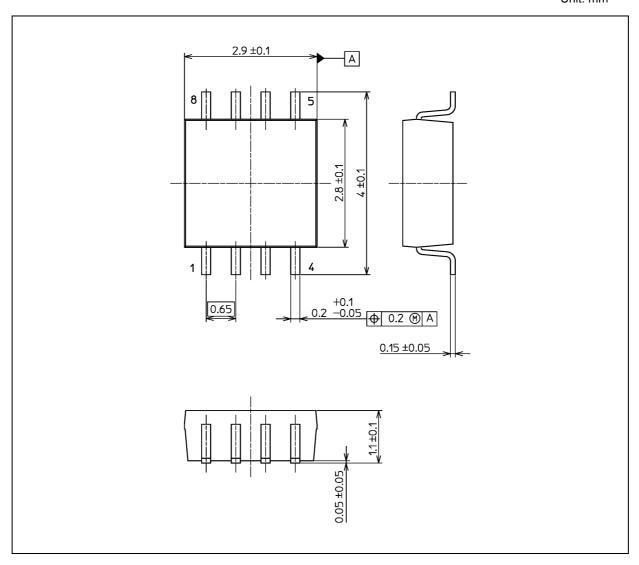
Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	1.8 ± 0.15	15	1.5	10.0	ns
			2.5 ± 0.2		1.2	7.0	
			3.3 ± 0.3		0.8	5.0	
			5.0 ± 0.5		0.5	4.0	
		R _L = 500 Ω	3.3 ± 0.3	50	1.2	8.0	ns
			5.0 ± 0.5		0.8	7.0	

Note: For devices with the ordering part number ending in J(CT.



Package Dimensions

Unit: mm



Weight: 21 mg (typ.)

	Package Name(s)	
JEDEC: SOT-505		
Nickname: SM8		



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8

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