Unit: mm

TOSHIBA Diode Silicon Epitaxial Planar Type

HN1D04FU

Ultra High Speed Switching Application

Low forward voltage : V_{F(3)} = 0.90V (typ.)
 Fast reverse recovery time : t_{rr} = 1.6ns (typ.)
 Small total capacitance : C_T = 0.9pF (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	V_{RM}	85	V (
Reverse voltage	V _R	80	M
Maximum (peak) forward current	I _{FM}	300*	mA
Average forward current	Io	100*	(mA)
Surge current (10ms)	I _{FSM}	2*	A
Power dissipation	Р	200**	mW
Junction temperature	Tj	150	> °C
Storage temperature	T _{stg}	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

2.1±0.1
1.25±0.1
1.25±0.1
1.25±0.1
1.25±0.1
1.25±0.1
1.25±0.1
2.CATHODE1
2.CATHODE2
3.ANODE4
CATHODE3
4.ANODE3
5.CATHODE4
6.CATHODE1
ANODE2

JEDEC

JEDEC

JEITA

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TOSHIBA
1-2T1F

Weight: 6.8 g (typ.)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

*: Where Q1 and Q2 or Q3 and Q4 are used independently or simultaneously, the Absolute Maximum Ratings per diode are 50% of those of the single diode.

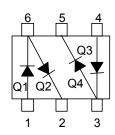
Electrical Characteristics (Q1, Q2, Q3, Q4 Common; Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
\wedge (())	V _F (1)	_	I _F = 1mA	1	0.60	_	
Forward voltage	V _F (2)	_	I _F = 10mA	1	0.75	_	V
	VF (3)	_	I _F = 100mA	1	0.90	1.20	
Reverse current	I _{R (1)}	_	V _R = 30V	-	_	0.1	
Reverse current	I _{R (2)}	_	V _R = 80V	ı	-	0.5	μΑ
Total capacitance	C _T	_	V _R = 0, f = 1MHz	_	0.9	_	pF
Reverse recovery time	t _{rr}	_	I _F = 10mA (fig.1)	-	1.6	_	ns

Start of commercial production 2000-12

^{**:} Total rating

Pin Assignment (Top View)



Marking

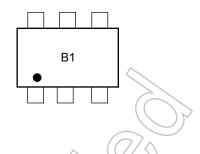
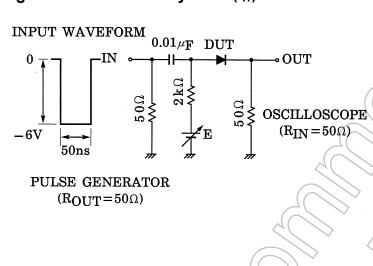
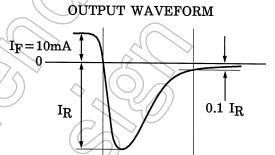


Fig. 1 Reverse Recovery Time (t_{rr}) Test Circuit

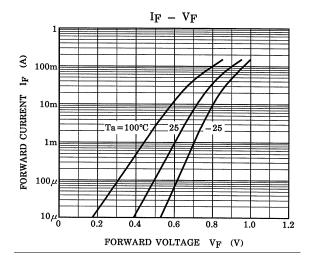


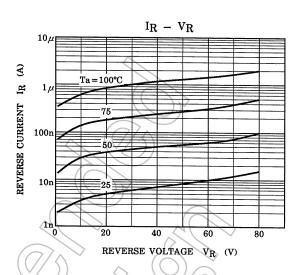


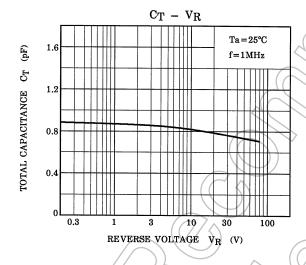
 t_{rr}

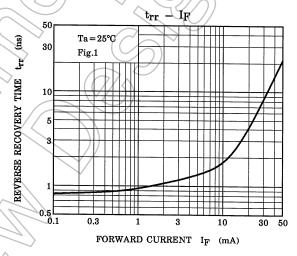
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Q1, Q2, Q3, Q4 Common









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