TOSHIBA Photocoupler IRED & Photo-Transistor

# **TLP785, TLP785F**

Office Equipment
Home Electric Appliances
Solid-State Relays
Switching Power Supplies
Contactless Controller Outputs
Simplex/Multiplex Data Transmission

The TOSHIBA TLP785 consists of a silicone phototransistor optically coupled to an infrared emitting diode in a four lead plastic DIP (DIP4) with having high isolation voltage

(AC: 5000 Vrms (min)).

TLP785F is a lead forming type for the long creepage surface mounting of TLP785.

- TLP785: 7.62 mm pitch type DIP4
- TLP785F: 10.16 mm pitch type DIP4
- Collector-emitter voltage: 80 V (min)
- Current transfer ratio: 50% (min)

Rank GB: 100% (min)

- Isolation voltage: 5000 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized :CSA Component Acceptance Service No.5A

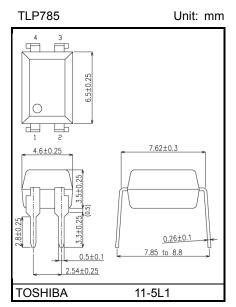
File No.E67349

- VDE-approved : EN 60747-5-5 (Note 1)
- CQC-approved: GB4943.1, GB8898 China Factory
- SEMKO-approved : EN 62368-1

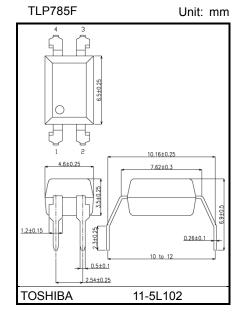
Note 1 : When a VDE approved type is needed, please designate the **Option(D4)**.

· Construction mechanical rating

	7.62 mm Pitch	10.16 mm Pitch
	Standard Type	TLPxxxF Type
Creepage distance	7.0 mm (min)	8.0 mm (min)
Clearance	7.0 mm (min)	8.0 mm (min)
Insulation thickness	0.4 mm (min)	0.4 mm (min)
Inner creepage distance	4.0 mm (min)	4.0 mm (min)

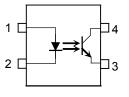


Weight: 0.25 g (typ.)



Weight: 0.25 g (typ.)

#### Pin Configurations (top view)



- 1 : Anode
- 2: Cathode
- 3 : Emitter
- 4 : Collector

Start of commercial production 2010-11



### **Current Transfer Ratio (Note)**

Туре	Classification (Note 1)	(I <sub>C</sub>	sfer Ratio (%) / I <sub>F</sub> ) = 5V, Ta = 25°C   Max	Marking of Classification
	None	50	600	Blank
	Rank Y	50	150	YE
	Rank GR	100	300	GR
	Rank BL	200	600	BL
TLP785	Rank GB	100	600	GB
	Rank YH	75	150	Y+
	Rank GRL	100	200	G
	Rank GRH	150	300	G+
	Rank BLL	200	400	В

Note 1: e.g. rank GB: TLP785 (GB)

Note: Application type name for certification test, please use standard product type name, e.g. TLP785 (GB): TLP785

#### **Absolute Maximum Ratings (Note) (Ta = 25°C)**

	Characteristics		Symbol	Rating	Unit
	Forward current		lF	60	mA
	Forward current derating (Ta ≥ 39°C)		ΔI <sub>F</sub> / °C	-0.7	mA / °C
	Pulse forward current	(Note 2)	I <sub>FP</sub>	1	Α
ED	Power dissipation		PD	90	mW
	Power dissipation derating (Ta ≥ 39°C	;)	ΔP <sub>D</sub> / °C	-0.9	mW / °C
	Reverse voltage		V <sub>R</sub>	5	V
	Junction temperature		Tj	125	°C
	Collector-emitter voltage		V <sub>CEO</sub>	80	V
	Emitter-collector voltage		V <sub>ECO</sub>	7	V
tor	Collector current		Ic	50	mA
Detector	Power dissipation (single circuit)		PC	150	mW
	Power dissipation derating (Ta ≥ 25°C)		ΔP <sub>C</sub> / °C	-1.5	mW / °C
	Junction temperature		Tj	125	°C
Оре	Operating temperature range		T <sub>opr</sub>	−55 to 110	°C
Stor	Storage temperature range		T <sub>stg</sub>	−55 to 125	°C
Lead	Lead soldering temperature (10 s)		T <sub>sol</sub>	260	°C
Tota	Total package power dissipation		PT	240	mW
	Total package power dissipation derating (Ta ≥ 25°C)		ΔP <sub>T</sub> / °C	-2.4	mW / °C
Isola	ation voltage (	(Note 3)	BVs	5000	V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Pulse Width: 100 µs or less, 100 Hz frequency

Note 3: AC, 60 s., R.H. ≤ 60%. Apply voltage to LED pin and detector pin together.

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## **Recommended Operating Conditions (Note)**

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	24	V
Forward current	lF	_	16	25	mA
Collector current	Ic	_	1	10	mA
Operating temperature	T <sub>opr</sub>	-25		85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

## **Individual Electrical Characteristics (Ta = 25°C)**

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10 mA	1.0	1.15	1.3	V
ED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	10	μΑ
	Capacitance	Ст	V = 0 V, f = 1 MHz	_	30	_	pF
	Collector-emitter breakdown voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	80	-	_	V
Detector	Emitter-collector breakdown voltage	V <sub>(BR)</sub> ECO	I <sub>E</sub> = 0.1 mA	7	_	_	V
Dete	Collector dark current	ln/long)	V <sub>CE</sub> = 24 V	_	0.01	0.1	μΑ
	Collector dark current	ID(ICEO)	V <sub>CE</sub> = 24 V ,Ta = 85 °C	_	0.6	50	μΑ
	Capacitance (collector to emitter)	C <sub>CE</sub>	V = 0 V, f = 1 MHz	_	6	_	pF

## **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition		Min	Тур.	Max	Unit
Current transfer ratio	lo / le	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V		50	_	600	%
Current transfer fatto	IC / IF		Rank GB	100	1	600	70
Saturated CTR	lo / le / »	IF = 1 mA, V <sub>CE</sub> = 0.4 V			60	١	%
Saturated CTK	I <sub>C</sub> / I <sub>F</sub> (sat)		Rank GB	30	١	1	/0
		I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = 8 mA			١	0.4	
Collector-emitter saturation voltage	VCE (sat)	IC = 0.2 mA, IF = 1 mA			0.2	_	V
			Rank GB	_	_	0.4	

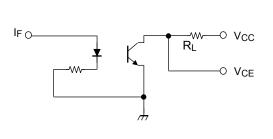
## Isolation Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVS	AC, 60 s	5000	_	_	V <sub>rms</sub>

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## **Switching Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>		_	2	_	
Fall time	tf	V <sub>CC</sub> = 10 V, I <sub>C</sub> = 2 mA	_	3	_	0
Turn-on time	ton	R <sub>L</sub> = 100 Ω	_	3	_	μS
Turn-off time	t <sub>off</sub>		_	3	_	
Turn-on time	ton		_	1.5	_	
Storage time	ts	$R_L$ = 1.9 kΩ (fig. 1) $V_{CC}$ = 5 V, $I_F$ = 16 mA	_	25	_	μS
Turn-off time	toff		_	50	_	



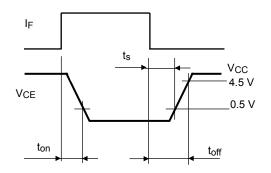
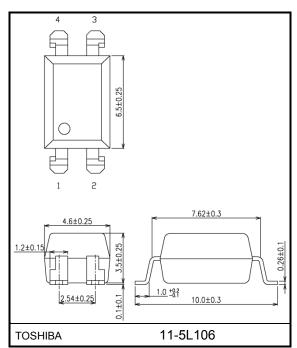


fig. 1: Switching time test circuit

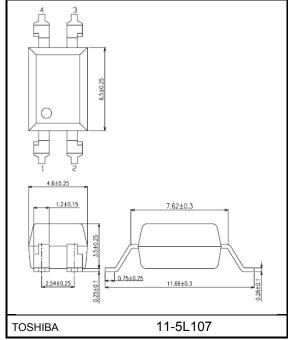
## **Surface-Mount Lead Form Option**

TLP785(LF6) Unit: mm



Weight: 0.24 g (typ.)

TLP785F(LF7)



Weight: 0.25 g (typ.)

Unit: mm

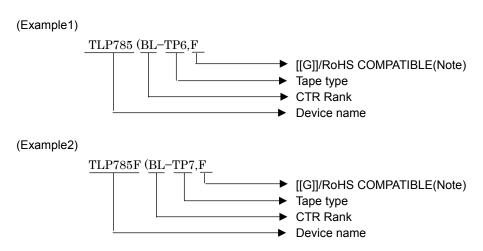
## Option: Specifications for Embossed-Tape Packing; (TP6) / (TP7)

#### 1. Applicable Package

Package Name	Product Type
DIP4LF6	TLP785
DIP4LF7	TLP785F

#### 2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.



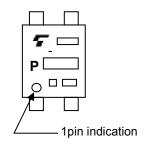
Note: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

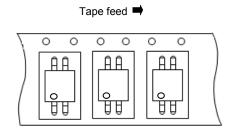
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#### 3. Tape Dimensions

#### 3.1 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 2.





**Figure2 Device Orientation** 

### 3.2 Tape Packing Quantity: 2000 devices per reel



#### 3.3 Empty Device Recesses Are as Shown in Table 1.

#### **Table1 Empty Device Recesses**

	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0 device	Within any given 40mm section of tape, not including leader and trailer
Single empty device recesses	6 devices (max) per reel	Not including leader and trailer

#### 3.4 Start and End of Tape

The start of the tape has 30 or more empty holes. The end of the tape has 50 or more empty holes.

#### 3.5 Tape Specification

- [1] TLP785(TP6) / TLP785F(TP7)
  - ·Tape material: Synthetic Resin
  - ·Dimensions: The tape dimensions are as shown in Figure 3.

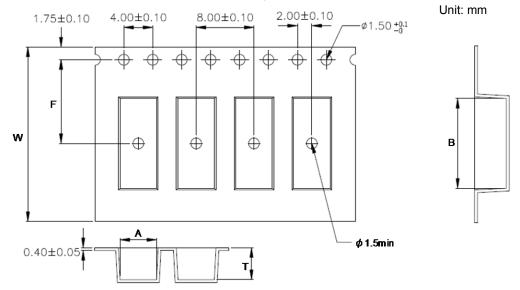


Figure 3 Tape Forms

	TP6 Type	TP7 Type
Α	5.1±0.1	5.05±0.1
В	10.6±0.1	12.35±0.1
W	16.0±0.3	24.0±0.3
F	7.5±0.1	11.5±0.1
Т	4.2±0.15	4.4±0.1

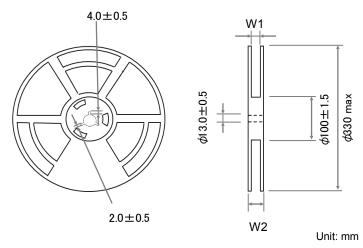
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#### 3.6 Reel Specification

#### [1] TLP785(TP6) / TLP785F(TP7)

- ·Material: Synthetic Resin
- ·Dimensions: The reel dimensions are as shown in Figure 4.



	TP6 Type	TP7 Type
W1	16.5typ	24.4typ
W2	23max	30.4max

Figure 4 Reel Forms

#### 4. Packing

Two reels of photocouplers are packed in a shipping carton.

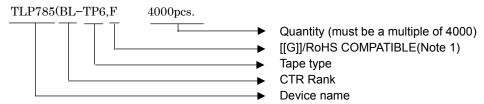
#### 5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

#### 6. Ordering Information

When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.

(Example)



Note: The order code may be suffixed with a letter or a digit.

Please contact your nearest Toshiba sales representative for more details.

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

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#### 7. Soldering and Storage

#### 7.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

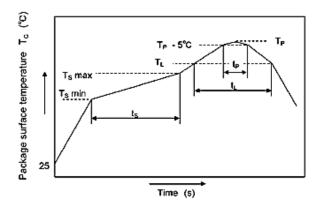
When using soldering reflow

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	Ts	150	200	°C
Preheat time	ts	60	120	5
Ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )			3	°C/s
Liquidus temperature	TL	2	°C	
Time above T <sub>L</sub>	t <sub>L</sub>	60	150	S
Peak temperature	Tp		260	°C
Time during which $T_c$ is between $(T_P - 5)$ and $T_P$	t <sub>P</sub>		30	5
Ramp-down rate (T <sub>P</sub> to T <sub>L</sub> )			6	°C/s

Fig. 7.1 An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

When using soldering flow

Preheat the device at a temperature of 150  $^{\circ}\text{C}$  (package surface temperature) for 60 to 120 seconds.

Mounting condition of 260 °C within 10 seconds is recommended.

Flow soldering must be performed once.

When using soldering Iron

Complete soldering within 10 seconds for lead temperature not exceeding 260  $^{\circ}\text{C}$  or within 3 seconds not exceeding 350  $^{\circ}\text{C}$ 

Heating by soldering iron must be done only once per lead.

## 7.2. Precautions for General Storage

- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- When restoring devices after removal from their packing, use anti-static containers.
- Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

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EN 60747-5-5 'Option: (D4)'

Attachment: Specification for EN 60747-5-5 option: (D4)

Types: TLP785, TLP785F

Type designations for 'option: (D4) ', which are tested under EN 60747 requirements.

e.g.: TLP785(D4-GR-LF6,F D4: EN 60747 option

GR: CTR rank name

LF6: standard lead bend name

F: [[G]]/RoHS COMPATIBLE(Note 1)

Note: Use TOSHIBA standard type number for safety standard application.

e.g. TLP785(D4-GR-LF6,F → TLP785

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

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#### **EN 60747 Isolation Characteristics**

Description		Symbol	Rating	Unit
Application classification				
for rated mains voltage ≤ 300 V <sub>rms</sub> for rated mains voltage ≤ 600 V <sub>rms</sub>			I–IV I–III	_
Climatic classification			55 / 115 / 21	_
Pollution degree			2	_
Maximum operating insulation voltage	TLP785	)/(a=	890	Vpk
	TLP785F	VIORM	1140	
Input to output test voltage,  Vpr = 1.6×V <sub>IORM</sub> , type and sample test t <sub>p</sub> = 10 s, partial discharge < 5pC	TLP785	.,	1424	- Vpk
	TLP785F	V <sub>pr</sub>	1824	
Input to output test voltage,  Vpr = 1.875×V <sub>IORM</sub> , 100% production test t <sub>p</sub> = 1 s, partial discharge < 5pC	TLP785	.,	1670	- Vpk
	TLP785F	Vpr	2140	
Highest permissible overvoltage (transient overvoltage, t <sub>pr</sub> = 60s)	<u>.</u>	VTR	8000	Vpk
Safety limiting values (max. permissible ratings in case of fault) current (input current) P <sub>si</sub> = 0mW power (output or total power dissipation) temperature		I <sub>si</sub> Psi Ts	400 700 175	mA mW °C
Insulation resistance, V <sub>IO</sub> = 500 V, Ta=25°C		Rsi	≥10 <sup>12</sup>	Ω

## **Insulation Related Specifications**

		7.62 mm pitch TLPxxx type	10.16 mm pitch TLPxxxF type
Minimum creepage distance	Cr	7.0 mm	8.0 mm
Minimum clearance	CI	7.0 mm	8.0 mm
Minimum insulation thickness	ti	0.4 mm	
Comparative tracking index	СТІ	175	

- (1) If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value. (e.g.at a standard distance between soldering eye centres of 7.5mm). If this is not permissible, the user shall take suitable measures.
- (2) This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits.

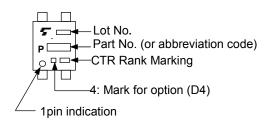
VDE test sign: Marking on product for EN 60747

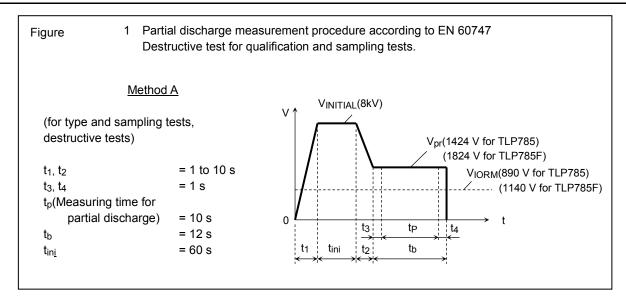
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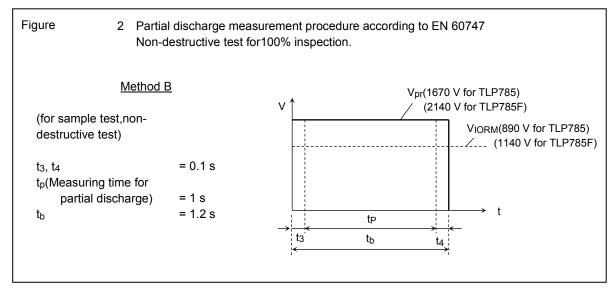
Marking on packing for EN 60747

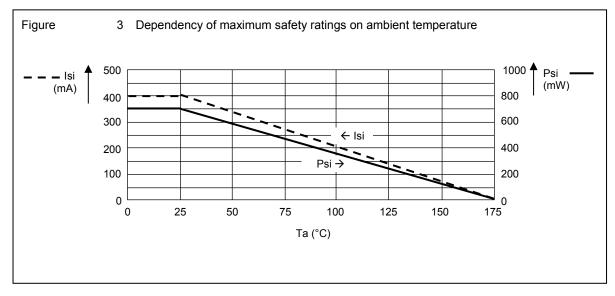


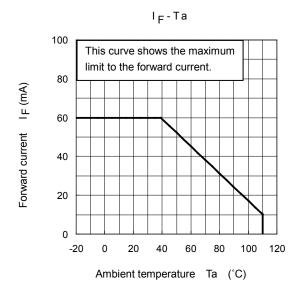
Marking Example: TLP785, TLP785F

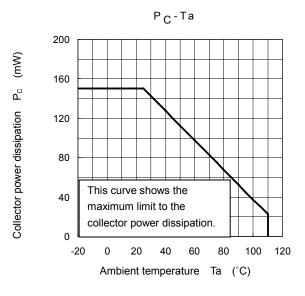


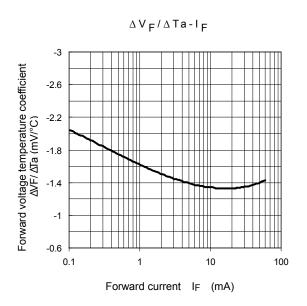


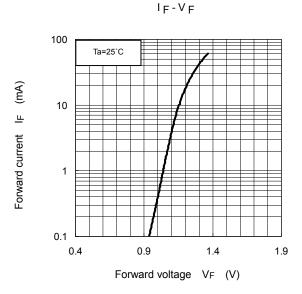


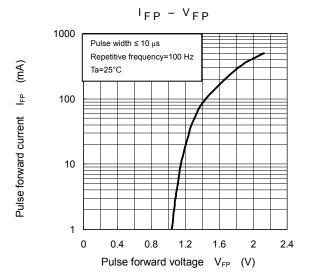




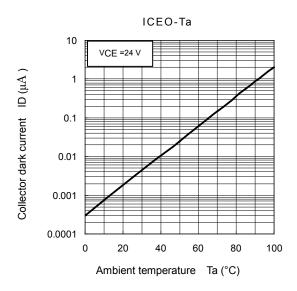


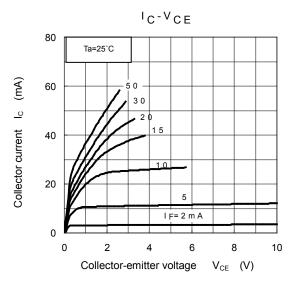


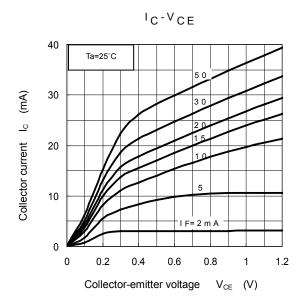


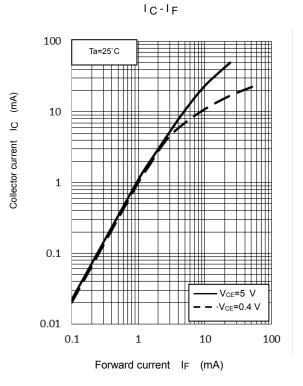


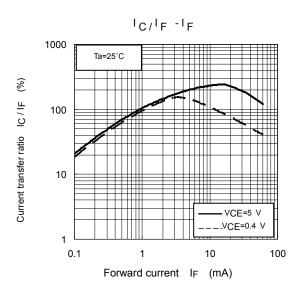
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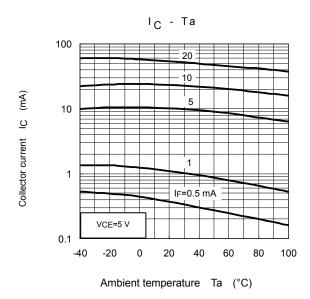


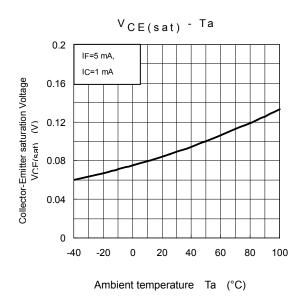


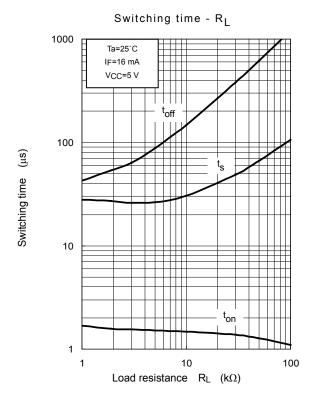




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