TOSHIBA Field Effect Transistor Silicon N Channel MOS Type $(\pi - MOSVII)$

TK12A53D

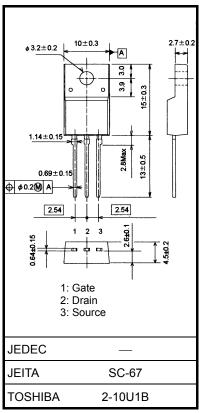
Switching Regulator Applications

Unit: mm

- Low drain-source ON-resistance: $R_{DS (ON)} = 0.5 \Omega (typ.)$
- High forward transfer admittance: $|Y_{fS}| = 6.0 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 525 \text{ V)}$
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	525	V
Gate-source voltage		V _{GSS}	±30	V
Drain current	DC (Note 1)	ID	12	Α
	Pulse (Note 1)	I _{DP}	48	
Drain power dissipation	on (Tc = 25°C)	PD	45	W
Single pulse avalanche energy (Note 2)		E _{AS}	378	mJ
Avalanche current		I _{AR}	12	Α
Repetitive avalanche energy (Note 3)		E _{AR}	4.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

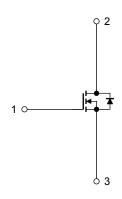
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.5 mH, R_{G} = 25 Ω , I_{AR} = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



Start of commercial production 2008-10



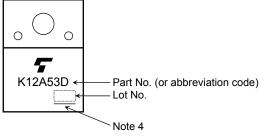
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm30~V,~V_{DS}=0~V$	_	_	±1	μА
Drain cut-off current		I _{DSS}	V _{DS} = 525 V, V _{GS} = 0 V	_	_	10	μА
Drain-source brea	akdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	525	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 6 A	_	0.50	0.58	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6 A	1.5	6.0	_	S
Input capacitance		C _{iss}		_	1350	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	6	_	
Output capacitance		C _{oss}		_	135	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{ID} = 6 \text{ A} & \text{Vout} \\ V_{GS} & \text{O} & \text{V} & \text{S} \\ 50 \Omega & \text{V} & \text{RL} = 33 \Omega \end{array}$		22		
	Turn-on time	t _{on}			55	_	ns
	Fall time	t _f		_	15	_	110
	Turn-off time	t _{off}	Duty \leq 1%, $t_W = 10 \mu s$	_	100	_	
Total gate charge		Qg		_	25	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$	_	16	_	nC
Gate-drain charge		Q _{gd}		_	9	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	12	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	48	Α
Forward voltage (diode)	V_{DSF}	I _{DR} = 12 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 12 A, V _{GS} = 0 V,	_	1300	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	12	_	μС

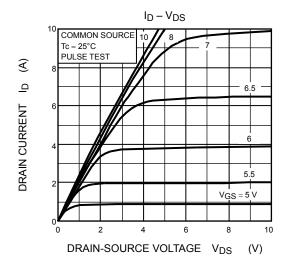
Marking

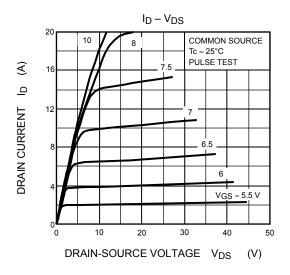


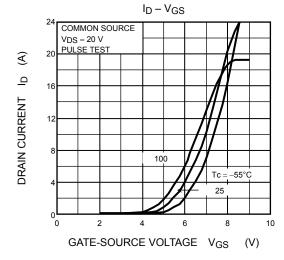
Note 4: A line under a Lot No. identifies the indication of product Labels.

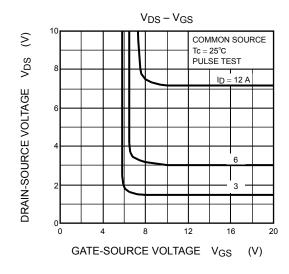
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

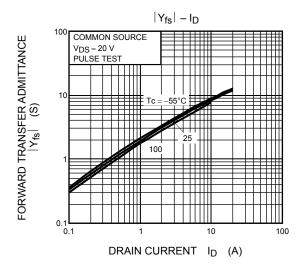
Part No. (or abbreviation code)
Lot No.
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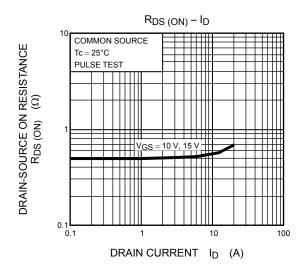


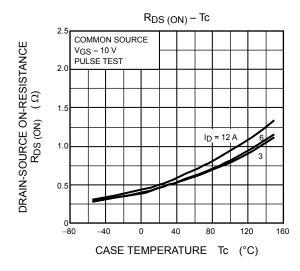


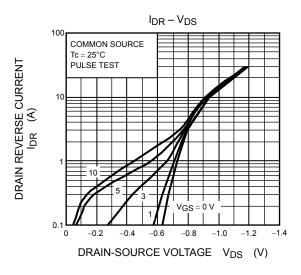


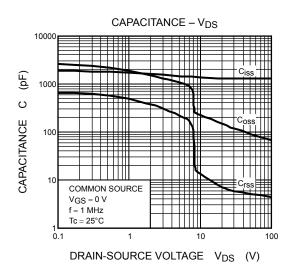


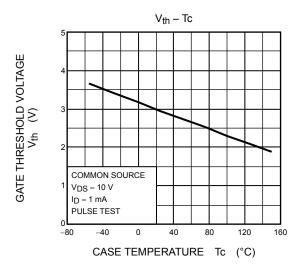


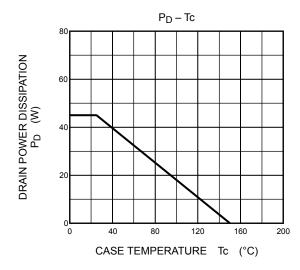


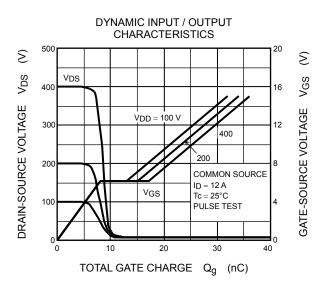


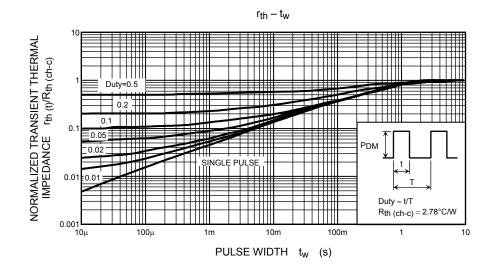


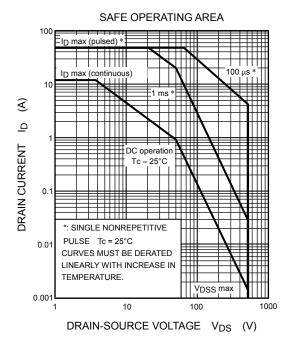


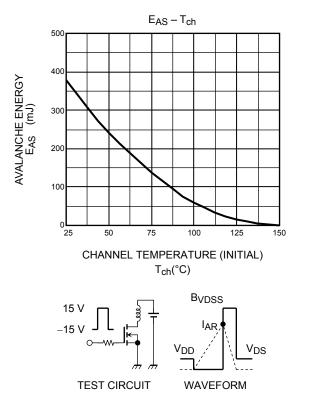












 $R_G = 25~\Omega$ $V_{DD} = 90~V,~L = 4.5~mH$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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