Unit: mm



TOSHIBA Diode Silicon Epitaxial Planar Type

# **1SS352**

#### **Ultra High Speed Switching Application**

• AEC-Q101 Qualified (Note1)

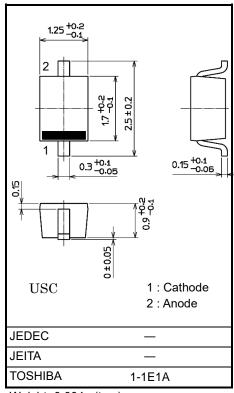
• Small package

• Low forward voltage : VF(3) = 0.98 V (typ.)• Fast reverse recovery time:  $t_{rr} = 1.6 \text{ ns (typ.)}$ • Small total capacitance : CT = 0.5 pF (typ.)

Note1: For detail information, please contact our sales.

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V <sub>RM</sub>	85	V	
Reverse voltage	V <sub>R</sub>	80	V	
Maximum (peak) forward current	I <sub>FM</sub>	200	mA	
Average forward current	lo	100	mA	
Surge current (10ms)	I <sub>FSM</sub>	1	Α	
Power dissipation	P <sub>D</sub> (Note 4)	200	mW	
Junction temperature	T <sub>j</sub> (Note 2)	150	°C	
	T <sub>j</sub> (Note 3)	125		
Storage temperature	T <sub>stg</sub> (Note 2)	-55 to 150	°C	
	T <sub>stg</sub> (Note 3)	-55 to 125		



Weight: 0.004g (typ.)

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: For devices with the ordering part number ending in H3F(T.

Note 3: For devices with the ordering part number in other than H3F(T.

Note 4: Mounted on a glass epoxy circuit board of 20 mm × 20 mm, Pad dimension of 4 mm × 4 mm.

#### **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	VF (1)	I <sub>F</sub> = 1 mA	_	0.62	_	V
	VF (2)	I <sub>F</sub> = 10 mA	_	0.75	_	
	V <sub>F</sub> (3)	I <sub>F</sub> = 100 mA	_	0.98	1.20	
Reverse current	I <sub>R (1)</sub>	V <sub>R</sub> = 30 V	_	_	0.1	μА
	I <sub>R (2)</sub>	V <sub>R</sub> = 80 V	_	_	0.5	
Total capacitance	CT	$V_R = 0 V, f = 1 MH_Z$	_	0.5	3.0	pF
Reverse recovery time	t <sub>rr</sub>	IF = 10 mA, Fig.1	_	1.6	4.0	ns

Start of commercial production 1989-10



#### INPUT WAVEFORM **OUTPUT WAVEFORM** $0.01 \mu F$ DUT INPUTo ⊸ OUTPUT 0 $I_F = 10mA$ \$00 \$00 | (\*) SAMPLING OSCILLOSCOPE $(R_{\rm IN}\!=\!50\Omega)$ $I_{\mathbf{R}}$ $0.1 I_{R}$ 50nsPULSE GENERATOR $(R_{OUT} = 50\Omega)$ $t_{rr}$

Fig.1 Reverse Recovery Time (trr) Test Circuit

### **Equivalent Circuit (Top View)**

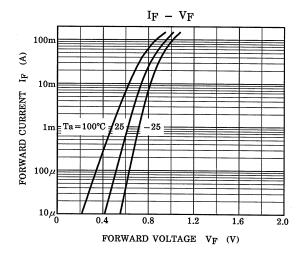


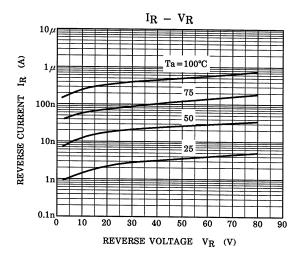
### Marking

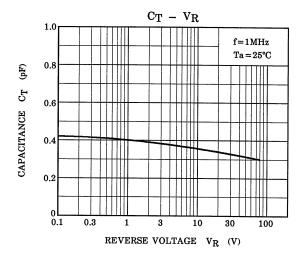




### **Electrical Characteristics (Ta = 25°C)**







The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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