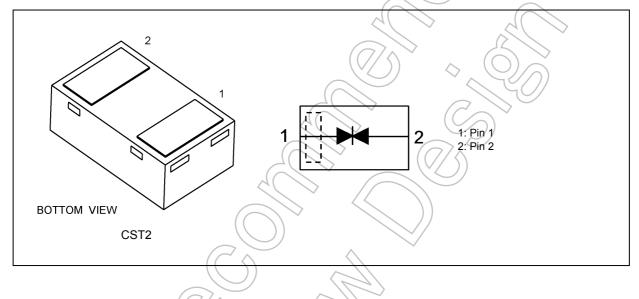
ESD Protection Diodes Silicon Epitaxial Planar

DF2B12M1CT

1. Applications

- ESD Protection
- Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

2. Packaging and Internal Circuit



3. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25^{\circ}$ C)

Characteristics	Symbol	Rating	Unit
Electrostatic discharge voltage (IEC61000-4-2)(Contact)	VESD	±8	kV
Junction temperature	Tj	150	°C
Storage temperature	∠ T _{stg}	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

I_R: Reverse current V_C: Clamp voltage IPP: Peak pulse current R_{DYN}: Dynamic resistance

4. Electrical Characteristics (Unless otherwise specified, T_a = 25°C)

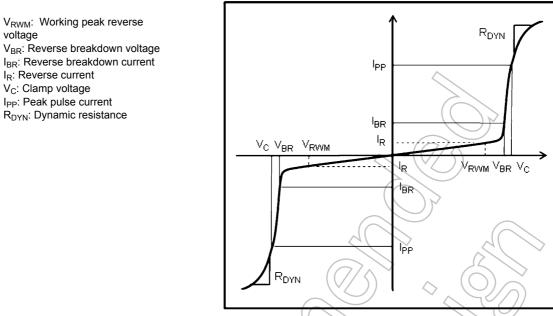


Fig. 4.1 Definitions of Electrical Characteristics

Symbol	Note	Test Condition	Min	Тур.	Max	Unit
V _{RWM}) - (_	8	V
V_{BR}	2(I _{BR} = 1 mA	10	_	_	V
I _R		V _{RWM} = 8 V	_	_	0.05	μA
V _C	(Note 1)	ү _{РР} = 1 А	_	18	_	V
R _{DYN}	(Note 2)	\downarrow	_	2.5	—	Ω
Ct	(Note 3)	V _R = 0 V, f = 1 MHz	_	0.3	0.5	pF
	V _{RWM} V _{BR} I _R V _C R _{DYN}	V _{RWM} V _{BR} I _R V _C (Note 1) R _{DYN} (Note 2)	V_{RWM} V_{BR} $I_{BR} = 1 \text{ mA}$ I_R $V_{RWM} = 8 \text{ V}$ V_C $(Note 1)$ $I_{PP} = 1 \text{ A}$ R_{DYN} $(Note 2)$	V _{RWM} V _{BR} I _{BR} = 1 mA 10 I _R V _{RWM} = 8 V V _C (Note 1) I _{PP} = 1 A R _{DYN} (Note 2)	V_{RWM} - - V_{BR} I_{BR} = 1 mA 10 - I_R V_{RWM} = 8 V - - - V_C (Note 1) I_{PP} = 1 A - 18 R_{DYN} (Note 2) - - 2.5	V _{RWM} - - 8 V _{BR} I _{BR} =1.mA 10 - - I _R V _{RWM} =8 V - - 0.05 V _C (Note 1) I _{PP} =1 A - 18 - R _{DYN} (Note 2) - - 2.5 -

Note 1: Based on IEC61000-4-5 8/20 µs pulse.

Note 2: TLP parameter: Z0 = 50 Ω , tp = 100 ns, tr = 300 ps, averaging window: t1 = 30 ns to t2 = 60 ns,

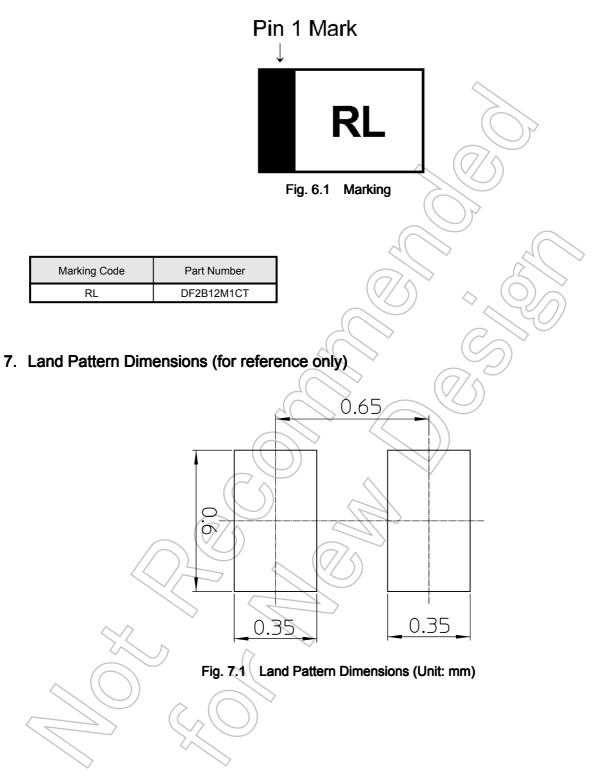
extraction of dynamic resistance using a least-squares fit of TLP characteristics at IPP between 3 A to 8 A.

Note 3: Guaranteed by design.

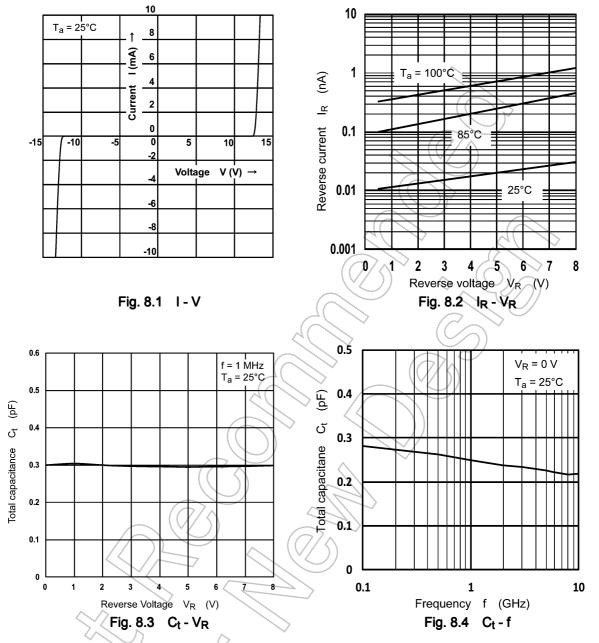
5. Guaranteed ESD Protection (Note)

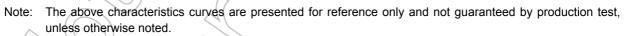
Test Condition	ESD Protection		
IEC61000-4-2 (Contact discharge)	±8 kV		
Note: Criterion: No damage to de	vices.		

6. Marking



8. Characteristics Curves (Note)





9. Clamp Voltage V_C - Peak Pulse Current (IPP) (Note)

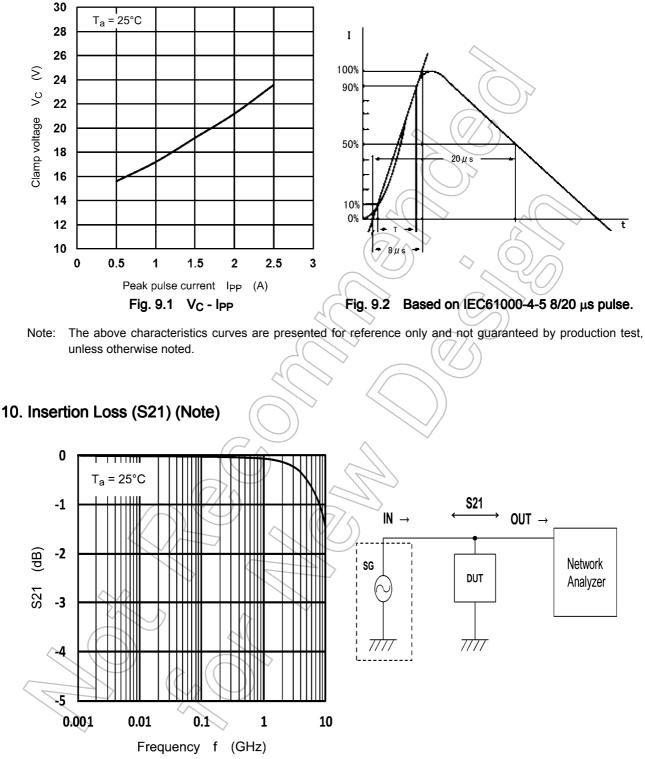
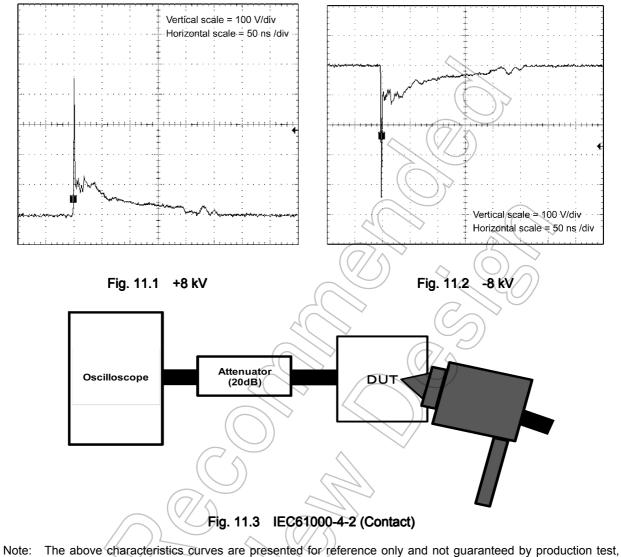


Fig. 10.1 S21 - f

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

11. ESD Clamp Waveform (Note)

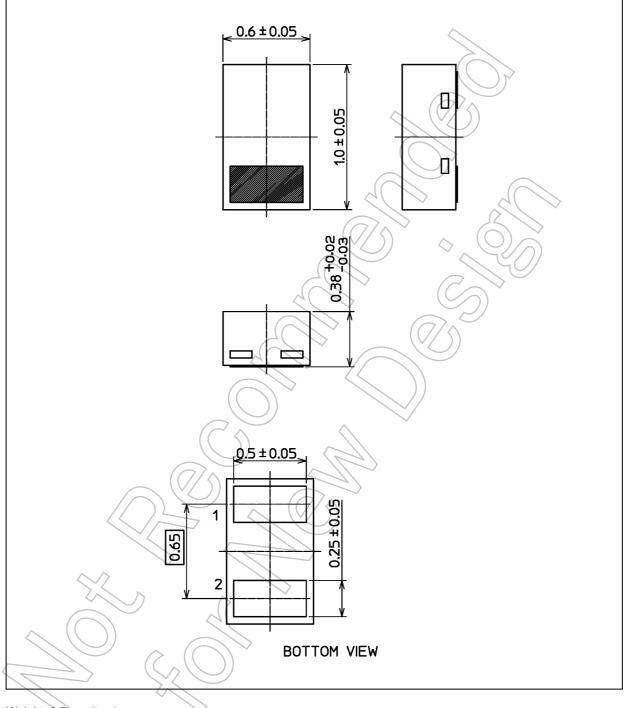


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.7 mg (typ.)

	Package Name(s)	
TOSHIBA: 1-1P1S		
Nickname: CST2		

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