Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVII)

# TK8A65D

### **Switching Regulator Applications**

• Low drain-source ON-resistance: RDS (ON) =  $0.7 \Omega$  (typ.)

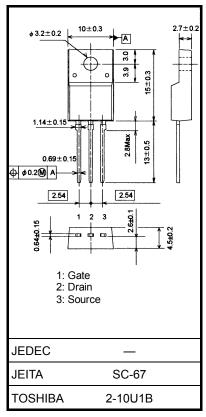
• High forward transfer admittance:  $|Y_{fs}| = 4.5 \text{ S (typ.)}$ 

• Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 650 \text{ V)}$ 

• Enhancement mode:  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$ 

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	650	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	8	Α	
	Pulse (Note 1)	I <sub>DP</sub>	32		
Drain power dissipati	on (Tc = 25°C)	P <sub>D</sub>	45	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	416	mJ	
Avalanche current		I <sub>AR</sub>	8	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	4.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

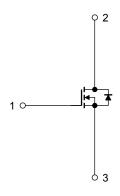
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 11.5 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 8 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2009-01



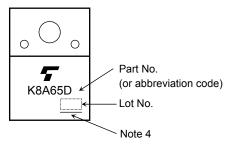
### **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source brea	akdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	650	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A	_	0.7	0.84	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 4 A	1.3	4.5	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		1350	_	pF
Reverse transfer capacitance		C <sub>rss</sub>			6	_	
Output capacitance		Coss			135	_	
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c c} 10 \text{ V} & I_D = 4 \text{ A} & V_{OUT} \\ \hline 0 \text{ V} & & \\ \hline 50  \Omega & & \\ \hline \end{array} \\ \begin{array}{c} R_L = 50  \Omega \\ \hline \end{array}$	_	22	_	- ns
	Turn-on time	t <sub>on</sub>		_	55	_	
	Fall time	t <sub>f</sub>		_	15	_	
	Turn-off time	t <sub>off</sub>		_	100	_	
Total gate charge		Qg		_	25	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8 A	_	16	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	9	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

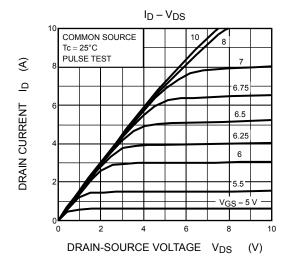
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	8	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	32	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 8 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 8 A, V <sub>GS</sub> = 0 V,	_	1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	12	_	μС

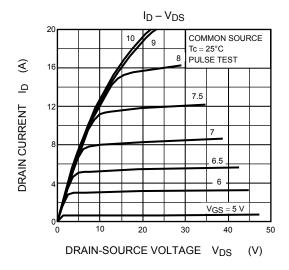
### Marking

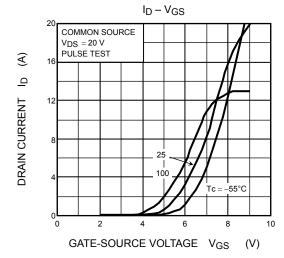


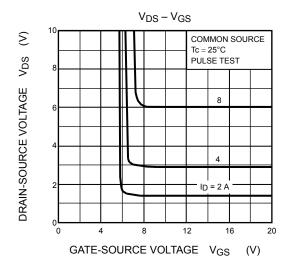
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

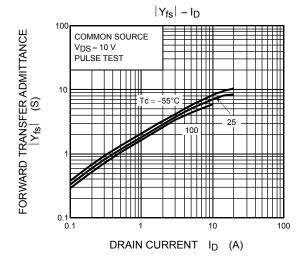
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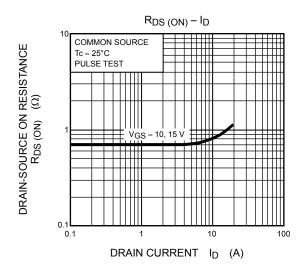


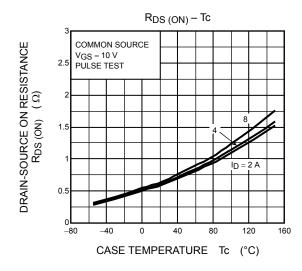


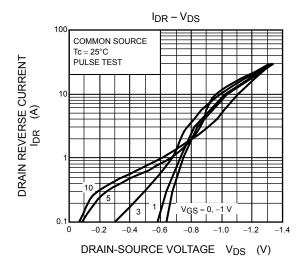


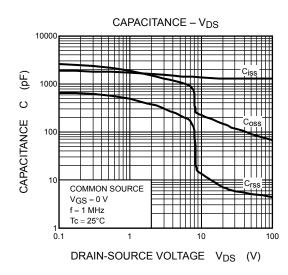


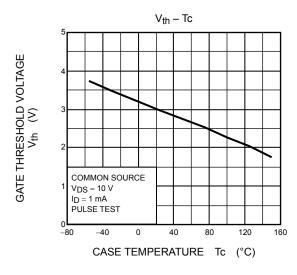


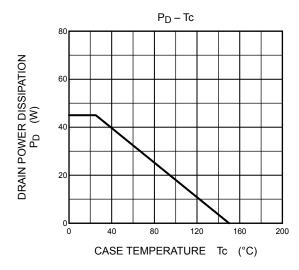


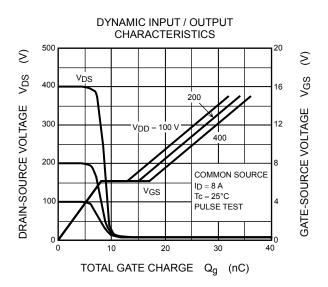


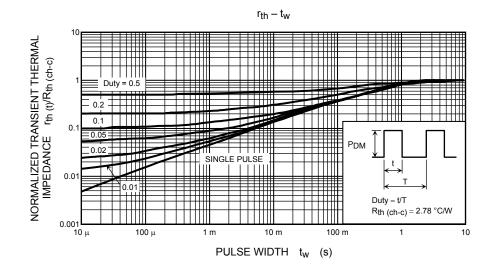


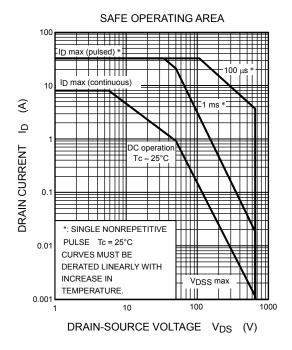


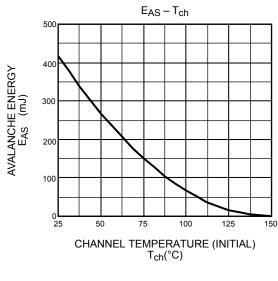


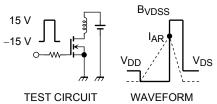












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 11.5~mH \end{aligned} \qquad \text{EAS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right) \cdot \frac{1}{2} \cdot \frac{1}{2}$$

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