

ESD Protection Diodes Silicon Epitaxial Planar

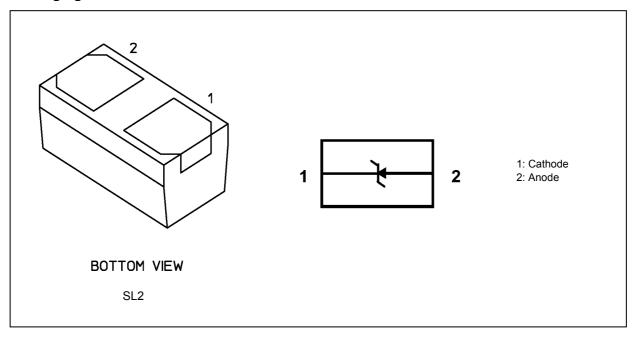
# DF2S8.2ASL

#### 1. Applications

· ESD Protection

Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

#### 2. Packaging and Internal Circuit



#### 3. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Note	Rating	Unit
Electrostatic discharge voltage (IEC61000-4-2)(Contact)	V <sub>ESD</sub>	(Note 1)	±30	kV
Electrostatic discharge voltage (IEC61000-4-2)(Air)				
Peak pulse power(tp = 8/20 μs)	P <sub>PK</sub>		55	W
Peak pulse current(tp = 8/20 μs)	I <sub>PP</sub>	(Note 2)	2.5	Α
Junction temperature	Tj		150	°C
Storage temperature	T <sub>stg</sub>		-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: According to IEC61000-4-2. Note 2: According to IEC61000-4-5.

Start of commercial production



#### 4. Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25°C)

 $V_{\text{RWM}}$ : Working peak reverse voltage

V<sub>Z</sub>: Zener voltage

V<sub>BR</sub>: Reverse breakdown voltage

Z<sub>Z</sub>: Dynamic impedance

Iz: Zener current

I<sub>BR</sub>: Reverse breakdown current

IR: Reverse current V<sub>C</sub>: Clamp voltage

I<sub>PP</sub>: Peak pulse current R<sub>DYN</sub>: Dynamic resistance

I<sub>F</sub>: Forward current

V<sub>F</sub>: Forward voltage

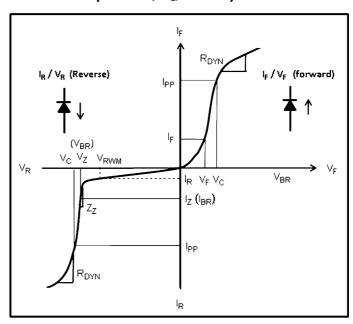


Fig. 4.1 Definitions of Electrical Characteristics

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Working peak reverse voltage	$V_{RWM}$		_	_	_	6.5	V
Zener voltage (Reverse breakdown voltage)	V <sub>Z</sub> (V <sub>BR</sub> )		I <sub>Z</sub> = 5 mA (I <sub>BR</sub> = 5 mA)	7.7	8.2	8.7	V
Dynamic impedance	Z <sub>Z</sub>		$I_Z = 5 \text{ mA}$ $(I_{BR} = 5 \text{ mA})$	_		30	Ω
Reverse current	I <sub>R</sub>		V <sub>RWM</sub> = 6.5 V	_		0.5	μА
Clamp voltage	V <sub>C</sub>	(Note 1)	I <sub>PP</sub> = 1 A	_	11.5	_	V
			I <sub>PP</sub> = 2.5 A	_	17	22	
Clamp voltage	V <sub>C</sub>	(Note 2)	I <sub>TLP</sub> = 16 A	_	28	_	V
			I <sub>TLP</sub> = 30 A	_	39	_	
Dynamic resistance	R <sub>DYN</sub>	(Note 2)	_	_	0.8	_	Ω
Total capacitance	Ct	(Note 3)	V <sub>R</sub> = 0 V, f = 1 MHz	_	20	_	pF

Note 1: Based on IEC61000-4-5 8/20  $\mu s$  pulse.

Note 2: TLP parameter: Z0 = 50  $\Omega$ , tp = 100 ns, tr = 300 ps, averaging window: t1 = 30 ns to t2 = 60 ns, extraction of dynamic resistance using a least-squares fit of TLP characteristics at IPP between 8 A to 16 A.

Note 3: Guaranteed by design.



### 5. Marking

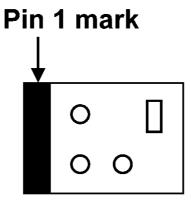


Fig. 5.1 Marking

#### 6. Land Pattern Dimensions (for reference only)

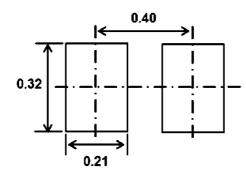


Fig. 6.1 Land Pattern Dimensions (Unit: mm)



### 7. Characteristics Curves (Note)

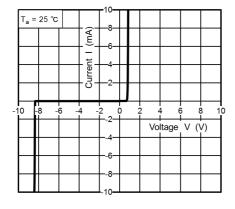


Fig. 7.1 I-V

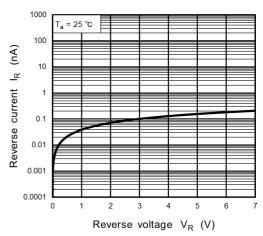
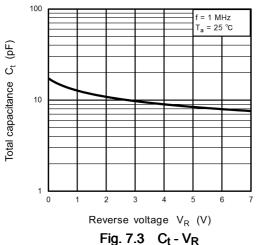


Fig. 7.2 I<sub>R</sub> - V<sub>R</sub>



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



### 8. Clamp Voltage - Peak Pulse Current (V<sub>C</sub> - I<sub>PP</sub>) (Note)

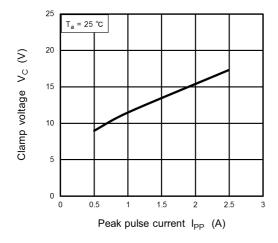


Fig. 8.1 V<sub>C</sub> - I<sub>PP</sub>

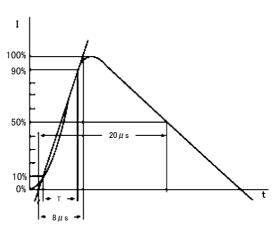


Fig. 8.2 Based on IEC61000-4-5 8/20  $\mu$ s pulse.(Ed.2)

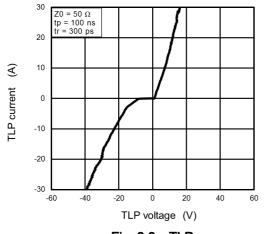
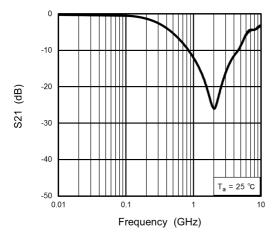


Fig. 8.3 TLP

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### 9. Insertion Loss (S21) (Note)



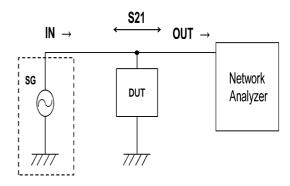
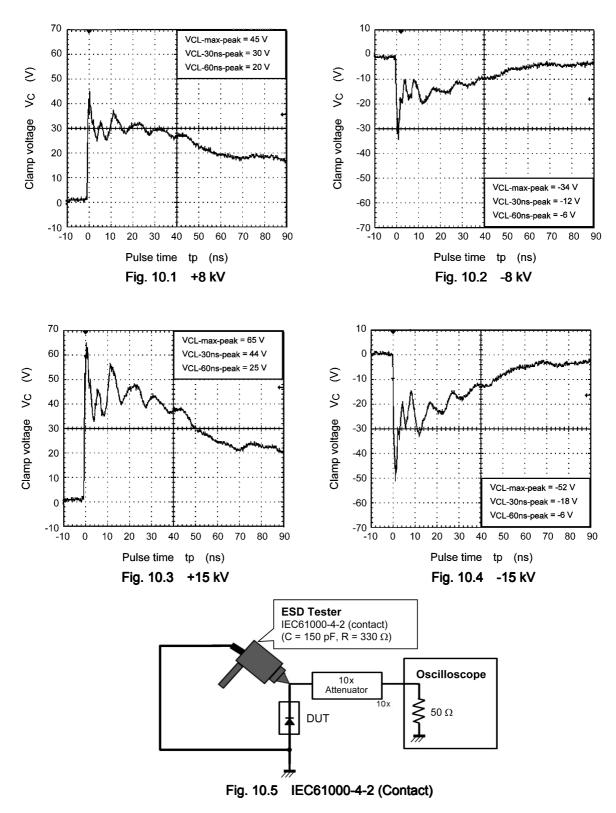


Fig. 9.1 S21 - f

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#### 10. ESD Clamp Waveform (Note)

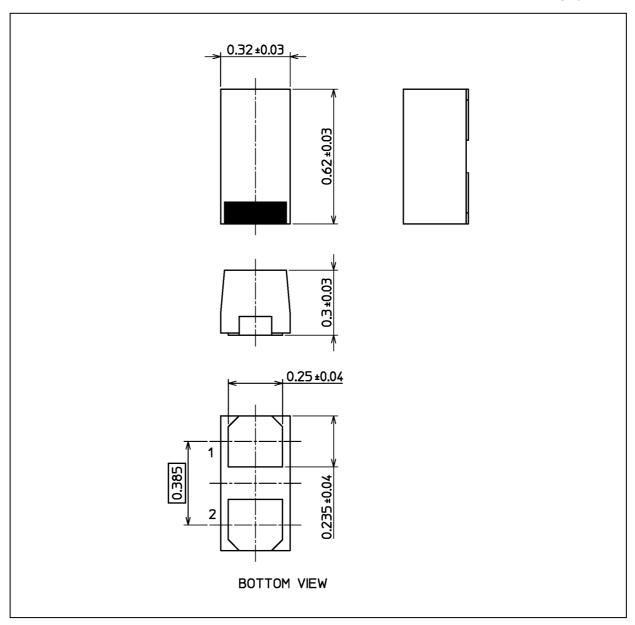


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



#### **Package Dimensions**

Unit: mm



Weight: 0.2 mg (typ.)

Package Name(s)
TOSHIBA: 1-1AL1A
Nickname: SL2

Rev.1.0



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