TPC8129

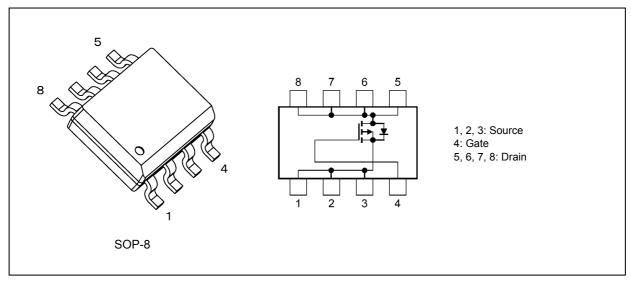
1. Applications

- Lithium-Ion Secondary Batteries
- Power Management Switches

2. Features

- (1) Small footprint due to a small and thin package
- (2) Low drain-source on-resistance: $R_{DS(ON)} = 17 \text{ m}\Omega$ (typ.) ($V_{GS} = -10 \text{ V}$)
- (3) Low leakage current: $I_{DSS} = -10 \ \mu A \ (max) \ (V_{DS} = -30 \ V)$
- (4) Enhancement mode: V_{th} = -0.8 to -2.0 V (V_{DS} = -10 V, I_D = -0.2 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25 \ ^{\circ}C$ unless otherwise specified)

Characteris	Symbol	Rating	Unit		
Drain-source voltage			V _{DSS}	-30	V
Gate-source voltage			V _{GSS}	-25/+20]
Drain current (DC)		(Note 1)	Ι _D	-9	A
Drain current (pulsed)		(Note 1)	I _{DP}	-36	1
Power dissipation	(t = 10 s)	(Note 2)	PD	1.9	W
Power dissipation	(t = 10 s)	(Note 3)	PD	1.0	W
Single-pulse avalanche energy		(Note 4)	E _{AS}	21	mJ
Avalanche current			I _{AR}	-9	A
Channel temperature			T _{ch}	150	°C
Storage temperature			T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2010-07

5. Thermal Characteristics

Characteristi	cs		Symbol	Max	Unit
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 2)	R _{th(ch-a)}	65.7	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R _{th(ch-a)}	125	°C/W

Note 1: Ensure that the channel temperature does not exceed 150 °C.

Note 2: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 3: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 4: V_DD = -24 V, T_ch = 25 °C (initial), L = 0.2 mH, R_G = 25 Ω , I_{AR} = -9 A





Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a) Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

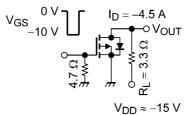
6.1. Static Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V	_		±0.1	μA
Drain cut-off current	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V	_		-10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = -10 mA, V _{GS} = 0 V	-30		_	V
Drain-source breakdown voltage (Note 5)	V _{(BR)DSX}	I _D = -10 mA, V _{GS} = 10 V	-21	_	—	
Gate threshold voltage	V _{th}	V _{DS} = -10 V, I _D = -0.2 mA	-0.8		-2.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = -4.5 V, I _D = -4.5 A	_	22	28	mΩ
		V _{GS} = -10 V, I _D = -4.5 A	_	17	22	

Note 5: If a reverse bias is applied between gate and source, this device enters V_{(BR)DSX} mode. Note that the drainsource breakdown voltage is lowered in this mode.

6.2. Dynamic Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz		1650	_	pF
Reverse transfer capacitance	C _{rss}]	_	260	_	
Output capacitance	C _{oss}		_	300	_	
Switching time (rise time)	tr	See Fig. 6.2.1.	_	8	_	ns
Switching time (turn-on time)	t _{on}]	_	16	_	
Switching time (fall time)	t _f]	_	42	—	
Switching time (turn-off time)	t _{off}]		140		



Duty \leq 1%, t_w = 10 µs

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx$ -24 V, V_{GS} = -10 V, I_D = -9 A	—	39	—	nC
Gate-source charge 1	Q _{gs1}		_	4	_	
Gate-drain charge	Q _{gd}		_	10		

6.4. Source-Drain Characteristics ($T_a = 25$ °C unless otherwise specified)

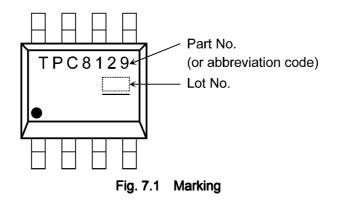
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed)	(Note 6)	I _{DRP}		_	_	-36	А
Diode forward voltage		V _{DSF}	I _{DR} = -9 A, V _{GS} = 0 V	_	_	1.2	V

Note 6: Ensure that the channel temperature does not exceed 150 °C.

TPC8129

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7. Marking (Note)



 Note:
 A line under a Lot No. identifies the indication of product Labels.

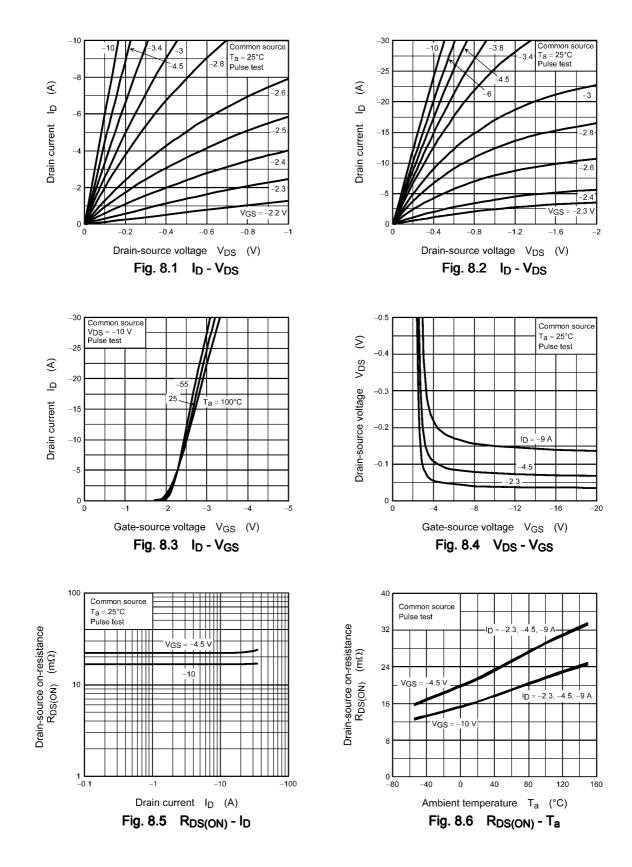
 Not underlined: [[Pb]]/INCLUDES > MCV

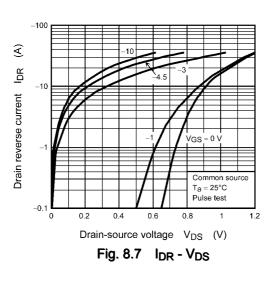
 Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

 Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

 The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

8. Characteristics Curves (Note)





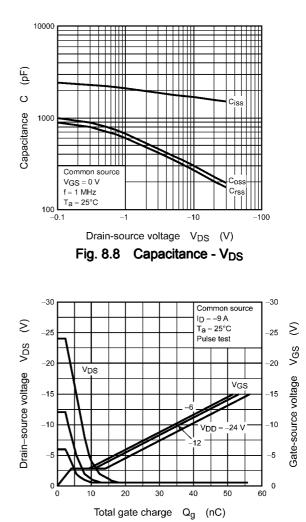
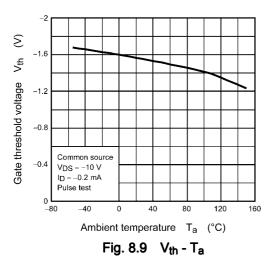
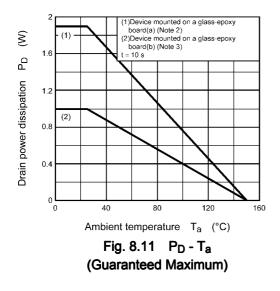
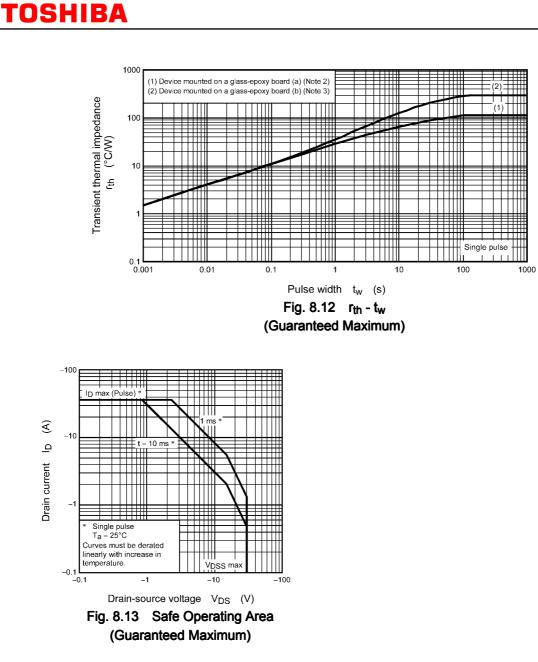


Fig. 8.10 Dynamic Input/Output Characteristics







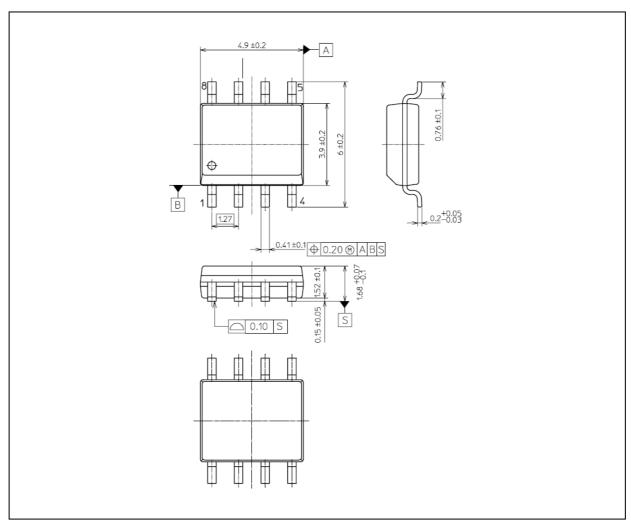
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

TPC8129

Unit: mm



Weight: 0.085 g (typ.)

	Package Name(s)
TOSHIBA: 2-5R1S	
Nickname: SOP-8	

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