

TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process) (Bias Resistor built-in Transistor)

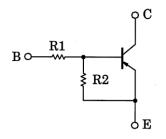
# RN2130MFV

Switching Applications
Inverter Circuit Applications
Interface Circuit Applications
Driver Circuit Applications

- Ultra-small package, suited to very high density mounting
- Incorporating a bias resistor into the transistor reduces the number of parts, so
  enabling the manufacture of ever more compact equipment and lowering assembly
  cost.
- A wide range of resistor values is available for use in various circuits.
- Complementary to the RN1130MFV

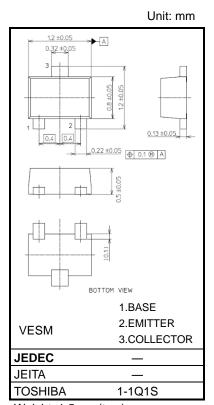
#### **Equivalent Circuit**

Note:



### Absolute Maximum Ratings (Ta = 25°C)

Characterisstic	Symbol	Rating	Unit	
Collector-base voltage	Vсво	-50	V	
Collector-emitter voltage	VCEO	-50	V	
Emitter-base voltage	V <sub>EBO</sub>	-10	V	
Collector current	Ic	-100	mA	
Collector power dissipation	Pc (Note1)	150	mW	
Junction temperature	Tj	150	°C	
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C	



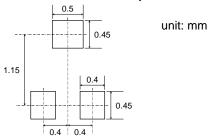
Weight: 1.5 mg (typ.)

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Mounted on FR4 board (25.4 mm × 25.4 mm × 1.6 mm)

#### Land Pattern Dimensions (for reference only)



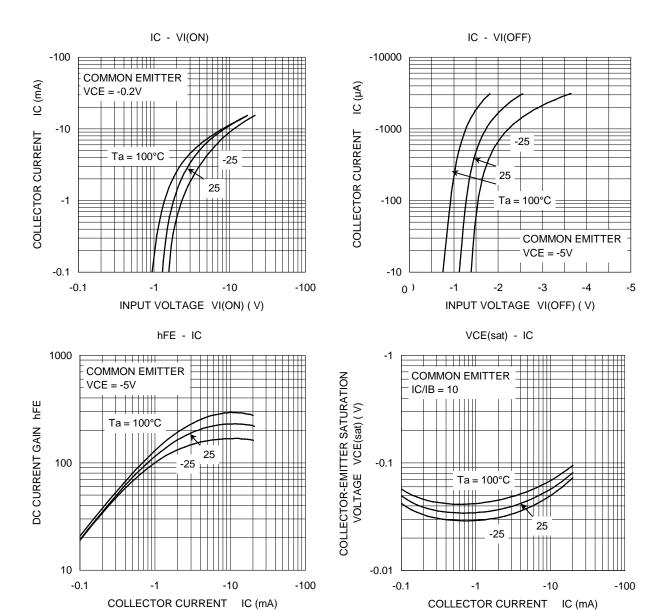
Start of commercial production 2005-04



### Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_{E} = 0 \text{ A}$	1	_	-100	nA
	ICEO	V <sub>CE</sub> = -50 V, I <sub>B</sub> = 0 A	_	_	-500	nA
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = -10 \text{ V}, I_{C} = 0 \text{ A}$	-38	_	-72	μΑ
DC current gain	hFE	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	100	_	_	_
Collector-emitter saturation voltage	VCE (sat)	$I_C = -5 \text{ mA}, I_B = -0.5 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V <sub>I(ON)</sub>	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-1.7	_	-8.2	V
Input voltage (OFF)	V <sub>I</sub> (OFF)	$V_{CE} = -5 \text{ V}, I_{C} = -0.1 \text{ mA}$	-1.0	_	-1.6	V
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_{E} = 0 \text{ A}, f = 1 \text{ MH}_{Z}$	_	0.9	_	pF
Input resistor	R1	_	70	100	130	kΩ
Resistor ratio	R1/R2	_	0.8	1.0	1.2	_







### Marking

Type Name	Marking	
RN2130MFV	Type Name Y 2	



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