TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# **TC7S66F, TC7S66FU**

#### **Bilateral Switch**

The TC7S66 is a high Speed  $C^2MOS$  Bilateral Switch fabricated with silicon gate  $C^2MOS$  technology.

It consists of a high speed switch capable of controlling either digital or analog signals while maintaining the  ${\rm C^2MOS}$  low power dissipation.

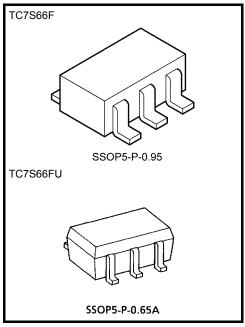
Control input (C) is provided to control the switch.

The switch turns ON while the C input is high, and the switch turns OFF while low.

Input is equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

- High speed:  $t_{pd} = 7 \text{ ns (typ.)} @V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 1 \mu A \text{ (max) } @Ta = 25 \text{°C}$
- High noise immunity:  $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (min)
- Low ON resistance:  $R_{ON} = 100 \Omega$  (typ.) @ $V_{CC} = 9 V$
- Low T.H.D: THD = 0.05% (typ.) @V<sub>CC</sub> = 5 V
- Pin and function compatible with TC4S66F



Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

### Absolute Maximum Ratings (Ta = 25°C)

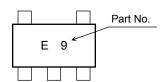
Characteristics	Symbol	Rating	Unit	
DC Supply voltage	$V_{CC}$	–0.5 to 13	V	
Control input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V	
Switch I/O voltage	V <sub>I/O</sub>	-0.5 to V <sub>CC</sub> + 0.5	V	
Control diode current	I <sub>CK</sub>	±20	mA	
I/O diode current	l <sub>IOK</sub>	±20	mA	
Through I/O current	Ι <sub>Τ</sub>	±12.5	mA	
DC V <sub>CC</sub> /ground current	ICC	±25	mA	
Power dissipation	P <sub>D</sub>	200	mW	
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

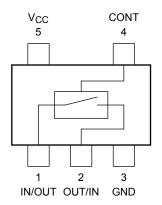
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 1991-06

#### Marking



## Pin Configuration (top view)



# **Logic Diagram**



#### **Truth Table**

Control	Switch Function
Н	ON
L	OFF

## **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub> 2 to 12		V
Control input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Switch I/O voltage	V <sub>I/O</sub>	0 to V <sub>CC</sub>	V
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time		0 to 1000 (V <sub>CC</sub> = 2.0 V)	ns
	4 4.	0 to 500 (V <sub>CC</sub> = 4.5 V)	
	t <sub>r</sub> , t <sub>f</sub>	0 to 400 (V <sub>CC</sub> = 6.0 V)	115
		0 to 250 (V <sub>CC</sub> = 10.0 V)	



#### **Electrical Characteristics**

#### **DC Electrical Characteristics**

Characteristics Symbol		Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit		
	'		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max		
				2.0	1.5	_	_	1.5	_	-
	High level	V		4.5	3.15	_	_	3.15	_	
	nigir ievei	VIHC	_	9.0	6.3	_	_	6.3	_	
Control input				12.0	8.4	_	_	8.4	_	V
voltage				2.0	_	_	0.5	_	0.5	V
	Low level	\/a		4.5	_	_	1.35	_	1.35	
	Low level	V <sub>ILC</sub>	_	9.0	_	_	2.7	_	2.7	
				12.0	_	_	3.6	_	3.6	
		$V_{IN} = V_{IHC}$ $V_{I/O} = V_{CC}$ to GND	VIN = VILIC	4.5	_	192	340	_	400	
			9.0	_	110	170	_	200	Ω	
ON resistance	Ron V <sub>IN</sub> = V <sub>IHC</sub>	I <sub>I/O</sub> ≤ 1 mA	12.0	_	90	160	_	180		
			2.0	_	320	_	_	_		
		4.5		140	200		260			
		V <sub>I/O</sub> = V <sub>CC</sub> or GND I <sub>I/O</sub> ≤ 1 mA	9.0		100	150		190		
			12.0		90	140		180		
Input/output leacurrent (switch		l <sub>OFF</sub>	$V_{OS} = V_{CC}$ or GND $V_{IS} = GND$ or $V_{CC}$ $V_{IN} = V_{ILC}$	12.0		_	±100		±1000	nA
Switch input leacurrent (switch on, out	_	lız	V <sub>OS</sub> = V <sub>CC</sub> or GND V <sub>IN</sub> = V <sub>IHC</sub>	12.0	_	_	±100	_	±1000	nA
Control input c	Control input current $I_{IN}$ $V_{IN} = V_{CC}$ or GND		12.0		_	±100	_	±1000	nA	
				6.0		_	1.0	_	10.0	
Quiescent devi	ce current	$I_{CC}$ $V_{IN} = V_{CC}$ or GND	9.0	-	_	4.0	_	40.0	μА	
					_	_	8.0	_	80.0	

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# AC Electrical Characteristics (C $_L$ = 50 pF, input $t_r$ = $t_f$ = 6 ns)

Characteristics	Symbol Test Condition			Ta = 25°C		Ta = -40 to 85°C		Unit	
	, ,		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
		_	2.0	_	20	75	_	100	- ns
Phase difference between	φI-O		4.5	_	7	15	_	20	
input and output	ΨΙ-Ο		9.0		4	12		15	
			12.0		4	11		14	
			2.0		20	150		190	
Output enable time	t <sub>pZL</sub>	R <sub>L</sub> = 1 kΩ	4.5		13	30		38	ns
Output enable time	t <sub>pZH</sub>	K[ = 1 K52	9.0		9	18		33	is is
			12.0		8	18		27	
	t <sub>pLZ</sub> t <sub>pHZ</sub>	$R_L = 1 \text{ k}\Omega$	2.0		40	170		220	ns - MHz
Output disable time			4.5		11	35	_	44	
			9.0		10	30		38	
			12.0		9	27		33	
	_	$R_L = 1 \text{ k}\Omega$ $C_L = 15 \text{ pF}$ $V_{OUT} = 1/2 \text{ V}_{CC}$	2.0		30			_	
Maximum control input			4.5		30			_	
frequency			9.0	_	30	_	_	_	
			12.0	_	30	_	_	_	
Control input capacitance	C <sub>IN</sub>	_			5	10	_	10	pF
Switch terminal capacitance	C <sub>I/O</sub>	_			6			_	pF
Feedthrough capacitance	C <sub>IOS</sub>	_			0.5	_	_		pF
Power dissipation capacitance	C <sub>PD</sub>		(Note)	_	15	_	_	_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

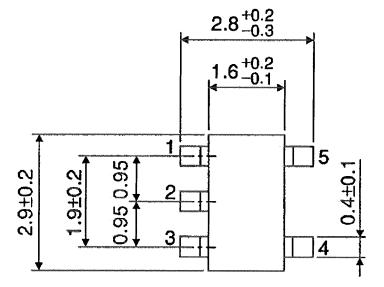
#### Analog Switch Characteristics (GND = 0 V, Ta = 25°C) (Note)

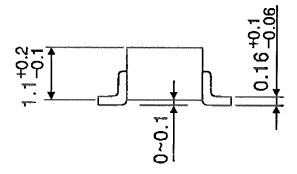
Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
	_	f <sub>IN</sub> = 1 kHz, V <sub>IN</sub> = 4 V <sub>p-p</sub> (V <sub>CC</sub> = 4.5 V)	4.5	0.05	%
Total harmonic distortion (T.H.D)		$R_L = 10 \text{ k}\Omega, V_{IN} = 8 \text{ V}_{p\text{-}p} \text{ (V}_{CC} = 9.0 \text{ V)}$ $C_L = 50 \text{ pF}$	9.0	0.04	
Maximum propagation frequency (switch on)	f <sub>MAX</sub>	Adjust f <sub>IN</sub> voltage to obtain 0dBm at V <sub>OS</sub> increase f <sub>IN</sub> frequency until dB meter reads	4.5	200	MHz
		-3dB. R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 10 pF f <sub>IN</sub> = 1 MHz, Sine wave	9.0	200	
Feedthrough (switch on)		$V_{IN}$ is centered at $V_{CC}/2$ adjust input for 0dBm $R_L=600~\Omega,~C_L=50~pF$ $f_{IN}=1~MHz,~Sine~wave$	4.5	-60	dB
			9.0	-60	uБ
Crosstalk (control switch)	_	$R_L = 600 \Omega$ , $C_L = 50 pF$	4.5	60	- mV
		$f_{IN} = 1$ MHz, Pulse ( $t_r = t_f = 6$ ns)	9.0	100	

Note: These characteristics are determined by design of devices.

#### **Package Dimensions**

SSOP5-P-0.95 Unit: mm



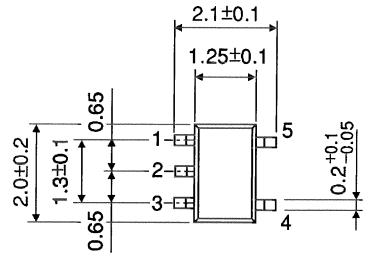


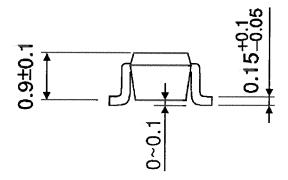
Weight: 0.016 g (typ.)

Unit: mm

#### **Package Dimensions**

SSOP5-P-0.65A





Weight: 0.006 g (typ.)

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