TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC02P, TC74AC02F, TC74AC02FT

Quad 2-Input NOR Gate

The TC74AC02 is an advanced high speed CMOS 2-INPUT NOR GATE fabricated with silicon gate and double-layer metal wiring C^2 MOS technology.

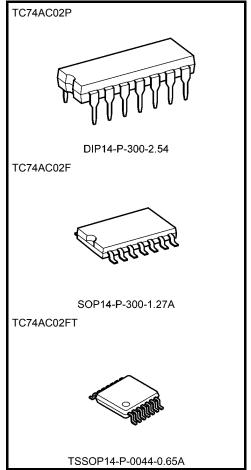
It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 3 stages including buffer output, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

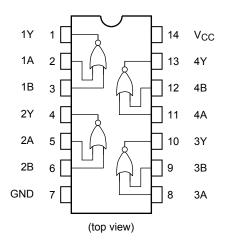
- High speed: $t_{pd} = 3.7 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A \text{ (max)}$ at $T_a = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24$ mA (min) Capability of driving 50 Ω transmission lines.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: $V_{CC (opr)} = 2 \text{ to } 5.5 \text{ V}$
- Pin and function compatible with 74F02



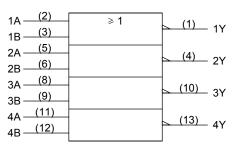
Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.)

Pin Assignment



IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	−0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	I _{CC}	±100	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.



Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0 to 5.5	V	
Input voltage	V _{IN}	0 to V _{CC}	V	
Output voltage	V _{OUT}	0 to V _{CC}	V	
Operating temperature	T _{opr}	−40 to 85	°C	
Input rise and fall time	dt/dV	0 to 100 ($V_{CC} = 3.3 \pm 0.3 \text{ V}$)	ns/V	
input rise and rail time	avav	0 to 20 ($V_{CC} = 5 \pm 0.5 \text{ V}$)	115/ V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition $\begin{array}{c} V_{CC} \\ (V) \end{array}$			Ta = 25°C			Ta = −40 to 85°C		Unit		
					V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
					2.0	1.50	_	_	1.50	_	V	
High-level input voltage	V_{IH}	_		3.0	2.10	_	_	2.10	_			
3.0					5.5	3.85	_	-	3.85	_		
					2.0	1	_	0.50	_	0.50		
Low-level input voltage	V_{IL}	_			3.0	_	_	0.90	_	0.90	V	
3.0						-	_	1.65	-	1.65		
			I _{OH} = -50 μA		2.0	1.9	2.0	_	1.9	_		
					3.0	2.9	3.0	_	2.9	_		
High-level output	V _{OH}	V _{IN}			4.5	4.4	4.5	-	4.4	_	V	
voltage	VOH	= V _{IL}	$I_{OH} = -4 \text{ mA}$		3.0	2.58	_	_	2.48	_	V	
			I _{OH} = -24 mA		4.5	3.94	_	_	3.80	_		
			$I_{OH} = -75 \text{ mA}$	(Note)	5.5	1	_	1	3.85	1		
		VIN = VIH or VIL			2.0		0.0	0.1	_	0.1		
			I _{OL} = 50 μA		3.0	_	0.0	0.1	_	0.1		
Low-level output voltage V _{OL}	Voi				4.5	1	0.0	0.1	1	0.1	1 V	
	VOL		I _{OL} = 12 mA		3.0	_	_	0.36	_	0.44	v	
			I _{OL} = 24 mA		4.5	_	_	0.36	_	0.44		
			I _{OL} = 75 mA	(Note)	5.5	_	_	_	_	1.65		
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND			5.5	l	1	±0.1	l	±1.0	μΑ	
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND			5.5	_	_	4.0	_	40.0	μA	

Note: This spec indicates the capability of driving 50 Ω transmission lines. One output should be tested at a time for a 10 ms maximum duration.

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AC Characteristics (CL = 50 pF, RL = 500 $\Omega,$ input: $t_{r}=t_{f}$ = 3 ns)

Characteristics Symbol	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay tpLH time tpHL	t _{pLH}		3.3 ± 0.3	_	6.1	9.8	1.0	11.2	20
	t _{pHL}	_	5.0 ± 0.5	_	4.8	7.0	1.0	8.0	ns
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)	_	82	ı	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

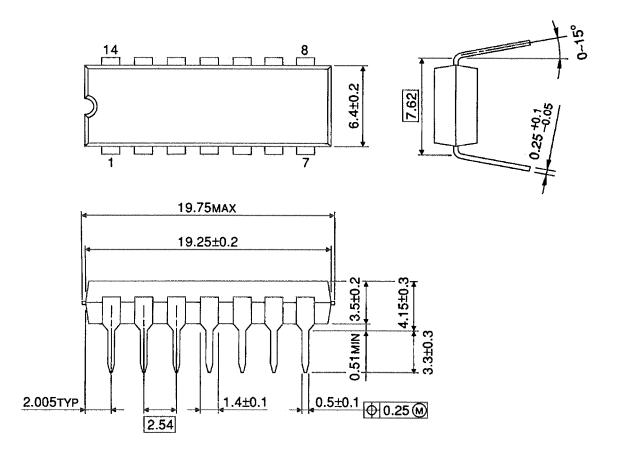
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$



Package Dimensions

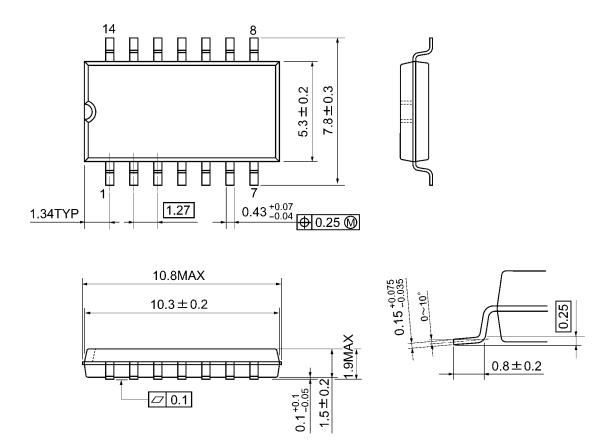
DIP14-P-300-2.54 Unit: mm



Weight: 0.96 g (typ.)

Package Dimensions

SOP14-P-300-1.27A Unit: mm

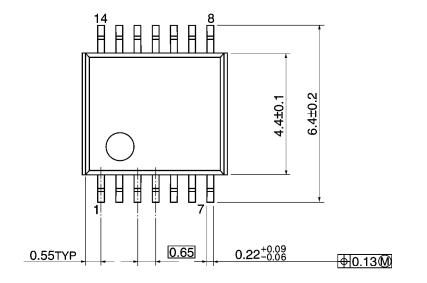


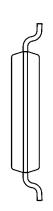
Weight: 0.18 g (typ.)

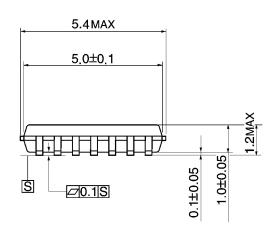
Package Dimensions

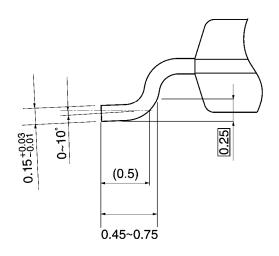
TSSOP14-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)

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2014-03-01

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