

MOSFETs Silicon N-channel MOS (U-MOSVIII-H)

# TPH8R008NH

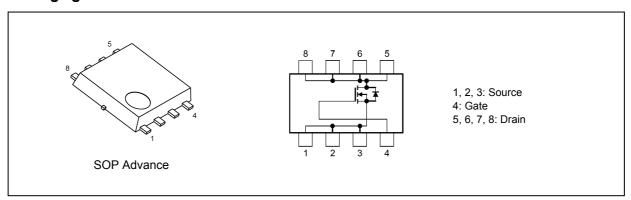
#### 1. Applications

- · DC-DC Converters
- Switching Voltage Regulators
- · Motor Drivers

#### 2. Features

- (1) Small, thin package
- (2) High-speed switching
- (3) Small gate charge:  $Q_{SW} = 13 \text{ nC (typ.)}$
- (4) Low drain-source on-resistance:  $R_{DS(ON)} = 6.6 \text{ m}\Omega$  (typ.) ( $V_{GS} = 10 \text{ V}$ )
- (5) Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 80 \text{ V)}$
- (6) Enhancement mode:  $V_{th} = 2.0 \text{ to } 4.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 0.5 \text{ mA})$

#### 3. Packaging and Internal Circuit



## 4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characterist	Symbol	Rating	Unit		
Drain-source voltage			$V_{DSS}$	80	V
Gate-source voltage			$V_{GSS}$	±20	
Drain current (DC)	(Silicon limit)	(Note 1), (Note 2)	I <sub>D</sub>	63	Α
Drain current (DC)	(T <sub>c</sub> = 25°C)	(Note 1)	Ι <sub>D</sub>	34	
Drain current (pulsed)	(t = 1 ms)	(Note 1)	I <sub>DP</sub>	147	
Power dissipation	(T <sub>c</sub> = 25°C)		$P_D$	61	W
Power dissipation	(t = 10 s)	(Note 3)	$P_{D}$	2.8	W
Power dissipation	(t = 10 s)	(Note 4)	$P_{D}$	1.6	W
Single-pulse avalanche energy		(Note 5)	E <sub>AS</sub>	94	mJ
Avalanche current			I <sub>AR</sub>	34	Α
Channel temperature			T <sub>ch</sub>	150	°C
Storage temperature			T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production

2012-05

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#### 5. Thermal Characteristics

Characterist	Symbol	Max	Unit		
Channel-to-case thermal resistance	$(T_c = 25^{\circ}C)$		R <sub>th(ch-c)</sub>	2.04	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R <sub>th(ch-a)</sub>	44.6	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 4)	R <sub>th(ch-a)</sub>	78.1	°C/W

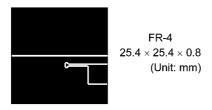
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Limited by silicon chip capability. Package limit is 60 A.

Note 3: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 4: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 5:  $V_{DD}$  = 60 V,  $T_{ch}$  = 25°C (initial), L = 0.069 mH,  $R_{G}$  = 1.0  $\Omega$ ,  $I_{AR}$  = 34 A



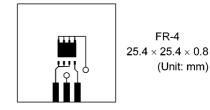


Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a)

Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



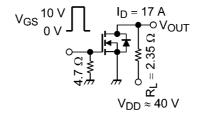
#### 6. Electrical Characteristics

## 6.1. Static Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μА
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	80			V
	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	60			
Gate threshold voltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 mA	2.0	_	4.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 17 A	_	6.6	8.0	mΩ

### 6.2. Dynamic Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	2300	3000	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	23	50	
Output capacitance	C <sub>oss</sub>		_	520	_	
Gate resistance	r <sub>g</sub>		_	1.0	1.5	Ω
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1.	_	6.0	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	18	_	
Switching time (fall time)	t <sub>f</sub>	]	_	12	_	
Switching time (turn-off time)	t <sub>off</sub>			36	_	



 $\label{eq:defDuty} \mbox{Duty} \leq 1\%, \, t_{\mbox{\scriptsize W}} = 10 \,\, \mu \mbox{\scriptsize S}$  Fig. 6.2.1 Switching Time Test Circuit

# 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 40 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 34 \text{ A}$	-	35		nC
Gate-source charge 1	Q <sub>gs1</sub>		_	12	_	
Gate-drain charge	$Q_{gd}$		_	8.1		
Gate switch charge	Q <sub>SW</sub>		_	13	_	

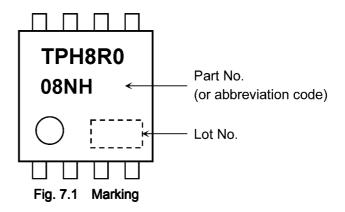
### 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed)	(Note 6)	I <sub>DRP</sub>	_	_	_	147	Α
Diode forward voltage		$V_{DSF}$	I <sub>DR</sub> = 34 A, V <sub>GS</sub> = 0 V			-1.2	V

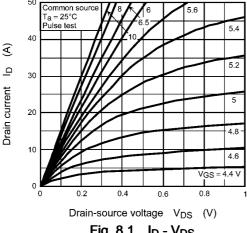
Note 6: Ensure that the channel temperature does not exceed 150°C.

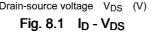


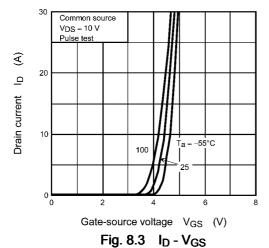
## 7. Marking

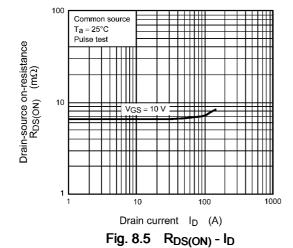


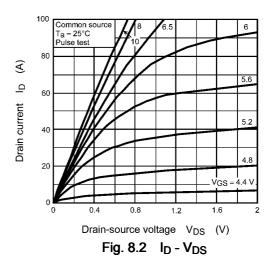
#### 8. Characteristics Curves (Note)

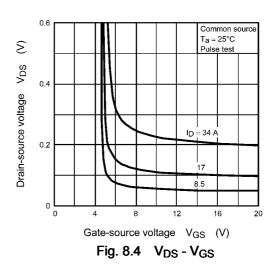












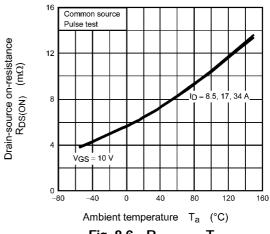


Fig. 8.6 R<sub>DS(ON)</sub> - T<sub>a</sub>

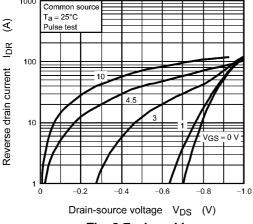


Fig. 8.7 IDR - VDS

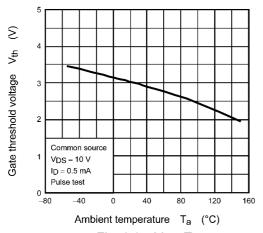


Fig. 8.9 V<sub>th</sub> - T<sub>a</sub>

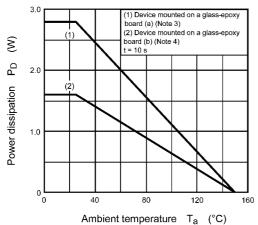


Fig. 8.11 PD - Ta

(Guaranteed Maximum)

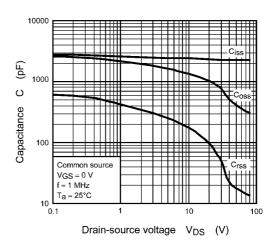


Fig. 8.8 Capacitance - V<sub>DS</sub>

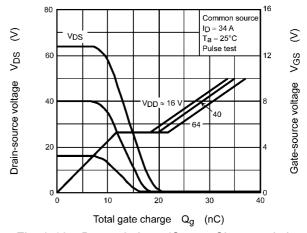


Fig. 8.10 Dynamic Input/Output Characteristics

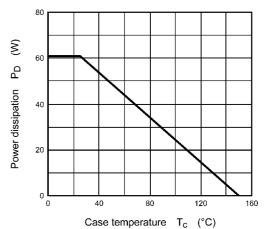


Fig. 8.12 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

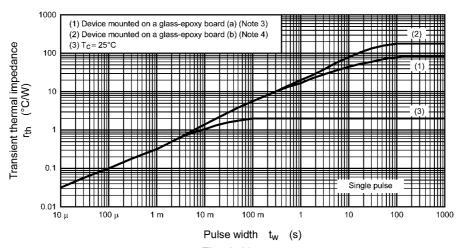


Fig. 8.13 r<sub>th</sub> - t<sub>w</sub> (Guaranteed Maximum)

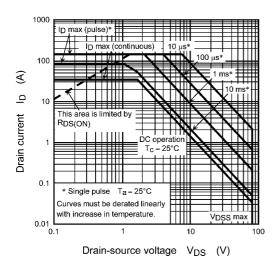


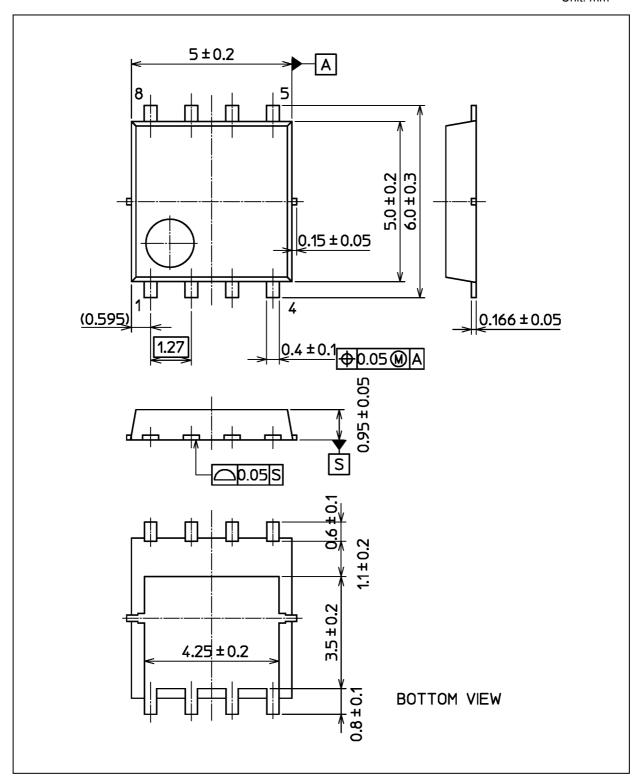
Fig. 8.14 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 0.069 g (typ.)

Package Name(s)	
TOSHIBA: 2-5Q1S	
Nickname: SOP Advance	



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