

MOSFETs Silicon N-channel MOS (U-MOSIV)

TK80S06K3L

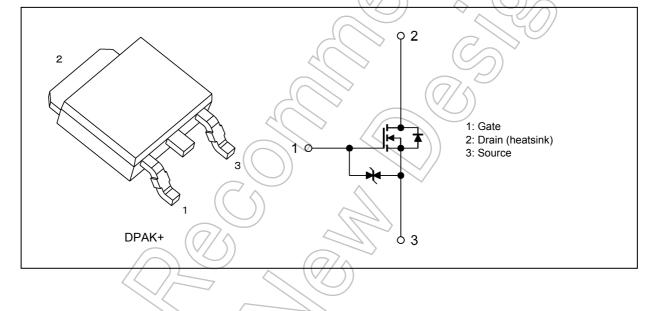
1. Applications

- Automotive
- · Motor Drivers
- DC-DC Converters
- · Switching Voltage Regulators

2. Features

- (1) AEC-Q101 qualified
- (2) Low drain-source on-resistance: $R_{DS(ON)} = 4.4 \text{ m}\Omega$ (typ.) ($V_{GS} = 10 \text{ V}$)
- (3) Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 60 \text{ V)}$
- (4) Enhancement mode: $V_{th} = 2.0$ to 3.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

3. Packaging and Internal Circuit



Start of commercial production



4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	60	V
Gate-source voltage		V _{GSS}	±20	
Drain current (DC)	(Note 1)	I _D	80	Α
Drain current (pulsed)	(Note 1)	I _{DP}	160	
Power dissipation (T _c =	= 25°C)	P_{D}	100	W
Single-pulse avalanche energy	(Note 2)	E _{AS}	94	mJ
Avalanche current		IAR	80	Α
Channel temperature	(Note 3)	T _{ch})) 175	ç
Storage temperature	(Note 3)	T _{stg}	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

	Characteristics		Symbol	Max	Unit
Channel-to-case thermal resistance			R _{th(ch-c)}	1.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 20 μ H, R_{G} = 1 Ω , I_{AR} = 80 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are qualified per AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-off current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	7	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	60	_	_	V
	$V_{(BR)DSX}$	I _D = 10 mA, V _{GS} = -20 V	40) ~		
Gate threshold voltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	/_	3.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 6 V, I _D = 40 A	/	5.0	7.8	mΩ
		V _{GS} = 10 V, I _D = 40 A	\mathcal{L}	4.4	5.5	

6.2. Dynamic Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		4200		pF
Reverse transfer capacitance	C _{rss}	((// \(\) \(\) \(\)	_((400	_	
Output capacitance	C _{oss}		K	650) —	
Switching time (rise time)	t _r	See Figure 6.2.1.		13	_	ns
Switching time (turn-on time)	t _{on}			28	_	
Switching time (fall time)	t _f		//-//	17		
Switching time (turn-off time)	t _{off}		$\widetilde{\wedge}$	80	_	

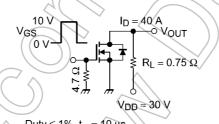


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Q _g	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 80 \text{ A}$		85	_	nC
Gate-source charge	Q_{gs}			55	_	
Gate-drain charge	Q_{gd}			30	_	

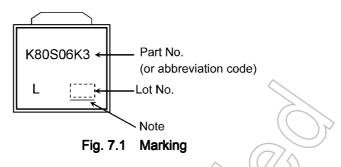
6.4. Source-Drain Characteristics (T_a = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I _{DR}	_	_	_	80	Α
Reverse drain current (pulsed)	(Note 4)	I _{DRP}	_	_	_	160	
Diode forward voltage		V _{DSF}	I _{DR} = 80 A, V _{GS} = 0 V	_	_	-1.2	V
Reverse recovery time		t _{rr}	I _{DR} = 80 A, V _{GS} = 0 V	_	61	_	ns
Reverse recovery charge		Q _{rr}	-dI _{DR} /dt = 50 A/μs	_	62	_	nC

Note 4: Ensure that the channel temperature does not exceed 175°C.



7. Marking (Note)



Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

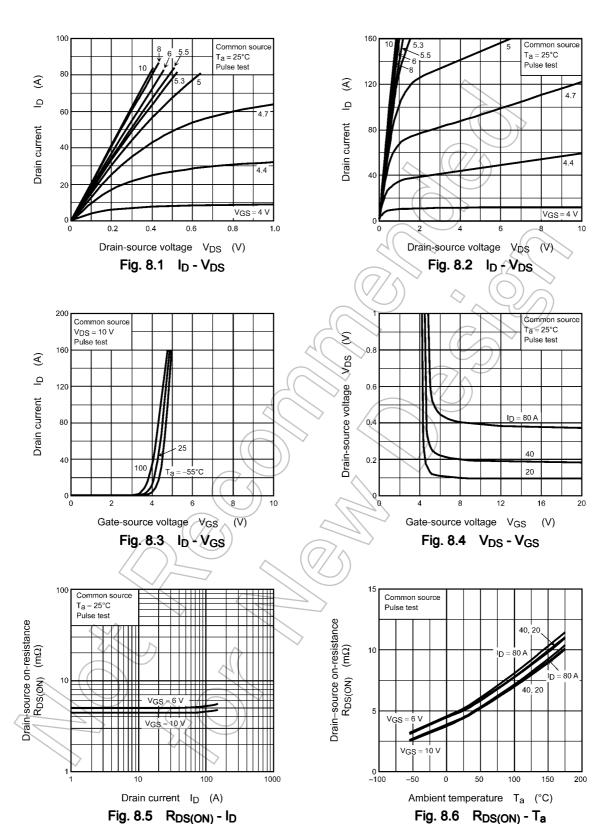
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



8. Characteristics Curves (Note)



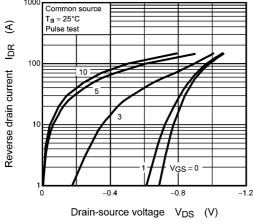


Fig. 8.7 I_{DR} - V_{DS}

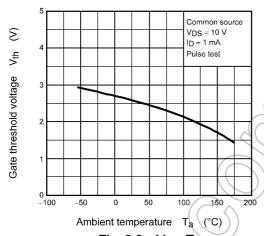


Fig. 8.9 V_{th} - T_a

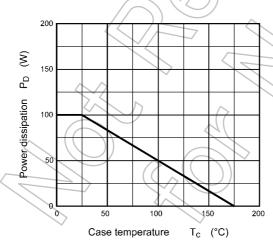


Fig. 8.11 P_D - T_c (Guaranteed Maximum)

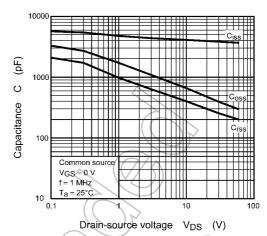


Fig. 8.8 Capacitance - V_{DS}

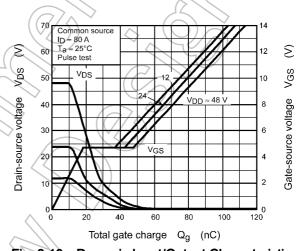
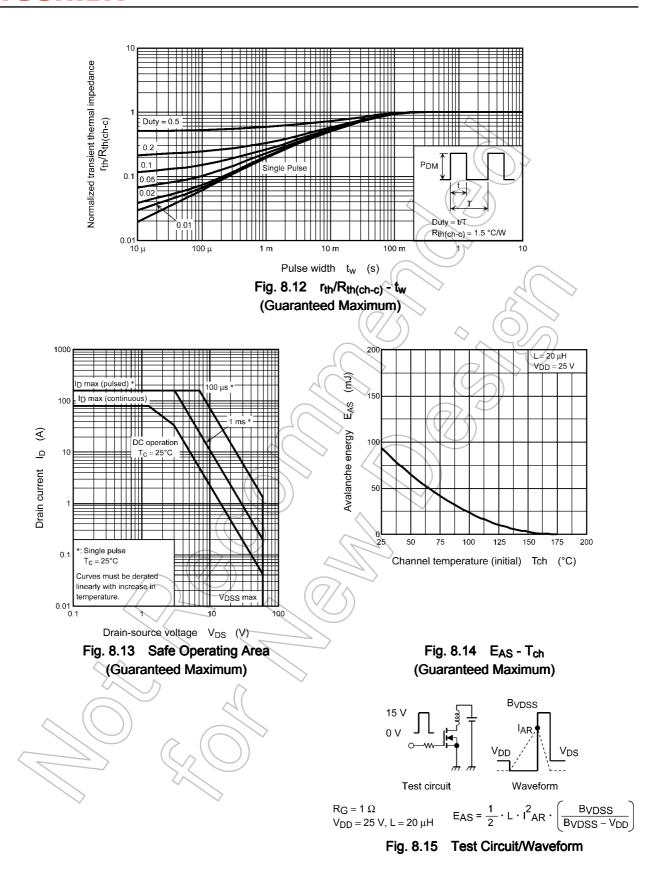


Fig. 8.10 Dynamic Input/Output Characteristics

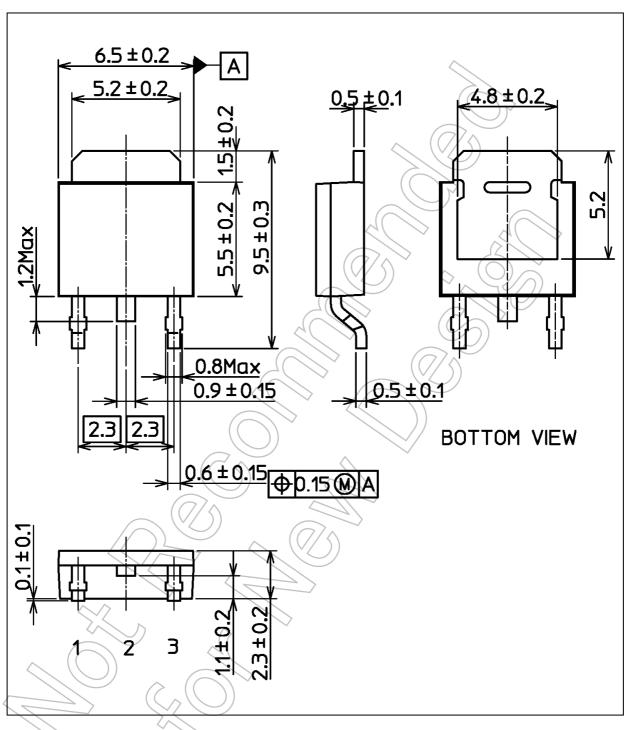


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.36 g (typ.)

	Package Name(s)
TOSHIBA: 2-7M1A	
Nickname: DPAK+	



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