TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

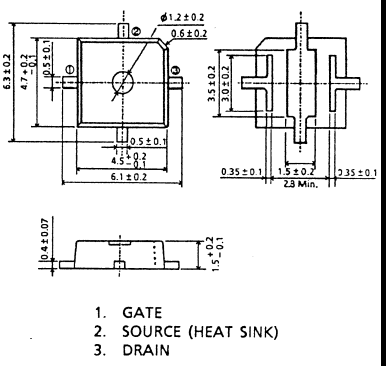
2SK4037

470 MHz Band Amplifier Applications

(Note)The TOSHIBA products listed in this document are intended for high frequency Power Amplifier of telecommunications equipment. These TOSHIBA products are neither intended nor warranted for any other use. Do not use these TOSHIBA products listed in this document except for high frequency Power Amplifier of telecommunications equipment.

- Output power: P_o = 36.5dBmW (typ)
- Gain: G_p = 11.5dB (typ)
- Drain Efficiency: ηD = 60.0% (typ)

Unit: mm



1. GATE
2. SOURCE (HEAT SINK)
3. DRAIN

JEDEC	—
JEITA	—
TOSHIBA	2-5N1A

Weight: 0.08 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

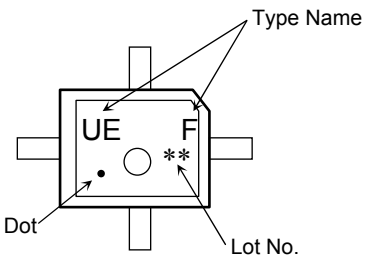
Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	12	V
Gate-source voltage	V_{GSS}(Note 1)	3	V
Drain current	I_D	3	A
Power dissipation	P_D (Note 2)	20	W
Channel temperature	T_{ch}	150	°C
Storage temperature range	T_{stg}	−45 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Operating Ranges: 0 to 3V

Note 2: Tc = 25°C (When mounted on a 0.8 mm glass epoxy PCB)

Marking



Caution: This device is sensitive to electrostatic discharge.

Please make enough tool and equipment earthed when you handle.

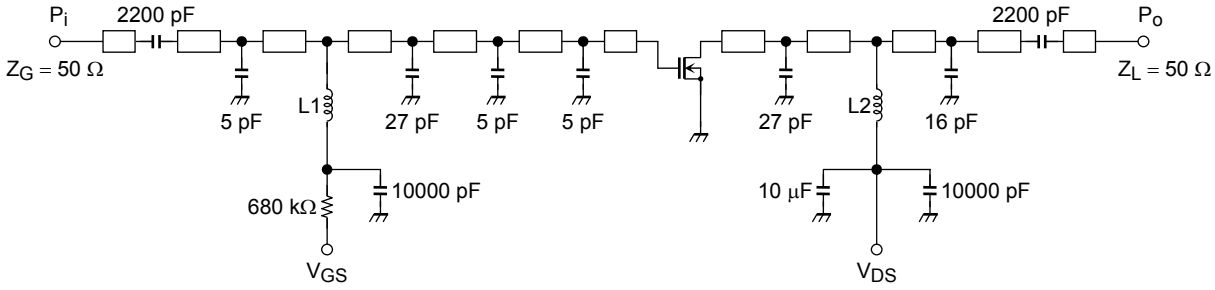
Start of commercial production
2005-01

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output power	P_O	V_{DS} = 6.0 V, Iidle = 250 mA (V_{GS} = adjust) f = 470 MHz, P_i = 25dBmW $Z_G = Z_L$ = 50 Ω	35.5	36.5	—	dBmW
Drain efficiency	η_D		55.0	60.0	—	%
Power gain	G_p		10.5	11.5	—	dB
Threshold voltage	V_{th}	V_{DS} = 6.0 V, I_D = 0.5 mA	—	1.0	1.5	V
Drain cut-off current	I_{DSS}	V_{DS} = 12 V, V_{GS} = 0 V	—	—	10	μA
Gate-source leakage current	I_{GSS}	V_{GS} = 3V, V_{DS} = 0 V	—	—	5	μA
Load mismatch (Note 3)	—	V_{DS} = 6.0 V, f = 470 MHz, P_i = 25dBmW, P_o = 36.5dBmW (V_{GS} = adjust) VSWR LOAD 10:1 all phase	No degradation			—

Note 3: These characteristic values are measured using measurement tools specified by Toshiba.

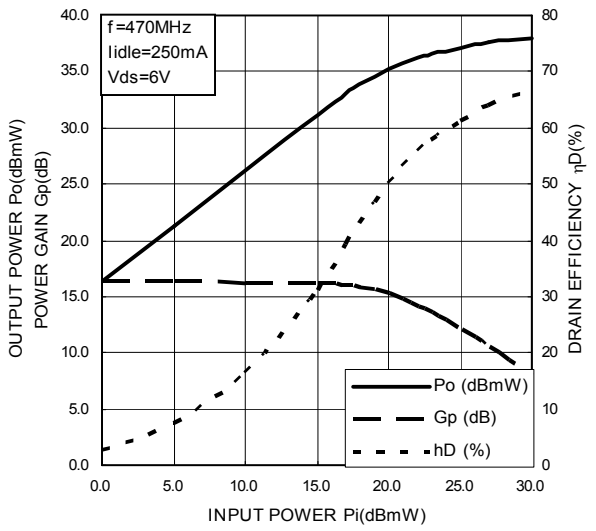
Test Circuit



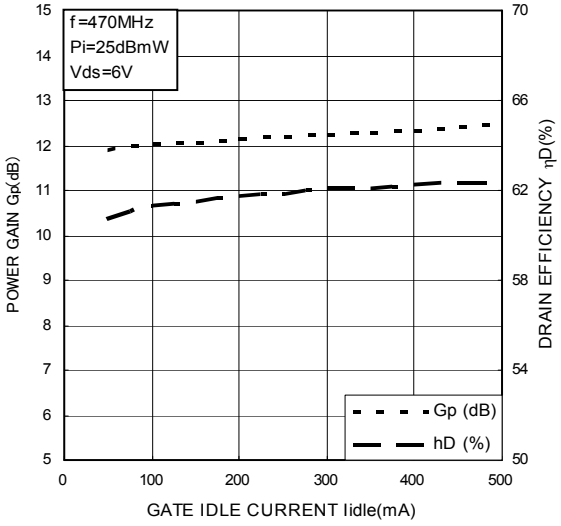
L1: ϕ0.6 mm enamel wire, 5.8ID, 8T
L2: ϕ0.6 mm enamel wire, 5.8ID, 8T

Line: 2mm

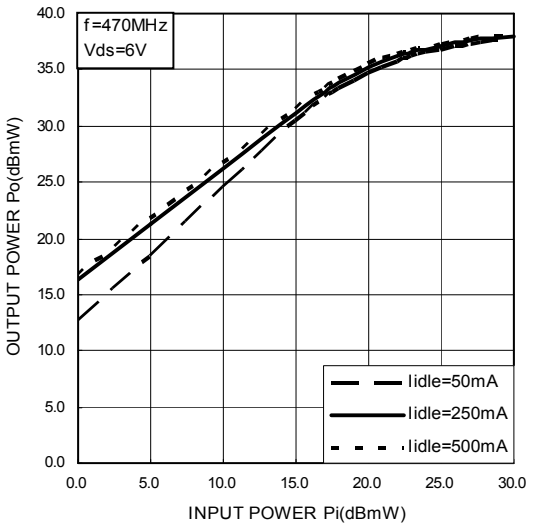
Po, Gp, ηD -Pi



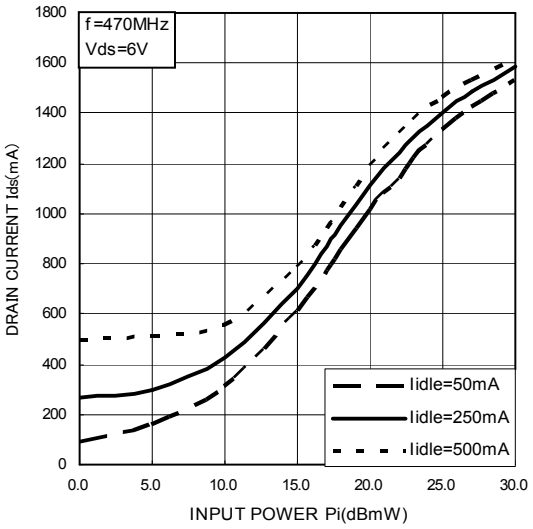
Gp, ηD -Iidle



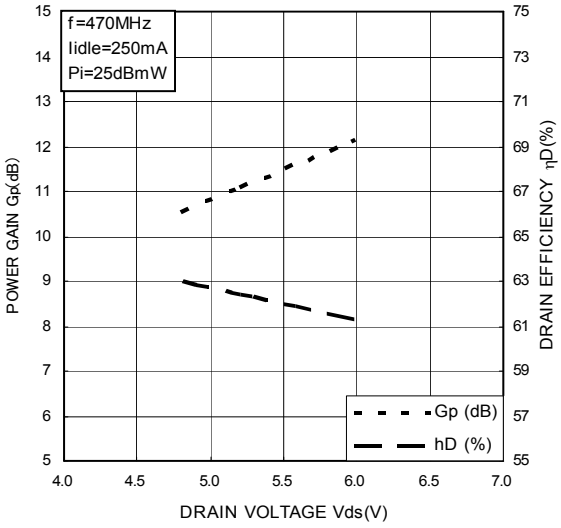
Po-Pi



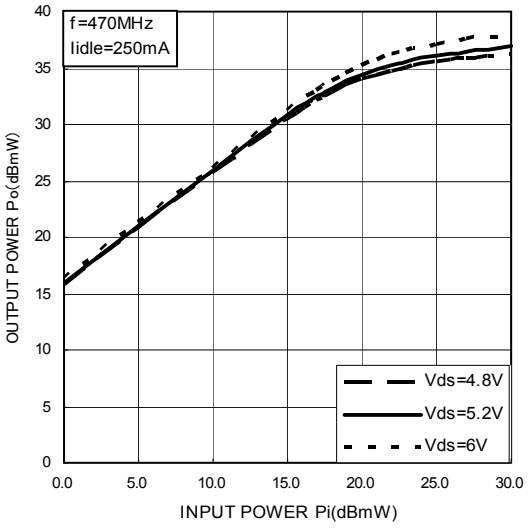
Pi-Ids

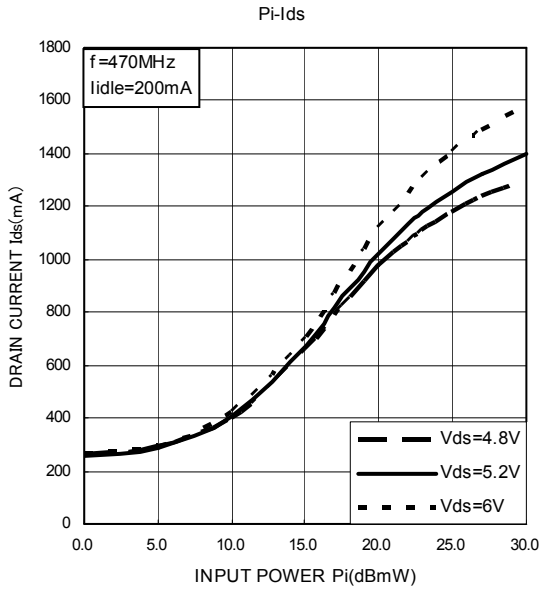


Gp, ηD -Vds



Po-Pi





Note 4: These are only typical curves and devices are not necessarily guaranteed at these curves.

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