TOSHIBA Field Effect Transistor Silicon N Channel MOS Type  $(\pi - MOSVII)$ 

# **TK13A45D**

#### **Switching Regulator Applications**

Low drain-source ON-resistance:  $R_{DS}$  (ON) = 0.38 Ω (typ.)

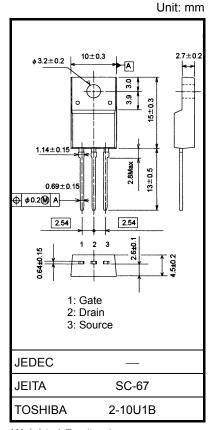
High forward transfer admittance: |Y<sub>fS</sub>| = 6.0 S (typ.)

• Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 450 \text{ V)}$ 

• Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	450	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	13		
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	52	Α	
Drain power dissipation (Tc = 25°C)		PD	45	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	372	mJ	
Avalanche current		I <sub>AR</sub>	13	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	4.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

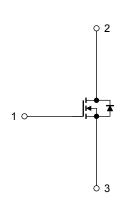
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 3.67 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



Start of commercial production 2008-11



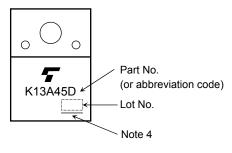
## **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 450 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source brea	akdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	450	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	-resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A		0.38	0.46	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6.5 A	1.5	6.0	_	S
Input capacitance		C <sub>iss</sub>			1350	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		6	_	pF
Output capacitance		Coss			135	_	
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c c} 10 \text{ V} & I_D = 6.5 \text{ A} & V_{OUT} \\ \hline V_{GS} & \\ \hline 0 \text{ V} & \\ \hline 50  \Omega & \\ \hline \end{array} \\ \begin{array}{c} R_L = 30  \Omega \\ \hline \end{array} \\ \begin{array}{c} V_{DD} \approx 200 \text{ V} \\ \hline \end{array} \\ \begin{array}{c} Duty \leq 1\%, \ t_W = 10  \mu s \end{array}$	_	22	_	. ns
	Turn-on time	t <sub>on</sub>			55	_	
	Fall time	t <sub>f</sub>			15		
	Turn-off time	t <sub>off</sub>		_	100	_	
Total gate charge		Qg		_	25	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 360 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	16	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	9	_	

## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	13	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	52	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 13 \text{ A}, V_{GS} = 0 \text{ V},$	_	1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs	_	12	_	μС

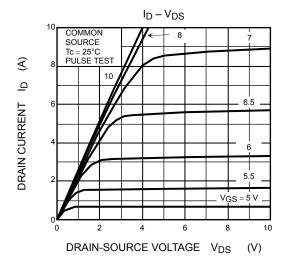
## Marking

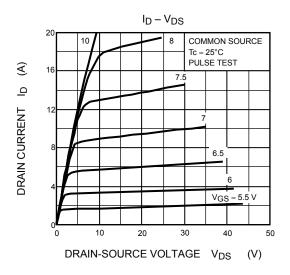


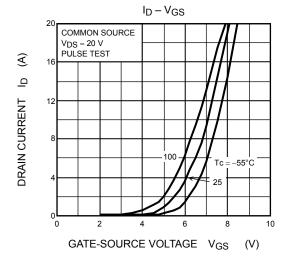
Note 4 : A line under a Lot No. identifies the indication of product Labels  $\hbox{[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$ 

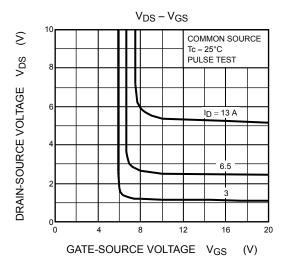
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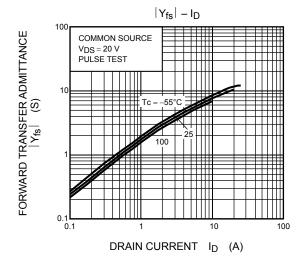
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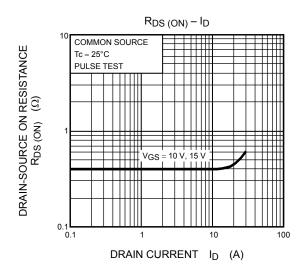


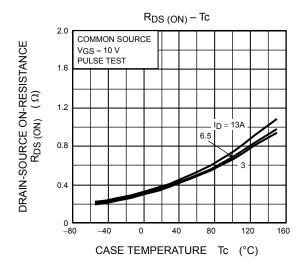


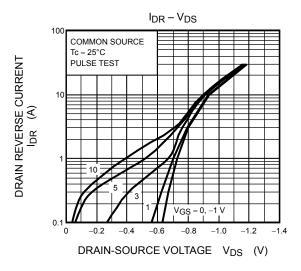


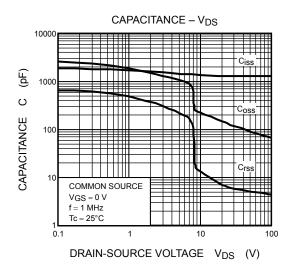


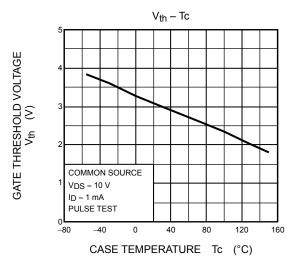


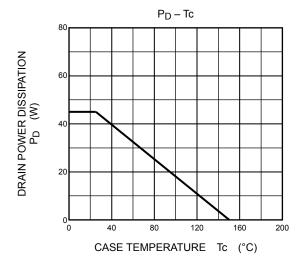


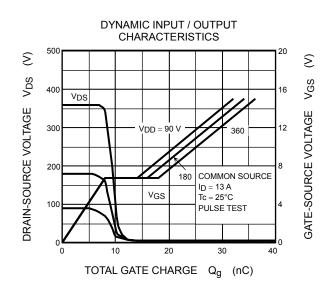


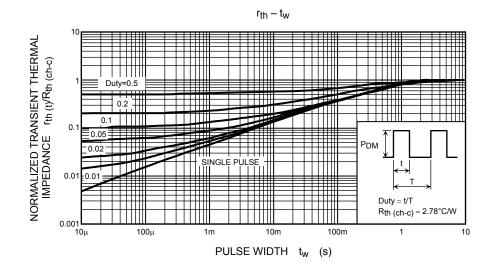


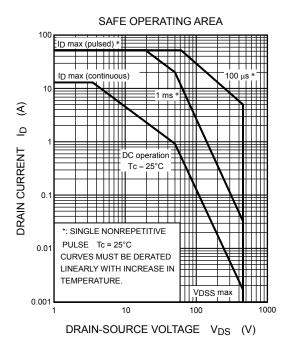


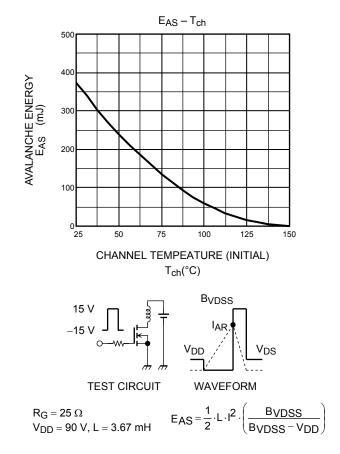












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