TOSHIBA Photocoupler IRED + Photo IC

TLP351

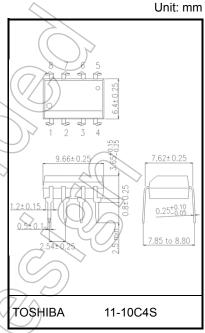
Inverter for Air Conditioner IGBT/Power MOS FET Gate Drive Industrial Inverter

The TOSHIBA TLP351 consists of an infrared emitting diode and an integrated photodetector.

This unit is 8-lead DIP package.

TLP351 is suitable for gate driving circuit of IGBT or power MOS FET. Especially TLP351 is capable of "direct" gate drive of lower Power IGBTs.

- Peak output current: ±0.6 A (max)
- Guaranteed performance over temperature: -40 to 100 °C
- Supply current: 2 mA (max)
- Power supply voltage: 10 to 30 V
- Threshold input current : $I_F = 5 \text{ mA (max)}$
- Switching time (t_{pLH}/t_{pHL}): 700 ns (max)
- Common mode transient immunity: ±10 kV/μs
- Isolation voltage: 3750 Vrms
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)



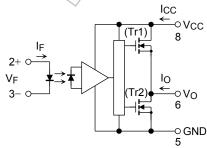
Weight: 0.54 g (typ.)

Note 1: When a VDE approved type is needed, please designate the **Option(D4)**.

Truth Table

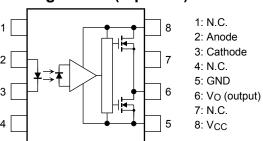
	_ ^			
Input	LED	∑)Tr1	Tr2	Output
Н	ON	ON	OFF	Н
4	OFF	OFF	ON	L

Schematic



A 0.1 μF bypass capacitor must be connected between pin 8 and 5.

Pin Configuration (top view)



Start of commercial production 2002-05

Absolute Maximum Ratings (Ta = 25°C)

	Characteristics		Symbol	Rating	Unit
	Forward current	lF	20	mA	
	Forward current derating (Ta ≥ 85°C)	ΔΙϝ/ΔΤα	-0.54	mA/°C	
	Peak transient forward current	IFP	1	<\A	
Ē	Reverse voltage		V_{R}	5	V
	Power Dissipation	PD	40	mW	
	Power Dissipation Derating (Ta ≥ 85°C)	ΔP _D /°C	-1.0	mW/°C	
	Junction temperature	Tj	125	(°G)	
	"H" peak output current	(Note 2)	IOPH	-0.6	A
	"L" peak output current	(Note 2)	IOPL	0.6) A
ō	Output voltage		Vo	35	V
Detector	Supply voltage		Vcc	35	V
ă	Output Power Dissipation	Po	260	mW	
	Output Power Dissipation Derating (Ta ≥ a	ΔP _O /°C	6.5	mW/°C	
	Junction temperature		Ţi	125	°C/
Oper	rating frequency	(Note 3)	(f)	25	kHz
Stora	age temperature range	Tstg	−55 to 125	~;c//	
Oper	rating temperature range	Topr	-40 to 100	∕\°C	
Lead	soldering temperature (10 s)	T _{sol}	260	//°C	
Isola	tion voltage (AC, 60 s, R.H. ≤ 60 %)	(Note 5)	BVS	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Pulse width PW ≤ 1 µs, 300 pps
- Note 2: Exponential waveform pulse width PW \leq 10 μ s, f \leq 15 kHz
- Note 3: Exponential waveform IOPH ≤ -0.4 A (≤ 2.0 μ s), IOPL $\leq +0.4$ A (≤ 2.0 μ s), Ta = 100 °C
- Note 4: It is 2 mm or more from a lead root.
- Note 5: Device considered a two terminal device: pins 1, 2, 3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.

Recommended Operating Conditions

Characteristics	()	Symbol	Min	Тур.	Max	Unit
Input current, ON	(Note 7)	IF (ON)	7.5	_	10	mA
Input voltage, OFF	~ / /	VF (OFF)	0	_	0.8	٧
Supply voltage		Vcc	10	1	30	>
Peak output current		IOPH/IOPL	_	_	±0.2	Α
Operating temperature		T _{opr}	-40	_	100	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 7: Input signal rise time (fall time) $< 0.5 \mu s$

Electrical Characteristics (Ta = -40 to 100°C, unless otherwise specified)

Characteristics		Symbol	Test Circuit	Test Co	ondition	Min	Тур.*	Max	Unit
Forward voltage		VF	_	$I_F = 5 \text{ mA}, \text{ Ta} = 25 ^{\circ}\text{C}$		_	1.55	1.70	V
Temperature coefficient of forward voltage		ΔV _F /ΔTa	_	IF = 5 mA		_	-2.0	_	mV/°C
Input reverse current		IR	_	V _R = 5 V, Ta = 25	°C	7/	_	10	μΑ
Input capacitance		Ст	_	V = 0 V , f = 1 MH	z,Ta = 25 °C	(-)	45	_	pF
	"I I" I aval	IOPH1	1	V _{CC} = 15 V	V ₈₋₆ = 4 V		-0.4	-0.2	
Output current	"H" Level	I _{OPH2}		$I_F = 5 \text{ mA}$	V ₈₋₆ = 10 V) –	-0.67	-0.4	A
(Note 8)	"L" Level	IOPL1	2	V _{CC} = 15 V I _F = 0 mA	V ₆₋₅ = 2 V	0.2	0.35	_	
		IOPL2	_		V ₆₋₅ = 10 V	0.4	0.63	_	
Output valtage	"H" Level	Vон	3	Vac = 10 V	10 = -100 mA, IF = 5 mA	6.0	8.5	-	V
Output voltage	"L" Level	VoL	4	VCC = 10 V	I _O = 100 mA, V _F = 0.8 V	4	0.4	1.0	V
Complete	"H" Level	Іссн	5	V _{CC} = 10 to 30 V V _O open]F = 10 mA <	(\mathcal{Q})	14	2.0	Λ
Supply current	"L" Level	ICCL	6		IF = 0 mA	1-5	1,3	2.0	mA
Threshold input current	L → H	lFLH	_	V _C C = 15 V, V _O > 1 V			2.5	5	mA
Threshold input voltage	H → L	VFHL	_	Vcc = 15 V, Vo < 1 V		0.8	_	_	V
Supply voltage	Vcc	-((- (7/5)	10	_	30	V	

^{*:} All typical values are at Ta = 25°C

Note 8: Duration of IO time \leq 50 μ s

Note 9: This product is more sensitive than the conventional product to static electricity (ESD) because of a lowest power consumption design.

General precaution to static electricity (ESD) is necessary for handling this component.

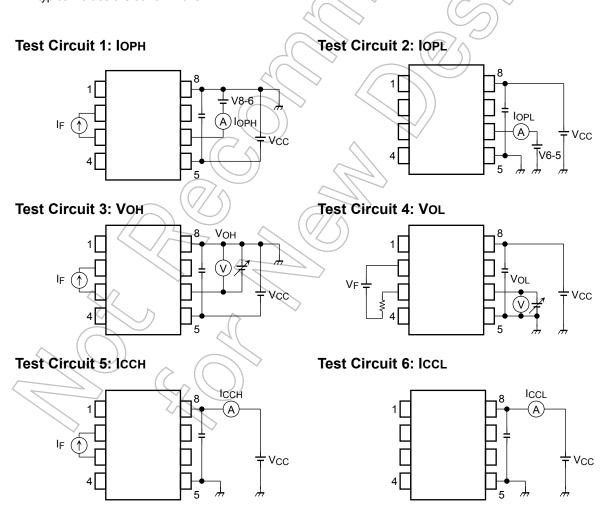
Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Conditions		Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0V, f = 1MHz	(Note5)	-	1.0	-	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60 %	(Note5)	1×10 ¹²	10 ¹⁴		Ω
Isolation voltage	BVs	AC,60 s		3750			V _{rms}

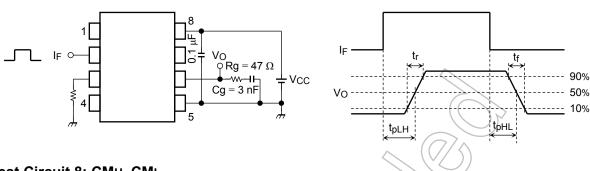
Switching Characteristics (Ta = -40 to 100°C, unless otherwise specified)

Characteristics		Symbol	Test Circuit	Test Condition		Min	Тур.*	Max	Unit
	L → H	tpLH	,	V _{CC} = 30 V	$I_F = 0 \rightarrow 5 \text{ mA}$	100	_	700	ns
Propagation delay time	H → L	t _{pHL}		$R_g = 47 \Omega$ $C_g = 3 \text{ nF}$ $I_F = 5 \rightarrow 0 \text{ mA}$	$I_F = 5 \rightarrow 0 \text{ mA}$	100	_	700	
Propagation delay difference between any two parts or channels		PDD t _{pHL} -t _{pLH}	7	$V_{CC} = 30 \text{ V},$ $R_g = 47 \Omega$ $C_g = 3 \text{ nF}$	< (C	-500) -	500	ns
Output rise time (10-90%)		tr		V _{CC} = 30 V	$I_F = 0 \rightarrow 5 \text{ mA}$		50	-	
Output fall time (90-10%)		tf		$R_g = 47 \Omega$ $C_g = 3 nF$	IF=5 → 0 mA	_	50		ns
Common mode transient immunity at high level output Common mode transient immunity at low level output		СМН		V _{CM} = 1000 V _p -p	IF = 5 mA VO (min) = 26 V	-10000		//	\// -
		CML	8	Ta = 25 °C V _{CC} = 30 V	IF = 0 mA VO (max) = 1 V	10000		_	V/μs

^{*:} All typical values are at Ta = 25°C

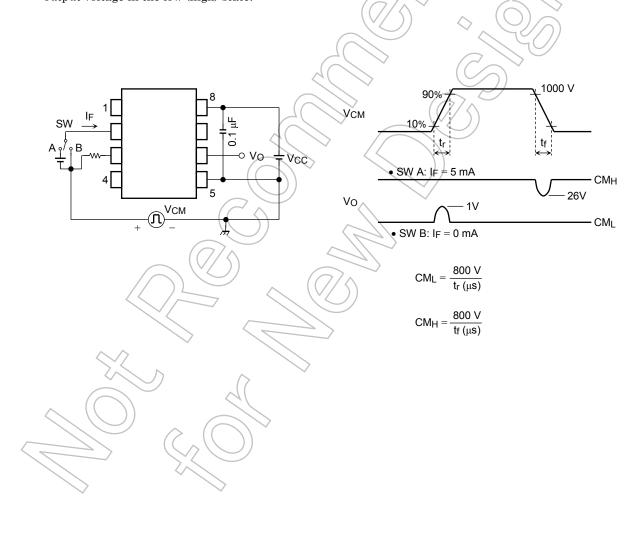


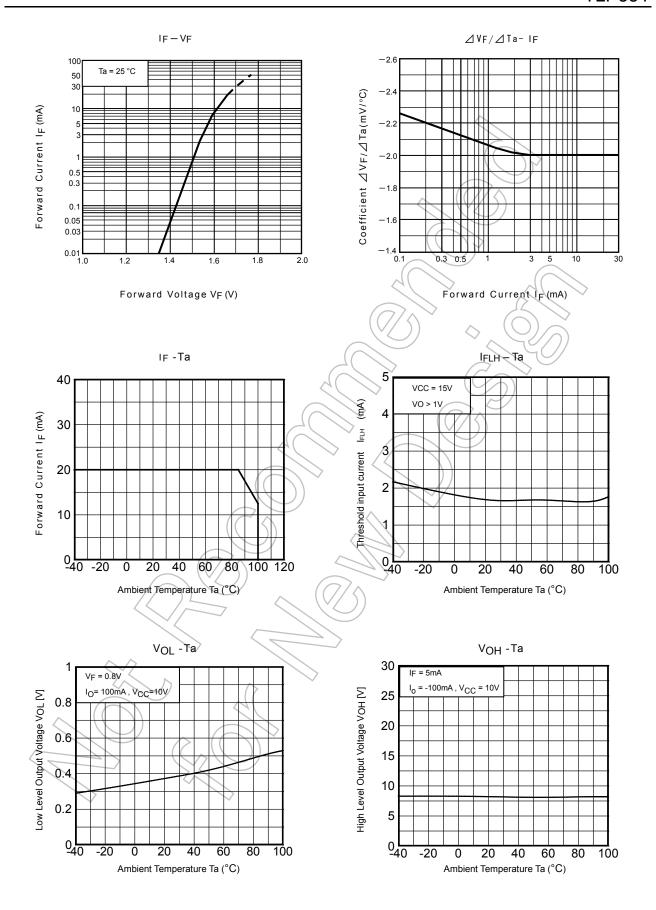
Test Circuit 7: tpLH, tpHL, tr, tf, PDD



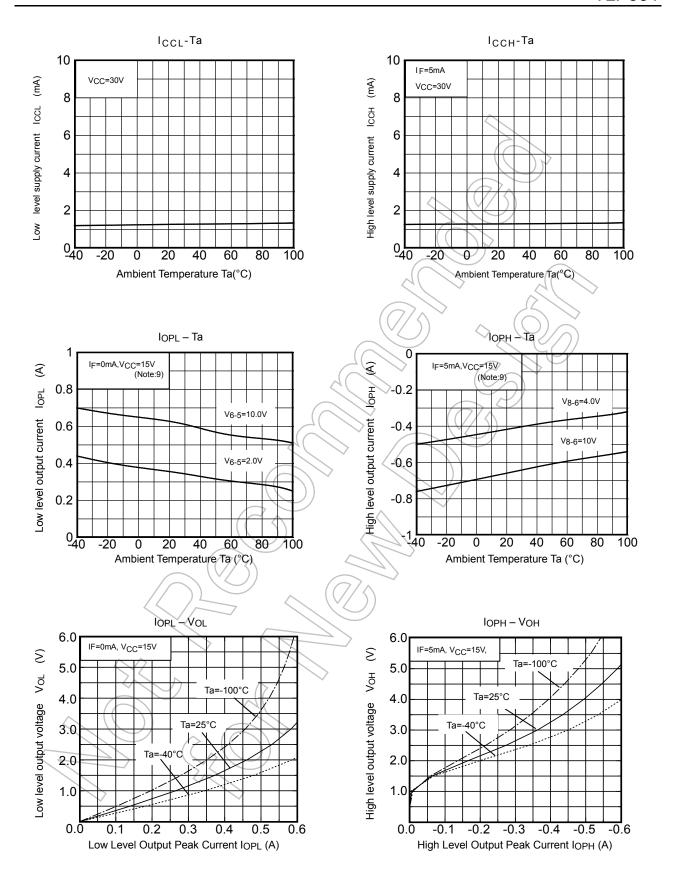
Test Circuit 8: CMH, CML

CML (CMH) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.

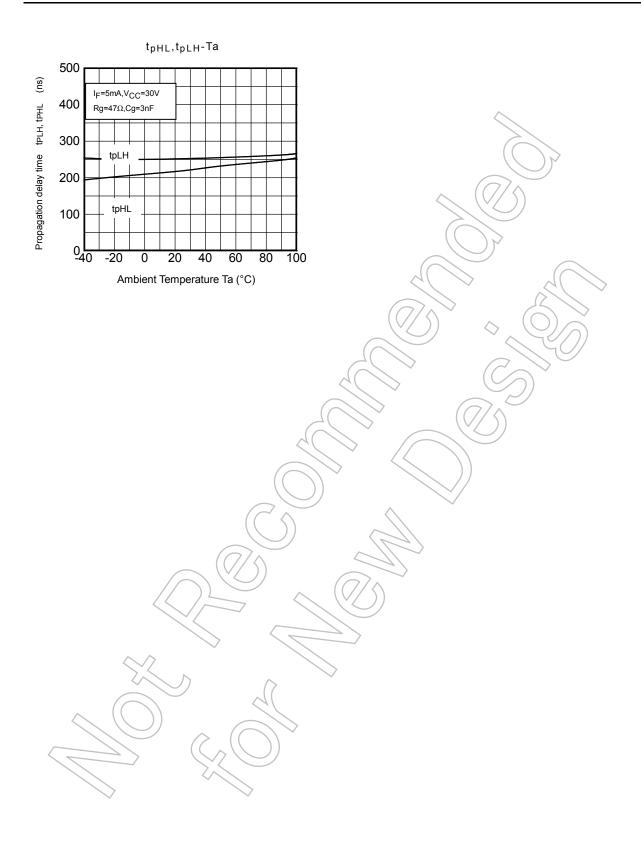




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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