

Bipolar Transistors Silicon NPN/PNP Epitaxial Type (PCT Process)(Bias Resistor built-in Transistor)

RN4984

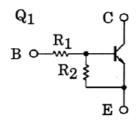
1. Applications

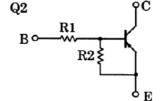
- · Switching
- · Inverter Circuits
- · Interfacing
- · Driver Circuits

2. Features

- (1) AEC-Q101 qualified (Please see the orderable part number list)
- (2) Including two devices in US6 (ultra super mini type with 6 leads)
- (3) The integrated bias resistor reduces the number of external parts required, making it possible to reduce system size and assembly time.

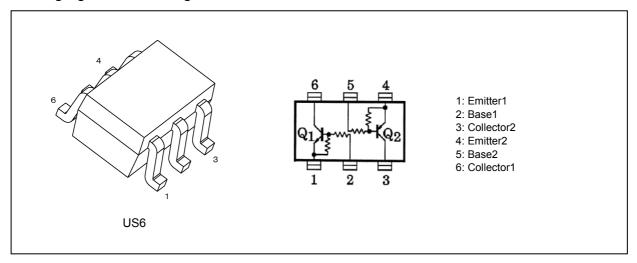
3. Equivalent Circuit





R1: 47 kΩ R2: 47 kΩ (Q1, Q2 Common)

4. Packaging and Pin Assignment



5. Orderable part number

Orderable part number	AEC-Q101		Note			
RN4984,LF	— General Use					
RN4984,LXGF	YES	(Note 1)	Unintended Use (Note 1)			
RN4984,LXHF	YES		Automotive Use			

Note 1: For more information, please contact our sales or use the inquiry form on our website.

Start of commercial production

1992-10



6. Q1 Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	
Emitter-base voltage	V _{EBO}	10	
Collector current	I _C	100	mA

7. Q2 Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-50	V
Collector-emitter voltage	V _{CEO}	-50	
Emitter-base voltage	V _{EBO}	-10	
Collector current	I _C	-100	mA

8. Q1, Q2 Common Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25 \, ^{\circ}\text{C}$)

Characteristics	Symbol	Rating	Unit	
Collector power dissipation	(Note 1)	P _C	200	mW
Junction temperature		T _j	150	°C
Storage temperature		T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

9. Q1 Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	V _{CB} = 50 V, I _E = 0 mA	_	_	100	nA
Collector cut-off current	I _{CEO}	V _{CE} = 50 V, I _B = 0 mA	_	_	500	
Emitter cut-off current	I _{EBO}	V _{EB} = 10 V, I _C = 0 mA	0.082	_	0.15	mA
DC current gain	h _{FE}	V _{CE} = 5 V, I _C = 10 mA	80	_	_	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 5 mA, I _B = 0.25 mA	_	0.1	0.3	٧
Input voltage (ON)	V _{I(ON)}	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.5	_	5.0	
Input voltage (off)	V _{I(off)}	V _{CE} = 5 V, I _C = 0.1 mA	1.0	_	1.5	
Transition frequency	f _T	V _{CE} = 10 V, I _C = 5 mA	_	250	_	MHz
Collector output capacitance	C _{ob}	V _{CB} = 10 V, I _E = 0 mA, f = 1 MHz		3	6	pF



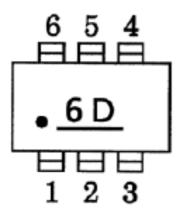
10. Q2 Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = -50 \text{ V}, I_{E} = 0 \text{ mA}$	_	_	-100	nA
Collector cut-off current	I _{CEO}	$V_{CE} = -50 \text{ V}, I_{B} = 0 \text{ mA}$	_	_	-500	
Emitter cut-off current	I _{EBO}	V _{EB} = -10 V, I _C = 0 mA	-0.082	_	-0.15	mA
DC current gain	h _{FE}	V _{CE} = -5 V, I _C = -10 mA	80	_	_	_
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = -5 mA, I _B = -0.25 mA	_	-0.1	-0.3	٧
Input voltage (ON)	V _{I(ON)}	V_{CE} = -0.2 V, I_{C} = -5 mA	-1.5	_	-5.0	
Input voltage (off)	V _{I(off)}	V _{CE} = -5 V, I _C = -0.1 mA	-1.0	_	-1.5	
Transition frequency	f _T	V _{CE} = -10 V, I _C = -5 mA	_	200	_	MHz
Collector output capacitance	C _{ob}	V _{CB} = -10 V, I _E = 0 mA, f = 1 MHz	_	3	6	pF

11. Q1, Q2 Common Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

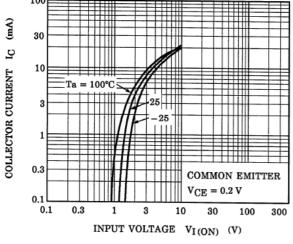
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistance	R ₁	-	32.9	47	61.1	kΩ
Resistor ratio	R1/R2	-	0.9	1.0	1.1	_

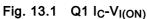
12. Marking

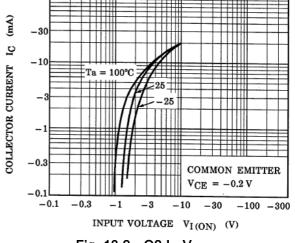




13. Characteristics Curves (Note)







-100

Fig. 13.2 Q2 I_C-V_{I(ON)}

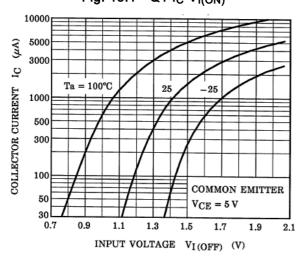


Fig. 13.3 Q1 I_C-V_{I(OFF)}

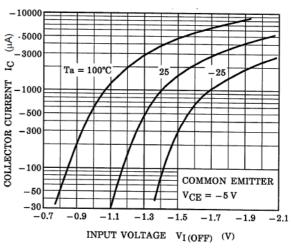


Fig. 13.4 Q2 I_C-V_{I(OFF)}

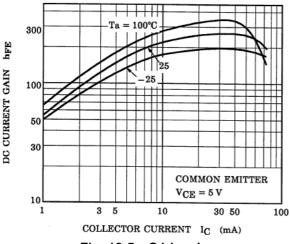


Fig. 13.5 Q1 h_{FE}-I_C

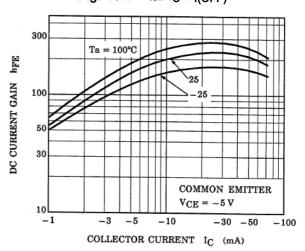
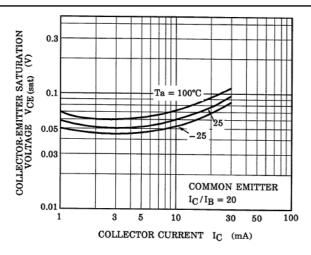


Fig. 13.6 Q2 h_{FE}-I_C





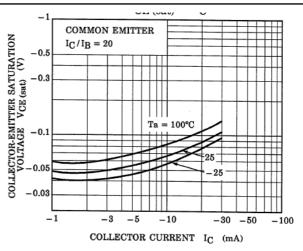


Fig. 13.7 Q1 V_{CE(sat)}-I_C

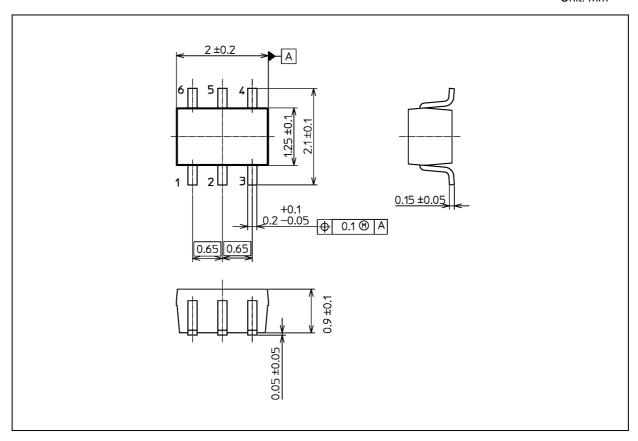
Fig. 13.8 Q2 V_{CE(sat)}-I_C

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 6.8 mg (typ.)

	Package Name(s)
TOSHIBA: 1-2T1S	
Nickname: US6	

Rev.2.0



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