

MOSFETs Silicon N-Channel MOS (DTMOSVI)

TK110A65Z

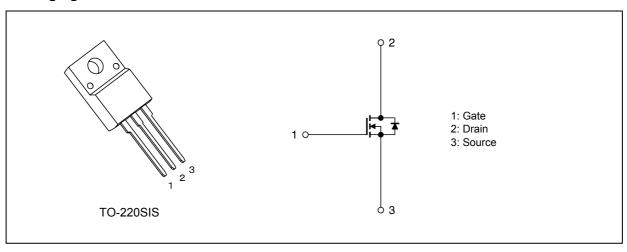
1. Applications

· Switching Power Supplies

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 0.092 \Omega$ (typ.)
- (2) High-speed switching properties with the lower capacitance.
- (3) Enhancement mode: V_{th} = 3 to 4 V (V_{DS} = 10 V, I_{D} = 1.02 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics			Rating	Unit
Drain-source voltage		V _{DSS}	650	V
Gate-source voltage		V _{GSS}	±30	
Drain current (DC)	(Note 1)	I _D	24	Α
Drain current (pulsed)	(Note 1)	I _{DP}	96	
Power dissipation (T _c = 25 °C)	P _D	45	W
Single-pulse avalanche energy	(Note 2)	E _{AS}	258	mJ
Single-pulse avalanche current		I _{AS}	6	Α
Reverse drain current (DC)	(Note 1)	I _{DR}	24	
Reverse drain current (pulsed)	(Note 1)	I _{DRP}	96	
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	
Isolation voltage (RMS)		V _{ISO(RMS)}	2000	V
Mounting torque		TOR	0.6	N·m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production

2020-02



5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	2.77	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	62.5	

Note 1: Ensure that the channel temperature does not exceed 150 °C.

Note 2: V_{DD} = 90 V, T_{ch} = 25 °C (initial), L = 12.7 mH, I_{AS} = 6 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current	I _{DSS}	V _{DS} = 650 V, V _{GS} = 0 V	_	_	2	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	650	_	_	V
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1.02 mA	3	_	4	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 12 A		0.092	0.11	Ω

6.2. Dynamic Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 300 V, V _{GS} = 0 V, f = 100 kHz	_	2250	_	pF
Reverse transfer capacitance	C _{rss}		_	1.9	_	
Output capacitance	C _{oss}		_	54	_	
Effective output capacitance	C _{o(er)}	V _{DS} = 0 to 400 V, V _{GS} = 0 V	_	85	_	pF
Gate resistance	r _g	V _{DS} = OPEN , f = 1 MHz	_	3.4	_	Ω
Switching time (rise time)	t _r	See Figure 6.2.1	_	28	_	ns
Switching time (turn-on time)	t _{on}		_	52	_	
Switching time (fall time)	t _f		_	4	_	
Switching time (turn-off time)	t _{off}		_	90	_	ns
MOSFET dv/dt ruggedness	dv/dt	$V_{DS} \le V_{(BR)DSS}$, $I_D \le 12 A$	90	_	_	V/ns

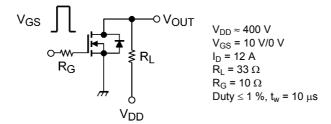


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 24 \text{ A}$		40		nC
Gate-source charge 1	Q _{gs1}		_	13	_	
Gate-drain charge	Q_{gd}			11		

6.4. Source-Drain Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	V_{DSF}	I _{DR} = 24 A, V _{GS} = 0 V	_		-1.7	V
Reverse recovery time	t _{rr}	V _{DD} = 400 V,	_	300	_	ns
Reverse recovery charge	Q_{rr}	I _{DR} = 12 A, V _{GS} = 0 V -dI _{DR} /dt = 100 A/μs	_	3.9	_	μС
Peak reverse recovery current	I _{rr}	-αιρκ/αι – 100 Α/μο	_	26	_	Α
Diode dv/dt ruggedness	dv/dt	$V_{DD} \le 400 \text{ V}, I_{DR} \le 12 \text{ A}, V_{GS} = 0 \text{ V}$	40		_	V/ns

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7. Marking (Note)

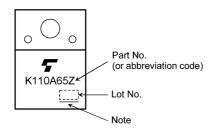


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

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8. Characteristics Curves (Note)

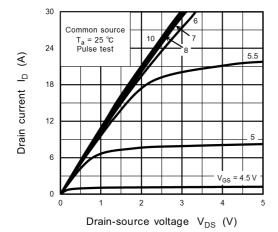
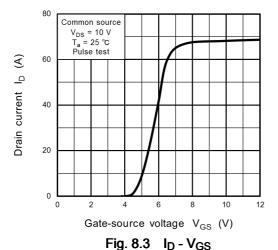
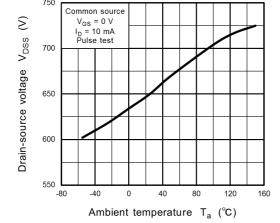


Fig. 8.1 I_D - V_{DS}





750

Fig. 8.5 V_{DSS} - T_a

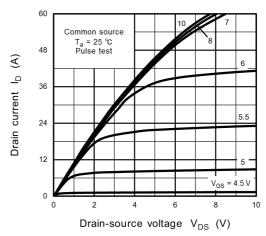


Fig. 8.2 I_D - V_{DS}

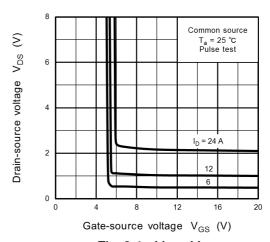


Fig. 8.4 V_{DS} - V_{GS}

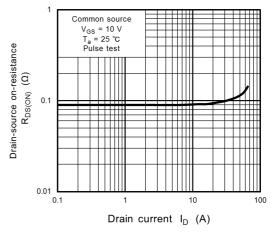
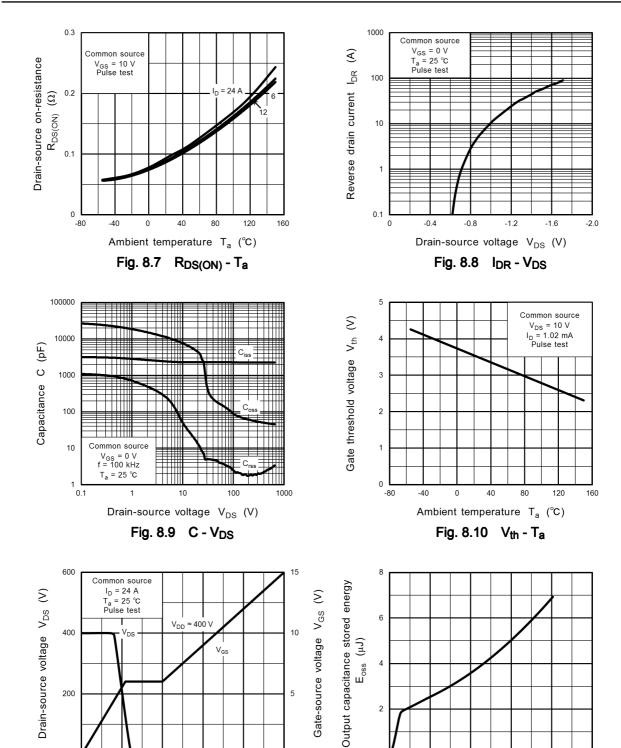


Fig. 8.6 R_{DS(ON)} - I_D





 $\begin{tabular}{ll} Total gate charge Q_g (nC) \\ \hline \begin{tabular}{ll} Fig. 8.11 & Dynamic Input/Output Characteristics \\ \hline \end{tabular}$

Drain-source voltage V_{DS} (V) Fig. 8.12 E_{OSS} - V_{DS}

0

0

= 0



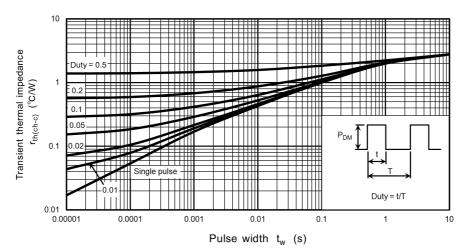
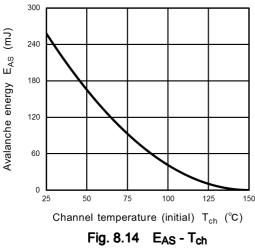


Fig. 8.13 rth - tw (Guaranteed Maximum)



(Guaranteed Maximum)

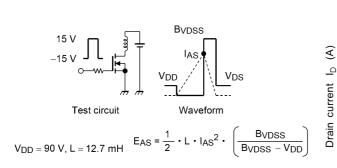


Fig. 8.16 Test Circuit/Waveform

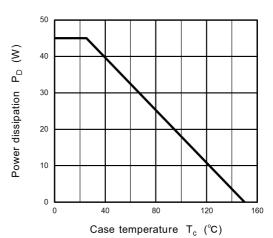


Fig. 8.15 P_D - T_c (Guaranteed Maximum)

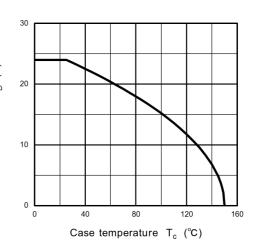


Fig. 8.17 I_D - T_c (Guaranteed Maximum)



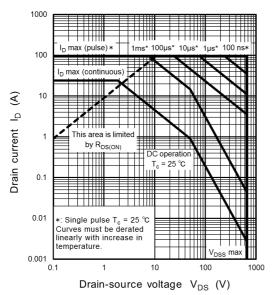


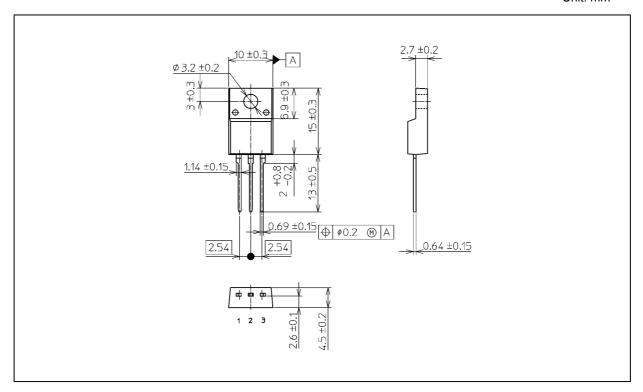
Fig. 8.18 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 1.7 g (typ.)

Package Name(s)	
JEITA: SC-67	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	



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