

TOSHIBA Diode Silicon Epitaxial Planar Type

HN1D03F

Ultra High Speed Switching Application

• Built in anode common and cathode common.

Unit 1

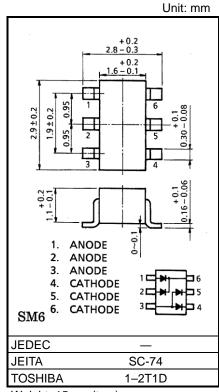
Low forward voltage Q1, Q2: V_F (3) = 0.90 V (typ.)
 Fast reverse recovery time Q1, Q2: t_{rr} = 1.6 ns (typ.)
 Small total capacitance Q1, Q2: C_T = 0.9 pF (typ.)

Unit 2

Low forward voltage Q3, Q4: V_F (3) = 0.92 V (typ.)
 Fast reverse recovery time Q3, Q4: t_{rr} = 1.6 ns (typ.)
 Small total capacitance Q3, Q4: C_T = 2.2 pF (typ.)

Unit 1, Unit 2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V _{RM}	85	V	
Reverse voltage	V _R	80	V	
Maximum (peak) forward current	I _{FM}	300 (*)	mA	
Average forward current	lo	100 (*)	mA	
Surge current (10 ms)	I _{FSM}	2 (*)	Α	
Power dissipation	P _D (Note 3)	300	mW	
Junction temperature	T _j (Note 1)	150	°C	
	T _j (Note 2)	125		
Storogo tomporaturo	T _{stg} (Note 1)	−55 to 150	°C	
Storage temperature	T _{stg} (Note 2)	-55 to 125	C	



Weight: 15 mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

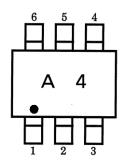
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: For devices with the ordering part number ending in LF(T.
- Note 2: For devices with the ordering part number in other than LF(T.
- Note 3: Total rating.
 - (*) These are the Absolute Maximum Ratings for a single diode (Q1 or Q2 or Q3 or Q4). If Unit 1 and Unit 2 are used independently or simultaneously, the Absolute Maximum Ratings per diode are 75% of those of a single diode.

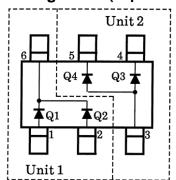
Start of commercial production 1992-05



Marking



Pin Assignment (Top View)



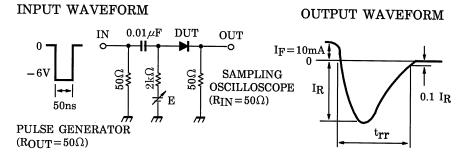


Fig.1 Reverse Recovery Time (t_{rr}) Test Circuit

Unit 1 Electrical Characteristics (Q1, Q2 Common) (Ta = 25°C)

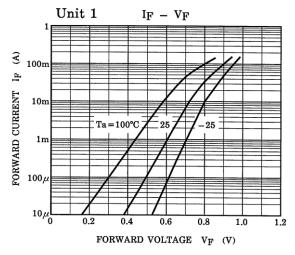
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F (1)}	_	I _F = 1 mA	_	0.60	_	
	VF (2)	_	IF = 10 mA	_	0.72	_	V
	V _{F (3)}	_	I _F = 100 mA	_	0.90	1.20	
Reverse current	I _{R (1)}	_	V _R = 30 V	_	_	0.1	μA
	I _{R (2)}	_	V _R = 80 V	_	_	0.5	
Total capacitance	Ст	_	V _R = 0 V, f = 1 MHz	_	0.9	3.0	pF
Reverse recovery time	t _{rr}	_	I _F = 10 mA (fig.1)	_	1.6	4.0	ns

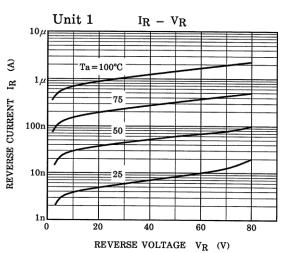
Unit 2 Electrical Characteristics (Q3, Q4 Common) (Ta = 25°C)

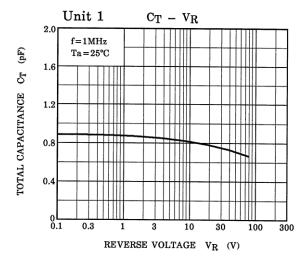
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	VF (1)	_	IF = 1 mA	-	0.61	_	
	VF (2)	_	IF = 10 mA	ı	0.74	_	V
	VF (3)	_	IF = 100 mA		0.92	1.20	
Reverse current	IR (1)	_	V _R = 30 V	1	_	0.1	
	I _{R (2)}	_	V _R = 80 V	ı	_	0.5	μA
Total capacitance	CT	_	V _R = 0 V, f = 1 MHz	I	2.2	4.0	pF
Reverse recovery time	t _{rr}	_	IF = 10 mA (fig.1)	_	1.6	4.0	ns

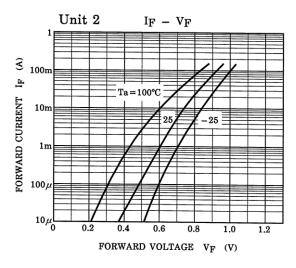


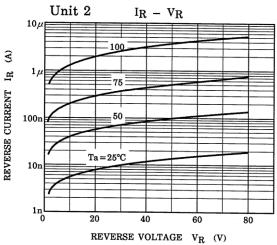
Characteristics Curves

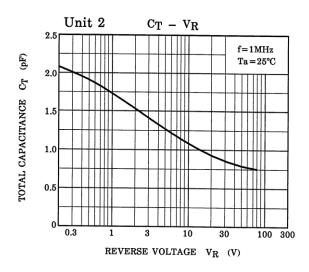












The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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