MOSFETs Silicon N-channel MOS (U-MOSIV)

TK40S10K3Z

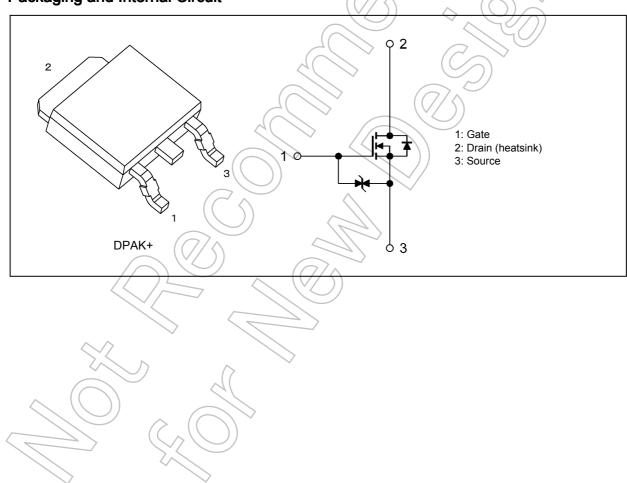
1. Applications

- Automotive
- Motor Drivers
- DC-DC Converters
- Switching Voltage Regulators

2. Features

- (1) AEC-Q101 qualified
- (2) Low drain-source on-resistance: $R_{DS(ON)} = 14.4 \text{ m}\Omega$ (typ.) ($V_{GS} = 10 \text{ V}$)
- (3) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 100 \ V)$
- (4) Enhancement mode: $V_{th} = 3.0$ to 4.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristic	3		Symbol	Rating	Unit
Drain-source voltage			V _{DSS}	100	V
Gate-source voltage			V _{GSS}	±20	
Drain current (DC)		(Note 1)	I _D	40	А
Drain current (pulsed)		(Note 1)	I _{DP}	80	
Power dissipation	(T _c = 25°C)		PD	93	W
Single-pulse avalanche energy		(Note 2)	E _{AS}	98	mJ
Avalanche current			IAR	40	A
Channel temperature		(Note 3)	T _{ch}) 175	°C
Storage temperature		(Note 3)	T _{stg}	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

	Characteristics	6	Symbol	Max	Unit
Channel-to-case thermal resistance	$ \geq \langle \rangle >$	(\vee)	R _{th(ch-c)}	1.6	°C/W
			/		

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 99 μ H, R_G = 25 Ω , I_{AR} = 40 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are qualified per AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

6.1. Static Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	_	_	±10	μA
Drain cut-off current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	X	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	100		_	V
	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	55	(_	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	2_	4.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 20 A	$\langle A \rangle$	14.4	18	mΩ

6.2. Dynamic Characteristics (Ta = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		3110	\rightarrow	pF
Reverse transfer capacitance	C _{rss}			245	Ι	
Output capacitance	C _{oss}		((400	-	
Switching time (rise time)	tr	See Figure 6.2.1.	R	22) —	ns
Switching time (turn-on time)	t _{on}		\sim	44	_	
Switching time (fall time)	t _f			13	_	
Switching time (turn-off time)	t _{off}		[ل_م_	57	_	

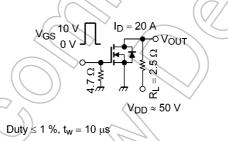


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 80$ V, V_{GS} = 10 V, I_D = 40 A	_	61	—	nC
Gate-source charge	Q _{gs}	\sim		36	_	
Gate-drain charge	Q _{gd}		_	25	_	

6.4. Source-Drain Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	<u>z</u>	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I _{DR}	—	_	_	40	А
Reverse drain current (pulsed)	(Note 4)	I _{DRP}	—	_	—	80	
Diode forward voltage		V_{DSF}	I _{DR} = 40 A, V _{GS} = 0 V	—	—	-1.2	V
Reverse recovery time		t _{rr}	I _{DR} = 40 A, V _{GS} = 0 V		58		ns
Reverse recovery charge		Q _{rr}	-dI _{DR} /dt = 50 A/μs	_	58	_	nC

Note 4: Ensure that the channel temperature does not exceed 175°C.

TK40S10K3Z

7. Marking (Note)

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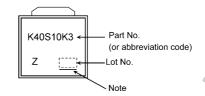
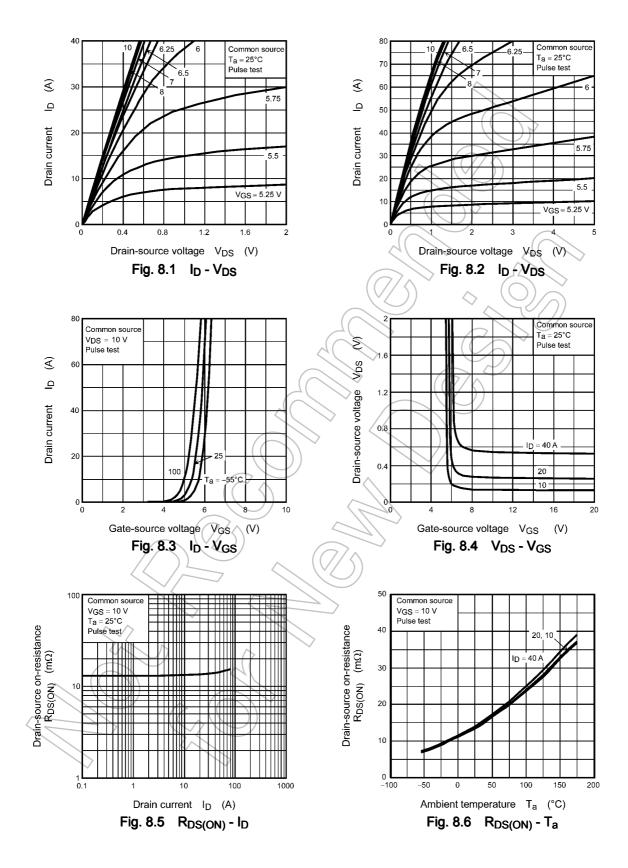
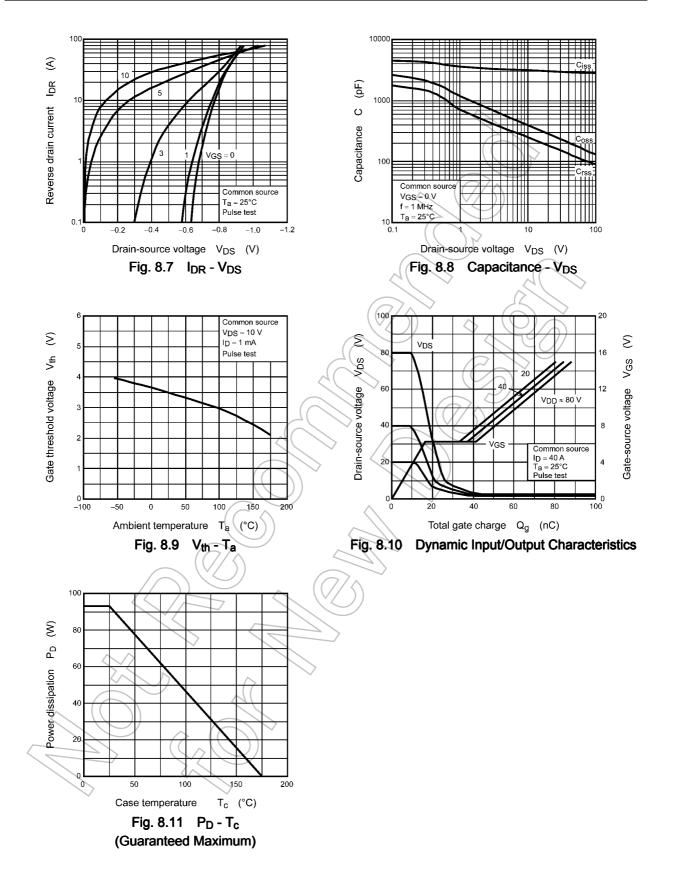


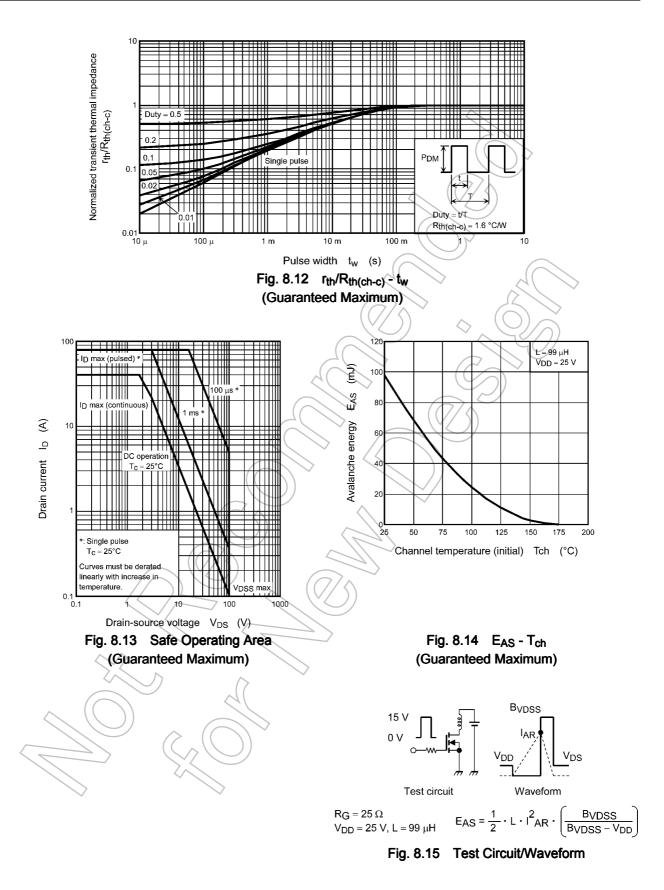
Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]] Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

8. Characteristics Curves (Note)



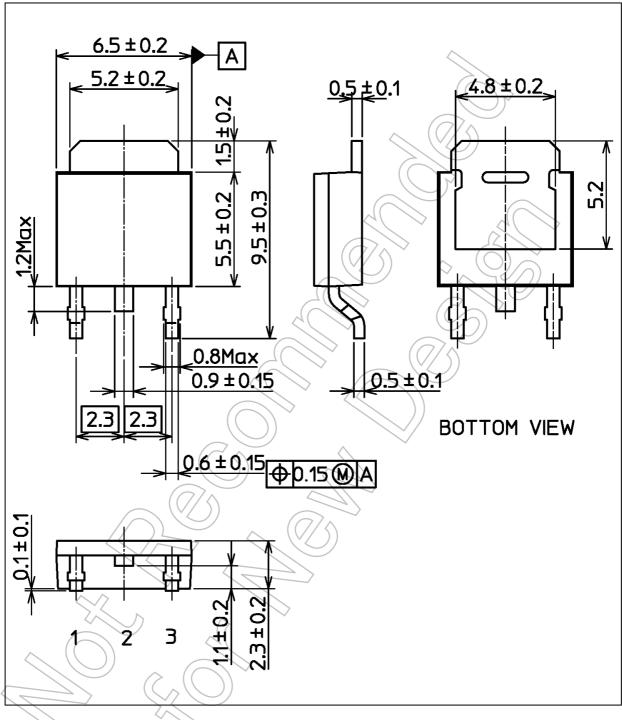




Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Unit: mm



Weight: 0.36 g (typ.)

TOSHIBA: 2-7M1A	
Nickname: DPAK+	

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