Unit: mm



### TOSHIBA PHOTOCOUPLER IRED & PHOTO-DIODE ARRAY

# **TLP3914**

# **TELECOMMUNICATION** PROGRAMMABLE CONTROLLERS MOSFET GATE DRIVER

The TOSHIBA SSOP coupler TLP3914 is a small outline coupler, suitable for surface mount assembly.

The TLP3914 consists of an infrared emitting diode, optically coupled to a series connected photo diode array which is suitable for MOSFET gate drive.

### **Features**

• 4 pin SSOP (SSOP4) : 1.8 mm high, 1.27 mm pitch

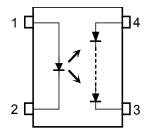
 Open Voltage : 7 V (min) • Short Current : 20 µA (min) • Isolation Voltage : 1500 Vrms (min)

: UL 1577, File No.E67349 • UL-recognized

# There is a detail drawing in P6. TOSHIBA 11-2B1

Weight: 0.03 g (typ.)

# Pin Configuration (top view)



- 1. ANODE
- 2. CATHODE
- 3. CATHODE
- 4. ANODE



### **Absolute Maximum Ratings (Ta = 25°C)**

	Characteristic	Symbol	Rating	Unit
	Forward Current	lF	30	mA
	Forward Current Derating (Ta ≥ 25°C)	ΔIF / °C	-0.3	mA / °C
LED	Reverse Voltage	VR	5	V
	Diode power dissipation	PD	50	mW
	Diode power dissipation derating (Ta ≥ 25°C)	ΔP <sub>D</sub> /°C	-0.5	mW/°C
	Junction Temperature	Tj	125	°C
	Forward Current	lFD	50	μА
DETECTOR	Reverse Voltage	VRD	10	V
DETECTOR	Output power dissipation	Po	0.5	mW
	Junction Temperature	Tj	125	°C
Storage Temperature Range		T <sub>stg</sub>	-55 to 125	°C
Operating Temperature Range		T <sub>opr</sub>	-40 to 85	°C
Lead Soldering Temperature (10 s)		T <sub>sol</sub>	260	°C
Isolation Voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)		BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal device: Pins 1 and 2 shorted together and pins 3 and 4 shorted together.

### Caution

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

### **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Forward Current	lF	7	_	20	mA
Operating Temperature	T <sub>opr</sub>	-25	_	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

### **Electrical Characteristics (Ta = 25°C)**

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward Voltage	VF	I <sub>F</sub> = 10 mA	1.15	1.30	1.45	V
LED	Reverse Current	IR	V <sub>R</sub> = 5 V	_	_	10	μА
	Capacitance	Ст	V = 0 V, f = 1 MHz	_	30	_	pF
DETECTOR	Forward Voltage	V <sub>FD</sub>	I <sub>FD</sub> = 10 μA	_	9.6	_	V
	Reverse Current	I <sub>RD</sub>	V <sub>RD</sub> = 10 V	_	1	_	nA
	Capacitance (Anode to Cathode)	CTD	V = 0 V, f = 1 MHz	_	2.5	_	pF



# **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Open-Circuit Voltage	Voc	I <sub>F</sub> = 10 mA	7	_	_	V
Short-Circuit Current	Isc	I <sub>F</sub> = 10 mA	20	_	_	μΑ

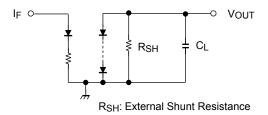
# Isolation Characteristics (Ta = 25°C)

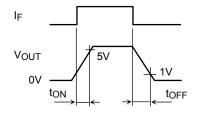
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance Input to Output	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	8.0	_	pF
Isolation Resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation Voltage	BVS	AC, 60 s	1500		_	Vrms

# **Switching Characteristics (Ta = 25°C)**

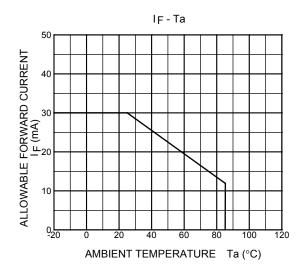
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on Time	ton	I <sub>F</sub> = 10 mA, R <sub>SH</sub> = 300 kΩ	_	0.6	_	ms
Turn-off Time	toff	C <sub>L</sub> = 1000 pF (Note 1)	_	0.3	1	ms

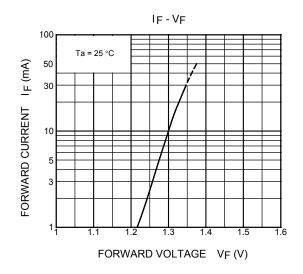
Note 1: SWITCHING TIME TEST CIRCUIT

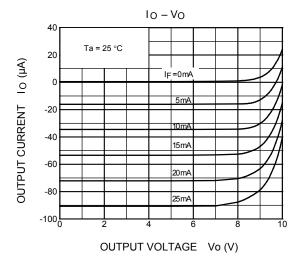


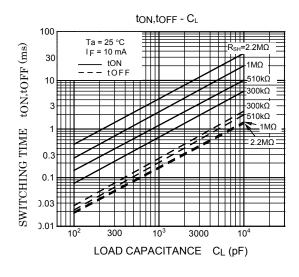


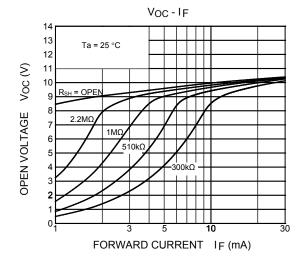


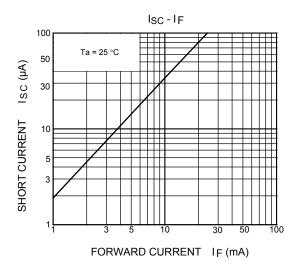




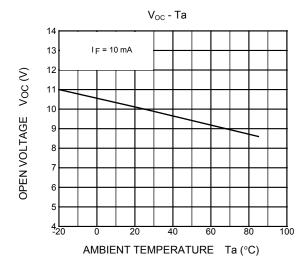


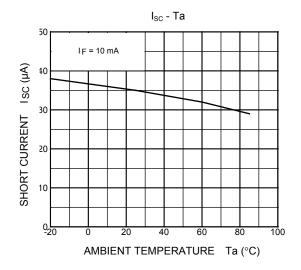






NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

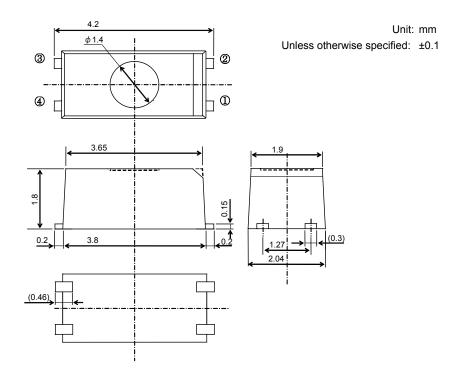




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### **OUTLINE DRAWING**





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