TOSHIBA Field Effect Transistor Silicon N-Channel Dual Gate MOS Type

# 3SK294

#### TV Tuner, VHF RF Amplifier Application

Superior cross modulation performance

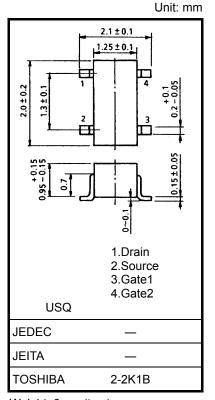
• Low reverse transfer capacitance:  $C_{rss} = 20 \text{ fF (typ.)}$ 

• Low noise figure: NF = 1.4dB (typ.)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Drain-source voltage	$V_{DS}$	12.5	V
Gate 1-source voltage	V <sub>G1S</sub>	±8	V
Gate 2-source voltage	$V_{G2S}$	±8	V
Drain current	ID	30	mA
Drain power dissipation	$P_{D}$	100	mW
Channel temperature	T <sub>ch</sub>	125	°C
Storage temperature range	T <sub>stg</sub>	-55 to 125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.



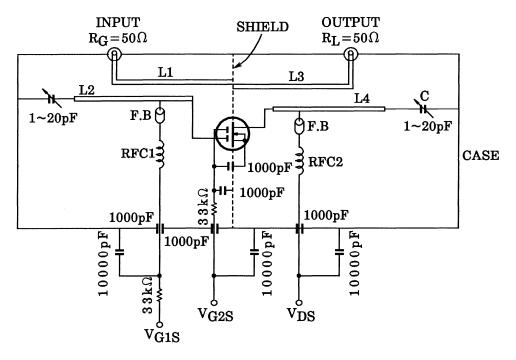
Weight: 6 mg (typ.)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate 1 leakage current	I <sub>G1SS</sub>	$V_{DS} = 0$ , $V_{G1S} = \pm 6$ V, $V_{G2S} = 0$	_	_	±50	nA
Gate 2 leakage current	I <sub>G2SS</sub>	$V_{DS} = 0$ , $V_{G1S} = 0$ , $V_{G2S} = \pm 6 \text{ V}$	_	_	±50	nA
Drain-source voltage	V (BR) DSX	$\begin{aligned} V_{G1S} &= -0.5 \; V,  V_{G2S} = -0.5 \; V, \\ I_D &= 100 \; \mu A \end{aligned}$	12.5	_	_	V
Drain current	I <sub>DSS</sub>	$V_{DS} = 6 \text{ V}, V_{G1S} = 0, V_{G2S} = 4.5 \text{ V}$	_	_	0.1	mA
Gate 1-source cut-off voltage	V <sub>G1S</sub> (OFF)	$V_{DS} = 6 \text{ V}, V_{G2S} = 4.5 \text{ V}, I_D = 100 \mu A$	0.3	0.9	1.3	V
Gate 2-source cut-off voltage	V <sub>G2S</sub> (OFF)	$V_{DS} = 6 \text{ V}, V_{G1S} = 4.0 \text{ V}, I_D = 100 \mu A$	0.5	1.0	1.5	V
Forward transfer admittance	Y <sub>fs</sub>	$V_{DS} = 6 \text{ V}, V_{G2S} = 4.5 \text{ V}, I_D = 10 \text{ mA},$ f = 1 kHz	19.5	23.5	_	mS
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 6 V, V <sub>G2S</sub> = 4.5 V, I <sub>D</sub> = 10 mA,	_	2.5	3.1	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1 MHz	_	20	40	fF
Power gain	G <sub>ps</sub>	$V_{DS} = 6 \text{ V}, V_{G2S} = 4.5 \text{ V}, I_D = 10 \text{ mA},$	23.5	26.0	_	dB
Noise figure	NF	f = 500 MHz (Figure 1)	_	1.4	2.5	dB

Start of commercial production 1996-10



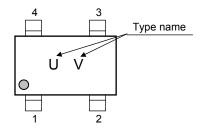
L1 to L4:  $\phi$ 0.8 mm silver plated copper wire

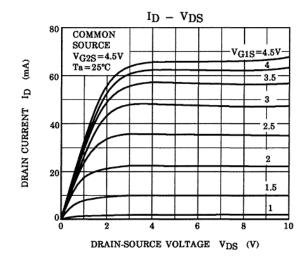
C: Air trimmer TTA25A200A (MURATA Manufacturing, Co., Ltd.)

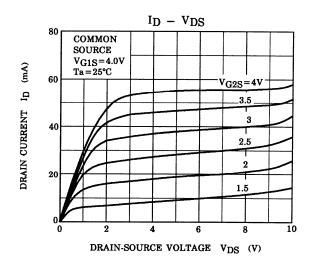
RFC 1:  $\phi$ 0.35 mm UEW 3I.D. 7 T RFC 2:  $\phi$ 0.35 mm UEW 3I.D. 10 T

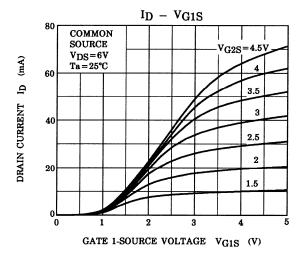
Figure 1  $G_{ps}$ , NF Test Circuit

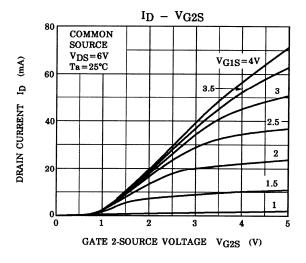
### Marking

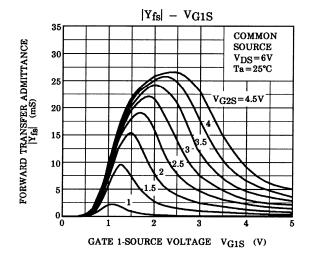


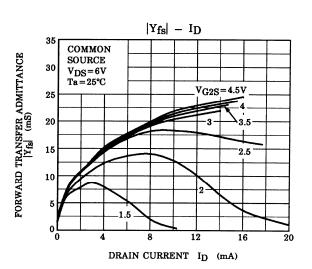


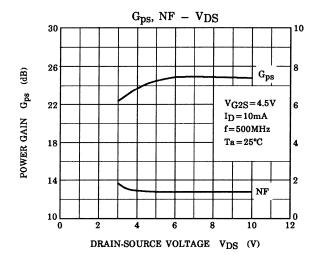


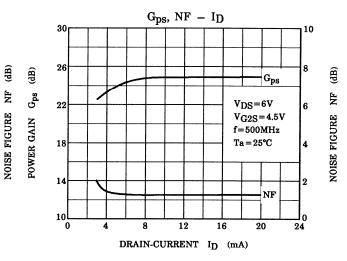


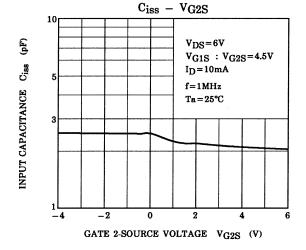


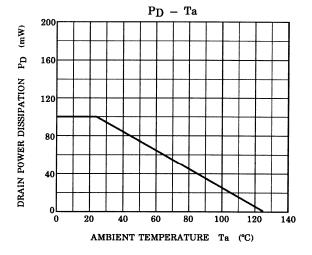












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