Unit:mm

TOSHIBA Diodes for Protecting Against ESD

# DF3A5.6CT

Product for Use Only as Protection against Electrostatic Discharge (ESD)

\*This product is for protection against electrostatic discharge (ESD) only and is not intended for any other usage, including without limitation, the constant voltage diode application.

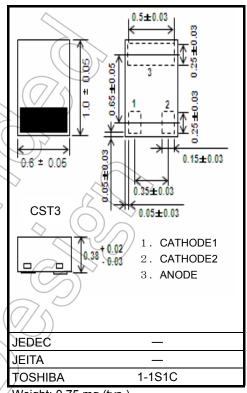
• The mounting of two devices on an ultra-compact package allows the number of parts and the mounting cost to be reduced.

#### Absolute Maximum Ratings (Ta = 25°C)

| Characteristic            | Symbol           | Rating        |
|---------------------------|------------------|---------------|
| Power dissipation         | Р                | 150* mW       |
| Junction temperature      | Tj               | 150 °C        |
| Storage temperature range | T <sub>stg</sub> | –55 to 150 °C |

<sup>\*:</sup> Mounted on FR4 board (10 mm × 10 mm × 1 mm(t))

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.



Weight: 0.75 mg (typ.)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Electrical Characteristics (Ta = 25°C)**

| Characteristic                                   | Symbol | Test Condition                  | Min | Тур. | Max | Unit |  |
|--|--------|---------------------------------|-----|------|-----|------|--|
| Zener voltage                                    | VŽ>    | I <sub>Z</sub> = 5 mA           | 5.3 | 5.6  | 6.0 | V    |  |
| Dynamic impedance                                | Zz     | I <sub>Z</sub> = 5 mA           | _   | _    | 40  | Ω    |  |
| Reverse current                                  | 1R     | V <sub>R</sub> =2.5 V           | _   | _    | 1.0 | μА   |  |
| Terminal capacitance (between Cathode and Anode) | CT     | V <sub>R</sub> = 0 V, f = 1 MHz | _   | 65   | _   | pF   |  |

Start of commercial production 2004-09

### **Guaranteed Level of ESD Immunity**

| Test Condition                      | ESD Immunity Level |
|-------------------------------------|--------------------|
| IEC61000-4-2<br>(Contact discharge) | ± 30 kV            |

Criterion: No damage to device elements

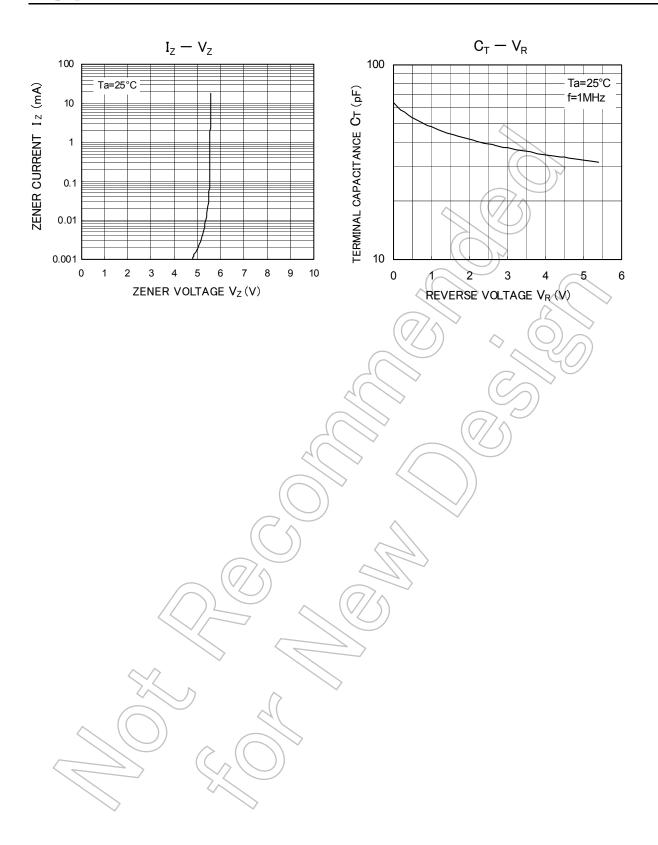
#### Marking

## **Equivalent Circuit (Top View)**









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