

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC138F, TC74VHC138FK

3-to-8 Line Decoder

The TC74VHC138 is an advanced high speed CMOS 3-to-8 DECODER fabricated with silicon gate C^2MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

When the device is enabled, 3 Binary Select inputs (A, B and C) determine which one of the outputs ($\overline{Y}0 - \overline{Y}7$) will go low.

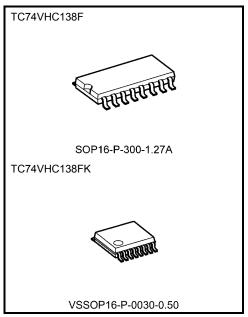
When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all outputs go high.

 $G1,\overline{G}2A$, and $\overline{G}2B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

An input protection circuit ensures that 0 to 5.5~V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5~V to 3~V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High speed: $t_{pd} = 5.7 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A \text{ (max)}$ at $T_a = 25 \text{°C}$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC (min)}$
- Power down protection is provided on all inputs.
- Balanced propagation delays: t_{pLH} ≃ t_{pHL}
- Wide operating voltage range: V_{CC} (opr) = 2 V to 5.5 V
- Pin and function compatible with 74ALS138



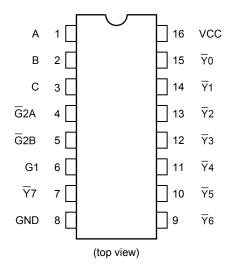
Weight

SOP16-P-300-1.27A : 0.18 g (typ.) VSSOP16-P-0030-0.50 : 0.02 g (typ.)

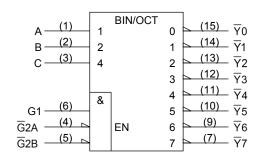
Start of commercial production 1991-11

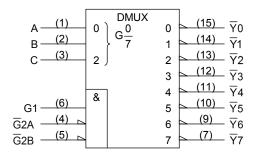


Pin Assignment



IEC Logic Symbol





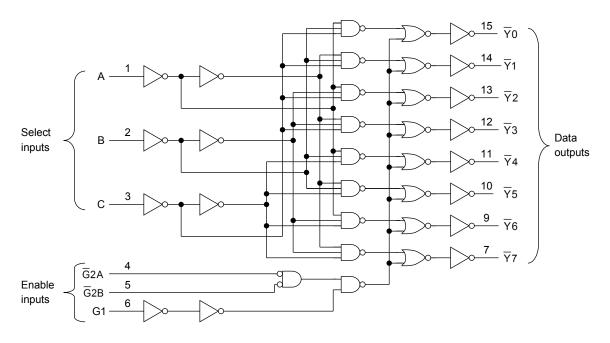
Truth Table

Inputs					Outputs									
Enable			Select		_ Y0	<u>\text{Y}1</u>	<u></u>	_ Y3	- ¥4	_ Y5	- Y6	7 7	Selected Output	
G1	G ₂ A	G ₂ B	С	В	Α	10	11	1 2	13	14	13	10	1 /	,
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Н	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Х	Н	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	₹0
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Y 1
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	₹2
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	7 3
Н	L	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Y 4
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	 Y 6
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	₹7

X: Don't care



Logic Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	−0.5 to 7.0	V
DC input voltage	V _{IN}	−0.5 to 7.0	V
DC output voltage	Vout	−0.5 to V _{CC} + 0.5	V
Input diode current	lıĸ	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC VCC/ground current	Icc	±75	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	Topr	−40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 ($V_{CC} = 3.3 \pm 0.3 \text{ V}$) 0 to 20 ($V_{CC} = 5 \pm 0.5 \text{ V}$)	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
				Vcc (V)	Min	Тур.	Max	Min	Max		
High-level input voltage	Viн	_		2.0 3.0 to 5.5	1.50 V _{CC} × 0.7	_ _	_ _	1.50 V _{CC} × 0.7	_	V	
Low-level input voltage	VIL	_		2.0 3.0 to 5.5		_	0.50 V _{CC} × 0.3		0.50 V _{CC} × 0.3	٧	
High-level output voltage	Vон	VIN = VIH or VIL	ΙΟΗ = -50 μΑ	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5		1.9 2.9 4.4		V	
Vollage			$I_{OH} = -4 \text{ mA}$ $I_{OH} = -8 \text{ mA}$	3.0 4.5	2.58 3.94	_	_	2.48 3.80			
Low-level output	Vol	VIN = VIH or VIL	I _{OL} = 50 μA	2.0 3.0 4.5		0.0 0.0 0.0	0.1 0.1 0.1		0.1	V	
			$I_{OL} = 4 \text{ mA}$ $I_{OL} = 8 \text{ mA}$	3.0 4.5	_ _	_ _	0.36 0.36	_ _	-		
Input leakage current	liN	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μА	
Quiescent supply current	lcc	V _{IN} = V _{CC} or GND		5.5	ı	1	4.0	ı	40.0	μА	



AC Characteristics (input: $t_r = t_f = 3$ ns)

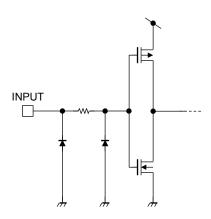
Characteristics	Course le se l	Test Condition Ta = 25°C						Ta = −40 to 85°C		Unit
Characteristics	Symbol		Vcc (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Unit
			3.3 ± 0.3	15	_	8.2	11.4	1.0	13.5	
Propagation delay time	t _{pLH}		3.3 ± 0.3	50	_	10.0	15.8	1.0	18.0	no
$(A, B, C-\overline{Y})$	t _{pHL}	_	5.0 ± 0.5	15	_	5.7	8.1	1.0	9.5	ns
				50		7.2	10.1	1.0	11.5	
			3.3 ± 0.3	15		8.1	12.8	1.0	15.0	ns
Propagation delay time	t _{pLH}			50	1	10.6	16.3	1.0	18.5	
(G1- \(\overline{Y} \))	t _{pHL}	_	5.0 ± 0.5	15	1	5.6	8.1	1.0	9.5	115
			3.0 ± 0.3	50	1	7.1	10.1	1.0	11.5	
			3.3 ± 0.3	15	1	8.2	11.4	1.0	13.5	- ns
Propagation delay time	t _{pLH}			50	1	10.7	14.9	1.0	17.0	
(G 2 - Y)	t _{pHL}	_	5.0 ± 0.5	15	1	5.8	8.1	1.0	9.5	115
			J.0 ± 0.5	50	1	7.3	10.1	1.0	11.5	
Input capacitance	C _{IN}		_		_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note)		34		_	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $ICC (opr) = CPD \cdot VCC \cdot fIN + ICC$

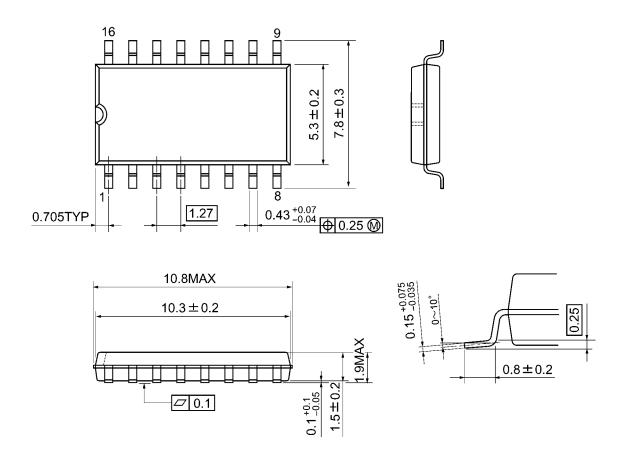
Input Equivalent Circuit





Package Dimensions

SOP16-P-300-1.27A Unit: mm

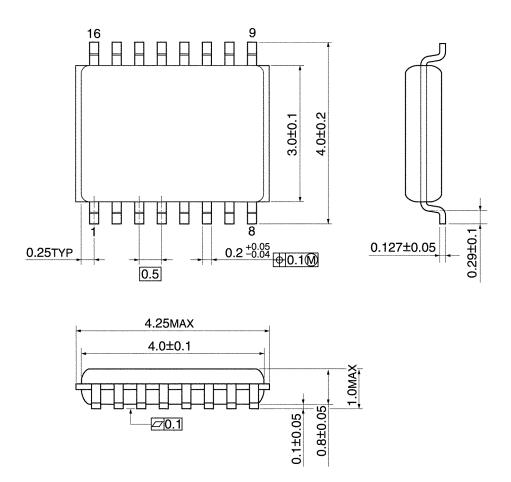


Weight: 0.18 g (typ.)



Package Dimensions

VSSOP16-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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