TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4538BP, TC4538BF

TC4538BP/TC4538BF Dual Precision

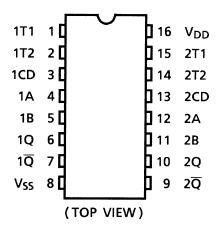
Retriggerable/Resettable Monostable Multivibrator

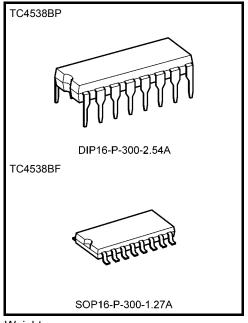
The TC4538BP/BF is the retriggerable/resettable monostable multivibrator and the trigger operation can be made at either the leading or trailing edge by 2 inputs of A and B. Since the output monostable pulse width is decided by time constant of the external resistor (Rx) and the external capacitor (Cx), it becomes possible to set a broad range of output pulse widths.

Features

• $t_{WOUT} = 10 \text{ ms} \pm 5\% \text{ (at } RX = 100 \text{ k}\Omega \text{ CX} = 0.1 \text{ }\mu\text{F}, \text{VDD} = 10 \text{ V)}$

Pin Assignment





Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

Truth Table (Note)

	Inputs		Out	puts	Note
Α	В	CD	Q	Q	Note
	Н	Н	П	П	Output Enable
	L	Н	L	Н	Inhibit
Н	\Box	Н	L	Н	Inhibit
L	\Box	Н	Л	Ţ	Output Enable
*	*	L	L	Н	Inhibit

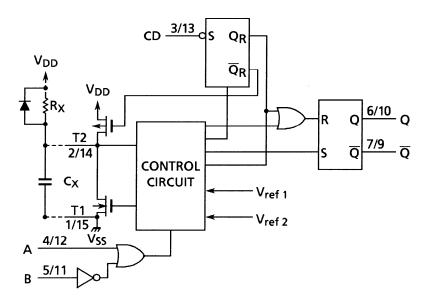
*: Don't care

Note: In the case of using only one circuit, CD should be tied to GND, T_2 , T_1 , Q, \overline{Q} should be tied to OPEN, and the other inputs should be tied to V_{CC} or GND.

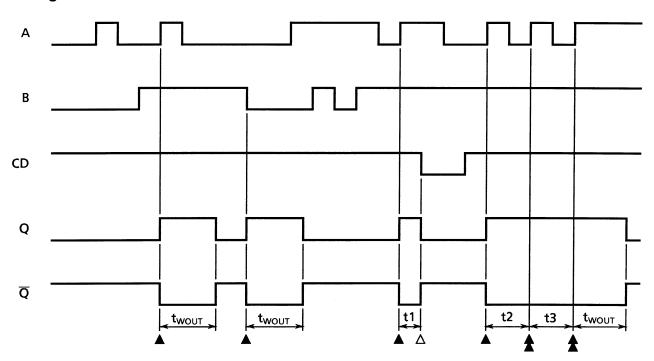
Start of commercial production 1978-04

Logic Diagram

1/2 TC4538BP/BF



Timing Chart



▲: TRIGGER

★: RETRIGGER

△: RESET

 $t_{\text{WOUT}} = C_X \cdot R_X$

 $t1\cdot t2\cdot t3\;;\quad t1\cdot t2\cdot t3\!<\!t_{WOUT}$



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V_{SS} – 0.5 to V_{SS} + 20	V
Input voltage	V _{IN}	V _{SS} – 0.5 to V _{DD} + 0.5	V
Output voltage	V _{OUT}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	٧
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	–65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}	_	0	_	V_{DD}	V
External resistance	RX	_	5	_	1000	kΩ
External capacitance	CX	_		No limits	i	μF

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .



Static Electrical Characteristics ($V_{SS} = 0 V$)

(haracteristics)		Sym-	Test Condition		-40°C		25°C			85°C			
		bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit	
High-level output voltage		V _{OH}	I _{OUT} < 1 μA	5 10	4.95 9.95	_	4.95 9.95	5.00 10.00	_	4.95 9.95	_	V	
			$V_{IN} = V_{SS}, V_{DD}$	15	14.95	_	14.95	15.00	_	14.95	_		
			I _{OUT} < 1 μA	5	_	0.05	_	0.00	0.05	_	0.05		
Low-level voltage	output	V _{OL}	$V_{IN} = V_{SS}, V_{DD}$	10	_	0.05	_	0.00	0.05	_	0.05	V	
			VIN = VSS, VDD	15	_	0.05	—	0.00	0.05	—	0.05		
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_		
			V _{OH} = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA	
Output hig	h current	loh	V _{OH} = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_		
			V _{OH} = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_		
			$V_{IN} = V_{SS}, V_{DD}$										
		loL	V _{OL} = 0.4 V	5	0.61	_	0.51	1.5	_	0.42	_	mA	
Output low	v current		V _{OL} = 0.5 V	10	1.50	_	1.30	3.8	_	1.10	_		
Output low current		IOL	V _{OL} = 1.5 V	15	4.00	_	3.40	15.0	_	2.80	_	11171	
			$V_{IN} = V_{SS}, V_{DD}$										
		V _{IH}	V _{OUT} = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_	V	
Input high	voltage		V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_		
input nign	voitage		V _{OUT} = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	v	
			$ I_{OUT} < 1 \mu A$										
		V _{IL}	V _{OUT} = 0.5 V, 4.5 V	5		1.5	_	2.25	1.5	_	1.5	V	
Input low y	voltago		V _{OUT} = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0		
Input low voltage		V IL	V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	V	
			$ I_{OUT} < 1 \mu A$										
Input	"H" level	l _{IH}	V _{IH} = 18 V	18		0.1		10 ⁻⁵	0.1		1.0	μА	
current	"L" level	I _Ι L	V _{IL} = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ	
				5	_	5	_	0.005	5	_	150		
Quiescent current	Quiescent supply current		$V_{IN} = V_{SS}, V_{DD}$ (Note)	10	_	10	_	0.010	10	_	300	μΑ	
			(130)	15	_	20		0.015	20	_	600		

Note: All valid input combinations.

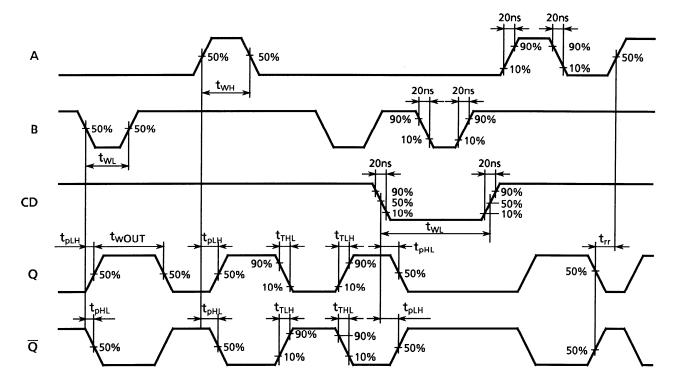


Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Ch arrastariation	Comments and	Test Condition		Min	Tura	Max	Unit
Characteristics	Symbol		V _{DD} (V)	IVIIII	Тур.	IVIAX	Unit
Output transition time			5	_	80	200	
(low to high)	t _{TLH}	_	10	_	50	100	ns
(low to flight)			15		40	80	
Output transition time			5	_	80	200	
(high to low)	t _{THL}	_	10	_	50	100	ns
(High to low)			15	_	40	80	
Propagation delay time	t _{erri}		5	_	380	760	
(A, B-Q, \overline{Q})	t _{pLH}	_	10	_	150	300	ns
(A, D-Q, Q)	t _{pHL}		15	_	100	220	
Propagation delay time	t _{pLH}		5	_	280	560	
(CD-Q, \overline{Q})	t _{pHL}	_	10	_	110	250	ns
(65 4, 4)	φпс		15	_	75	190	
Min input pulse width	twH		5	_	60	120	
(A, B)	t _{WL}	_	10	_	30	60	ns
(, _/	*VVL		15	_	25	50	
Min pulse width			5	_	95	190	
(CD)	t _{WL}	_	10	_	45	90	ns
			15	_	35	70	
	t _{rr}		5	_	0	_	
Min retrigger time		_	10	_	0	_	ns
			15	_	0	_	
		R _X = 100 kΩ	5	_	206	_	
		C _X = 0.002 μF	10	_	204	_	μS
			15	_	205	_	
		R _X = 100 kΩ	5	9.30	9.95	10.40	
Output pulse width	twout	C _X = 0.1 μF	10	9.50	10.00	10.50	ms
			15	9.55	10.05	10.65	
		R _X = 100 kΩ	5	_	0.98	_	
		C _X = 10 μF	10	_	1.00	_	S
			15	_	1.01		
Pulse width match between circuits in		$t_{\text{WOUT}}(Q2) - t_{\text{w}}(Q1)$	5	_	±1	_	
the same package	Δt_{wOUT}	$\frac{t_{\text{WOUT}}(Q2) - t_{\text{W}}(Q1)}{t_{\text{WOUT}}(Q1)} \times 100$	10	_	±1	_	%
			15	_	±1		_
Input capacitance	C _{IN}	_		_	5	7.5	pF

Waveform for Measurement of Dynamic Characteristics

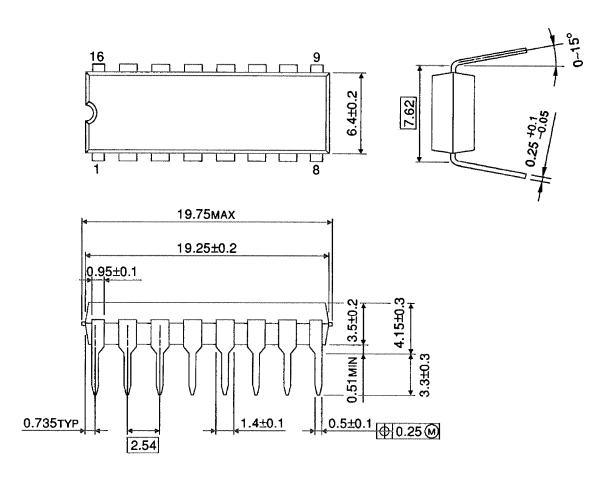
Waveform





Package Dimensions

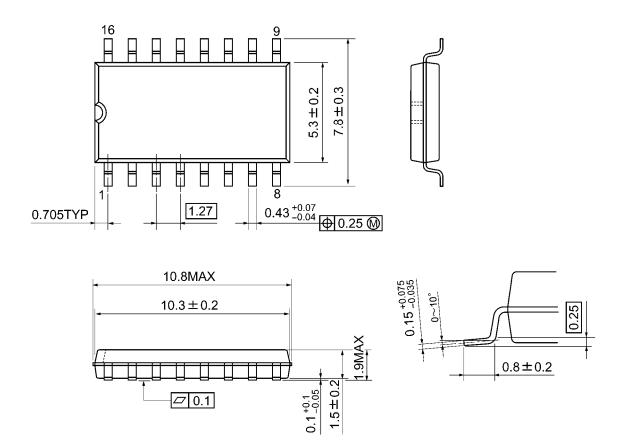
DIP16-P-300-2.54A Unit: mm



Weight: 1.00 g (typ.)

Package Dimensions

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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