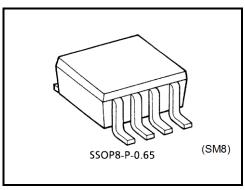
#### TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4W53FU

#### 2-Channel Multiplexer, Demultiplexer

The TC4W53FU is multiplexer with capabilities of selection and mixture of analog signal and digital signal. TC4W53FU has 2 channel configuration. The digital signal to the control terminal turns "ON" the corresponding switch of each channel a large amplitude (VDD – VEE) can be switched by the control signal with small logical amplitude (VDD – VSS). For example, in the case of VDD = 5 V, VSS = 0V and VEE = –5V , signals between –5 V and +5 V can be switched from the logical circuit with a signal power supply of 5 V. As the ON-resistance of each switch is low, these can be connected to circuit with low input impedance.

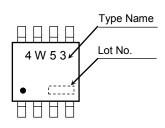


Weight SSOP8-P-0.65: 0.02 g (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Cupply voltage range	$V_{DD}$ - $V_{SS}$	-0.5 to 20	V	
Supply voltage range	V <sub>DD</sub> -V <sub>EE</sub>	-0.5 to 20		
Control input voltage	V <sub>CIN</sub>	$V_{SS}$ – 0.5 to $V_{DD}$ + 0.5	٧	
Switch I/O voltage	V <sub>I/O</sub>	$V_{EE} - 0.5$ to $V_{DD} + 0.5$	٧	
Control input current	I <sub>CIN</sub>	±10	mA	
Potential difference across I/O during ON	V <sub>I-O</sub>	-0.5 to 0.5	٧	
Power dissipation	P <sub>D</sub>	300	mW	
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C	
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

#### Marking



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

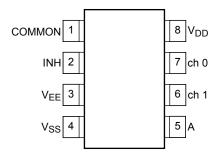
Start of commercial production 1990-05

#### **Truth Table**

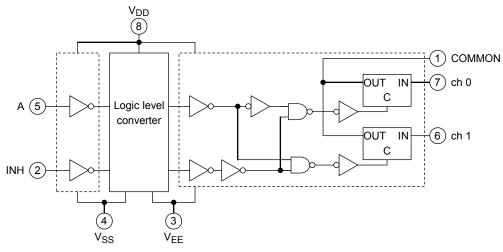
Control Input		On Channel			
INH	Α	On Channel			
L	L	ch 0			
L	Н	ch 1			
Н	Х	none			

X: Don't care

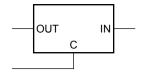
# Pin Assignment (top view)



### **Logic Diagram**



#### **Truth Table**



Control C	Impedance between IN/OUT
Н	$0.5 \text{ to } 5 \times 10^2 \Omega$
L	> 10 <sup>9</sup> Ω

# **Operating Ranges**

Characteristics	Symbol	Min.	Тур.	Max.	Unit	
DC supply voltage	$V_{DD}$ - $V_{SS}$	3	_	18	٧	
DC supply voltage	V <sub>DD</sub> -V <sub>EE</sub>	3	_	18	v	
Control input voltage	V <sub>IN</sub>	$V_{SS}$	_	$V_{DD}$	V	
Switch input/output voltage	V <sub>I/0</sub>	V <sub>EE</sub>	_	$V_{DD}$	V	



#### **Static Electrical Characteristics**

			Test Condition		Ta =	Ta = -40°C		Ta = 25°C			Ta = 85°C		
Characteristics	Symbol		V <sub>SS</sub> (V)	V <sub>EE</sub> (V)	V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
Control input high voltage	V <sub>IH</sub>		V <sub>EE</sub> = V <sub>SS</sub>		5	3.5	_	3.5	2.75		3.5	_	
		$V_{IS} = V_{DD}$			10	7.0	—	7.0	5.50	_	7.0	_	
			$R_L = 1$	l kΩ 2 μA	15	11.0		11.0	8.25	_	11.0	_	V
Control in must law.			on al	OFF	5	_	1.5	_	2.25	1.5	_	1.5	
Control input low voltage	$V_{IL}$	thru 1 kΩ	Chann	ieis	10	_	3.0	_	4.5	3.0	_	3.0	
					15	_	4.0	_	6.75	4.0	_	4.0	
On atata		0 ≤ V <sub>IS</sub>	0	0	5	_	850		240	950	_	1200	
On-state resistance	R <sub>ON</sub>	$\leq V_{DD}$ $R_L = 10 \text{ k}\Omega$	0	0	10	_	210		110	250	_	300	Ω
			0	0	15	_	140		80	160	_	200	
∆On-state	ΔR <sub>ON</sub>	_	0	0	5	_			10		_	_	Ω
resistance (between any 2			0	0	10	_	_	_	6	_	_	_	
switches)			0	0	15	_	_	_	4	_	_	_	
Input/output leakage	I <sub>OFF</sub>	V <sub>IN</sub> = 18 V, V <sub>OUT</sub> = 0 V		0 V	18	_	±100	_	±0.01	±100	_	±1000	nA
current		$V_{IN} = 0 V, V$	OUT = 18 V		18	_	±100	_	±0.01	±100	_	±1000	
	I <sub>DD</sub>		/ <sub>DD</sub> (Note)		5		5.0	_	0.005	5.0		150	μА
Quiescent device current		$V_{IN} = V_{SS}$ , \			10	_	10	_	0.010	10	_	300	
					15	_	20	_	0.015	20	_	600	
Input ourrent	I <sub>IN</sub>	V	V <sub>IL</sub> = 0 V		18	_	0.1	_	10 <sup>-5</sup>	0.1	_	1.0	
Input current		VIH = 10 V,			18	_	-0.1	_	-10 <sup>-5</sup>	-0.1	_	-1.0	μΑ
Input capacitance	C <sub>IN</sub>	-	_		_			_	5	7.5		_	pF
Switch Input Capacitance	C <sub>IN</sub>	_			_	_	_	_	10			_	- pF
Switch Output Capacitance	C <sub>OUT</sub>				10		_	_	17			_	
Feed through capacitance	C <sub>IN</sub> - C <sub>OUT</sub>	_			10		_	_	0.2				pF

Note: All valid input combinations.

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### Dynamic Electrical Characteristics (Ta = 25°C, $C_L = 50$ pF)

Characteristics	Symbol	Test Cond	V <sub>SS</sub> (V)	V <sub>EE</sub> (V)	V <sub>DD</sub> (V)	Min	Тур.	Max	Unit	
Phase difference between				0	0	5	_	15	45	
input to output	фІ-О	_	0	0	10	_	8	20	ns	
(switch IN-OUT)				0	0	15	_	6	15	
				0	0	5	_	170	550	
Dranagation delay time	t <sub>pZL</sub>	$R_L = 1 \text{ k}\Omega$		0	0	10	_	90	240	
Propagation delay time (A-OUT)	t <sub>pZH</sub>			0	0	15	_	70	160	ns
(A-001)	t <sub>pLZ</sub>			0	-5	5	_	100	240	
	t <sub>pHZ</sub>			0	-7.5	7.5	_	80	160	
			0	0	5	_	120	380	ns	
		$R_L = 1 \text{ k}\Omega$		0	0	10	_	60		200
	t <sub>pZL</sub>			0	0	15	_	50		160
				0	-5	5	_	80		200
Propagation delay time			0	-7.5	7.5	_	60	160		
(INH-OUT)	t <sub>pLZ</sub>	R <sub>L</sub> = 1 kΩ		0	0	5	_	170	450	ns
				0	0	10	_	90	210	
				0	0	15	_	70	160	
				0	-5	5	_	100	210	
				0	-7.5	7.5	_	80	160	
Frequency response	f <sub>MAX</sub> (I-O)	$R_L = 1 \text{ k}\Omega$	(Note 1)	-5	-5	5	_	40	_	MHz
		$R_L = 10 \text{ k}\Omega$ $f = 1 \text{ kH}_Z$	(Note 2)	-2.5	-2.5	2.5	_	0.15	_	%
Total harmonic distortion	_			-5	-5	5	_	0.03	_	
				-7.5	-7.5	7.5	_	0.02	_	
Feedthrough frequency (switch off)	_	$R_L = 1 \text{ k}\Omega$	(Note 3)	-5	-5	5	_	500	_	kHz
Crosstalk frequency	_	$R_L = 1 \text{ k}\Omega$	(Note 4)	-5	-5	5	_	1.5	_	MHz
0	_	$R_{IN} = 1  k\Omega$ $R_{OUT} = 10  k\Omega$ $C_L = 15  pF$		0	0	5	_	200	_	mV
Crosstalk				0	0	10	_	400	_	
(CONTROL-OUT)				0	0	15	_	600	_	

Note 1: Sine wave of  $\pm 2.5 \ V_{p-p}$  shall be used for  $V_{IS}$  and the frequency of 20 log  $_{10} \ \frac{V_{OS}}{V_{IS}} = -3 \text{dB}$  shall be  $f_{MAX}$ .

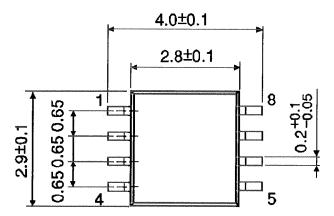
Note 2: VIS shall be sine wave of  $\pm \left( \frac{V_{\text{DD}} - V_{\text{EE}}}{4} \right)_{p\text{-}p}$  .

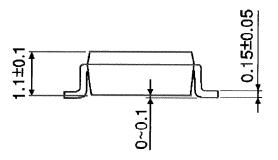
Note 3: Sine wave of  $\pm 2.5~V_{p-p}$  shall be used for  $V_{IS}$  and the frequency of 20  $\log_{10} \frac{V_{OS}}{V_{IS}} = -50 dB$  shall be feed-through.

Note 4: Sine wave of  $\pm 2.5~V_{p-p}$  shall be used for VIS and the frequency of 20 log  $\frac{V_{OS}}{V_{IS}}=-50 \text{dB}$  shall be crosstalk.

# **Package Dimensions**

SSOP8-P-0.65 Unit: mm





Weight: 0.02 g (typ.)

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