Unit: mm



TOSHIBA Field-Effect Transistor Silicon N Channel MOS Type

SSM3K36FS

○ High-Speed Switching Applications

1.5-V drive
 Low ON-resistance : Ron = 1.52 Ω (max) (@VGS = 1.5 V)

: Ron = 1.14 Ω (max) (@VGS = 1.8 V)

: Ron = 0.85Ω (max) (@VGS = 2.5 V)

: Ron = 0.66Ω (max) (@VGS = 4.5 V)

: Ron = 0.63Ω (max) (@VGS = 5.0 V)

Absolute Maximum Ratings (Ta = 25 °C)

| Characteristics | | Symbol | Rating | Unit | |
|---------------------------|-------|-------------------------|------------|------|--|
| Drain-source voltage | | V _{DS} | 20 | V | |
| Gate-source voltage | | Vgss | ± 10 | V | |
| Drain current | DC | ΙD | 500 | mA | |
| | Pulse | IDP | 1000 | | |
| Drain power dissipation | | P _D (Note 1) | 150 | mW | |
| Channel temperature | | T _{ch} | 150 | °C | |
| Storage temperature range | | T _{stg} | -55 to 150 | °C | |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

1. GATE
2. SOURCE
3. DRAIN

SSM

JEDEC

JEITA

TOSHIBA

2-2H1B

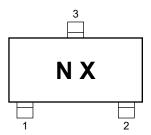
Weight: 2.4 mg (typ.)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/ "Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

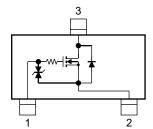
Note 1: Mounted on an FR4 board

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ mm}, \text{ Cu Pad: } 0.36 \text{ mm}^2 \times 3)$

Marking



Equivalent Circuit (top view)



Start of commercial production 2008-02

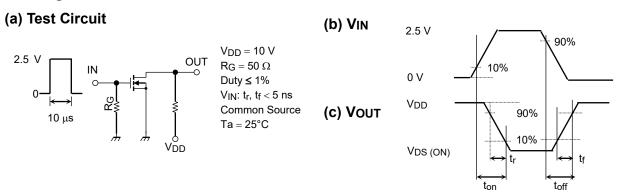


Electrical Characteristics (Ta = 25°C)

| Chara | cteristics | Symbol | Test Conditions | Min | Тур. | Max | Unit | |
|--|---------------|--|--|------|-------|------|------|--|
| Duning any many law and allowers well to the | V (BR) DSS | I _D = 1 mA, V _{GS} = 0 V | 20 | _ | _ | V | | |
| Drain-source breakdown voltage | | V (BR) DSX | I _D = 1 mA, V _{GS} = - 10 V | 12 | _ | _ | | |
| Drain cutoff current | | IDSS | V _{DS} = 20 V, V _{GS} = 0 V | _ | — | 1 | μА | |
| Gate leakage current | | IGSS | $V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | — | ±1 | μА | |
| Gate threshold volt | age | V _{th} | $V_{DS} = 3 \text{ V}, I_{D} = 1 \text{ mA}$ | 0.35 | — | 1.0 | V | |
| Forward transfer ad | dmittance | Y _{fs} | $V_{DS} = 3 \text{ V}, I_D = 200 \text{ mA}$ (Note 2) | 420 | 840 | _ | mS | |
| Drain-source ON-resistance | RDS (ON) | I _D = 200 mA, V _G S = 5.0 V (Note 2) | _ | 0.46 | 0.63 | Ω | | |
| | | I _D = 200 mA, V _G S = 4.5 V (Note 2) | _ | 0.51 | 0.66 | | | |
| | | I _D = 200 mA, V _{GS} = 2.5 V (Note 2) | _ | 0.66 | 0.85 | | | |
| | | I _D = 100 mA, V _{GS} = 1.8 V (Note 2) | _ | 0.81 | 1.14 | | | |
| | | I _D = 50 mA, V _G S = 1.5 V (Note 2) | _ | 0.95 | 1.52 | | | |
| Input capacitance Output capacitance | | Ciss | | _ | 46 | _ | pF | |
| | | Coss | V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz | _ | 10.8 | _ | | |
| Reverse transfer capacitance | | C _{rss} | | _ | 7.3 | _ | | |
| Total Gate Charge | | Qg | | _ | 1.23 | _ | nC | |
| Gate-Source Charge | | Q _{gs} | $V_{DS} = 10 \text{ V}, I_{D} = 0.5 \text{ A}$ | _ | 0.60 | _ | | |
| Gate–Drain Charge | | Q _{gd} | V _{GS} = 4.0 V | _ | 0.63 | _ | | |
| Switching time | Turn-on time | ton | V _{DD} = 10 V, I _D = 200 mA | _ | 30 | _ | ns | |
| | Turn-off time | toff | $V_{GS} = 0$ to 2.5 V, $R_{G} = 50 \Omega$ | _ | 75 | _ | | |
| Drain-source forward voltage | | VDSF | $I_D = -0.5 \text{ A}, V_{GS} = 0 \text{ V}$ (Note 2) | _ | -0.88 | -1.2 | V | |

Note 2: Pulse test

Switching Time Test Circuit

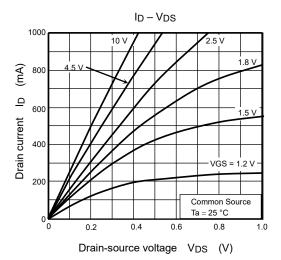


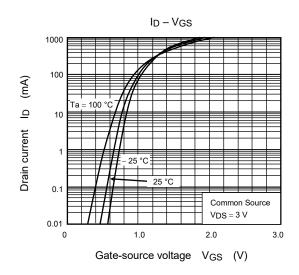
Usage Considerations

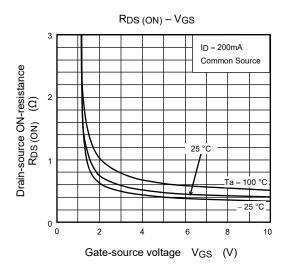
Let Vth be the voltage applied between gate and source that causes the drain current (ID) to below (1 mA for the SSM3K36FS). Then, for normal switching operation, VGS(on) must be higher than Vth, and VGS(off) must be lower than Vth. This relationship can be expressed as: VGS(off) < Vth < VGS(on). Take this into consideration when using the device.

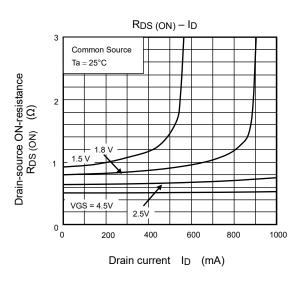
Handling Precaution

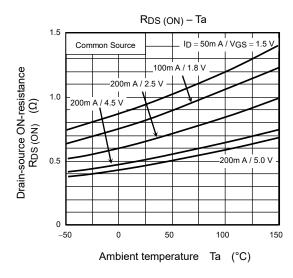
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

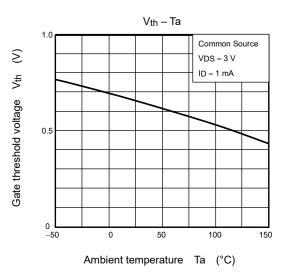




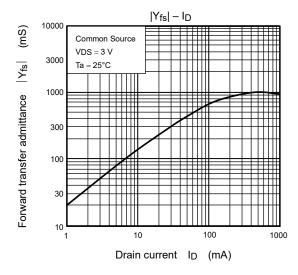


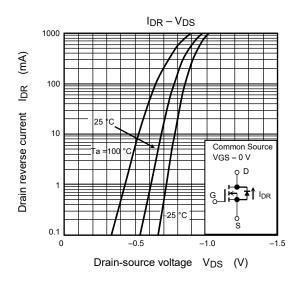


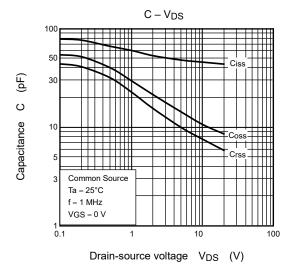


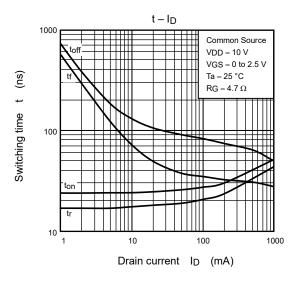


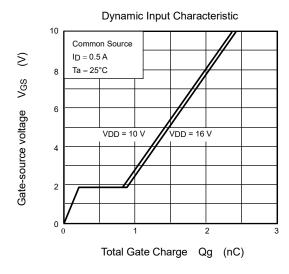
The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

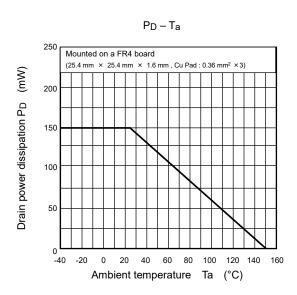












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