MOSFETs Silicon N-channel MOS (U-MOSIX-H)

TK6R7A10PL

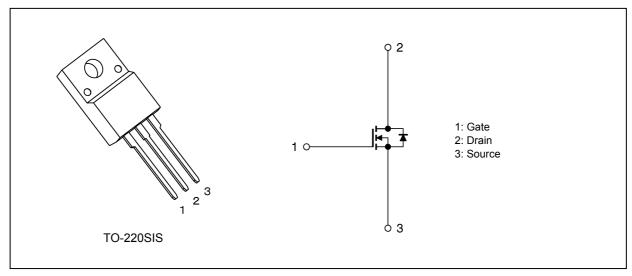
1. Applications

- High-Efficiency DC-DC Converters
- Switching Voltage Regulators
- Motor Drivers

2. Features

- (1) High-speed switching
- (2) Small gate charge: $Q_{SW} = 17 \text{ nC}$ (typ.)
- (3) Small output charge: $Q_{oss} = 58 \text{ nC}$ (typ.)
- (4) Low drain-source on-resistance: $R_{DS(ON)} = 5.7 \text{ m}\Omega$ (typ.) (V_{GS} = 10 V)
- (5) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 100 \ V)$
- (6) Enhancement mode: V_{th} = 1.5 to 2.5 V (V_{DS} = 10 V, I_D = 0.5 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25 \ ^{\circ}C$ unless otherwise specified)

Character	Symbol	Rating	Unit		
Drain-source voltage			V _{DSS}	100	V
Gate-source voltage			V _{GSS}	±20	
Drain current (DC)	(T _c = 25 °C)	(Note 1)	Ι _D	56	A
Drain current (pulsed)	(t = 100 μs)	(Note 1)	I _{DP}	300	Α
Power dissipation	(T _c = 25 °C)		PD	42	W
Single-pulse avalanche energy		(Note 2)	E _{AS}	57	mJ
Single-pulse avalanche current		(Note 2)	I _{AS}	56	А
Channel temperature			T _{ch}	175	°C
Storage temperature			T _{stg}	-55 to 175	°C
Isolation voltage (RMS)	(t = 1.0 s)		V _{ISO(RMS)}	2000	V
Mounting torque			TOR	0.6	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Channel-to-case thermal resistance	(T _c = 25 °C)	R _{th(ch-c)}	3.57	°C/W
Channel-to-ambient thermal resistance	(T _a = 25 °C)	R _{th(ch-a)}	62.5	

Note 1: Ensure that the channel temperature does not exceed 175 °C. Note 2: V_{DD} = 80 V, T_{ch} = 25 °C (initial), L = 14 μ H, I_{AS} = 56 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

6.1. Static Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V	_	_	±0.1	μA
Drain cut-off current	I _{DSS}	V_{DS} = 100 V, V_{GS} = 0 V	_	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	100	_	—	V
Drain-source breakdown voltage (Note 3)	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	65	_	_	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 0.5 mA	1.5		2.5	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 4.5 V, I _D = 16 A		7.2	10.1	mΩ
		V _{GS} = 10 V, I _D = 28 A	_	5.7	6.7	

Note 3: If a reverse bias is applied between gate and source, this device enters V_{(BR)DSX} mode. Note that the drainsource breakdown voltage is lowered in this mode.

6.2. Dynamic Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz	_	3455	_	pF
Reverse transfer capacitance	C _{rss}		—	30	—	
Output capacitance	C _{oss}		_	520	_	
Gate resistance	r _g	—	_	2.4	_	Ω
Switching time (rise time)	t _r	See Fig. 6.2.1	_	12	—	ns
Switching time (turn-on time)	t _{on}		_	29	_	
Switching time (fall time)	t _f		_	14	_	
Switching time (turn-off time)	t _{off}		_	67	_	

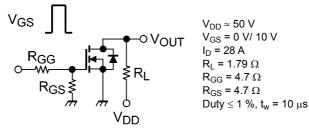


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus	Qg	$V_{DD} \approx 50$ V, V_{GS} = 10 V, I_D = 28 A	_	58	_	nC
gate-drain)		$V_{DD} \approx 50$ V, V_{GS} = 4.5 V, I_D = 28 A	_	30	—	
Gate-source charge 1	Q _{gs1}	$V_{DD} \approx 50$ V, V_{GS} = 10 V, I_D = 28 A	_	12	—	
Gate-drain charge	Q _{gd}		_	12	—	
Gate switch charge	Q _{SW}		_	17	_	
Output charge	Q _{oss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz	_	58	_	

6.4. Source-Drain Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed) (Note 4)	I _{DRP}	t = 100 μs	_	_	300	Α
Diode forward voltage	V _{DSF}	I _{DR} = 56 A, V _{GS} = 0 V	_	—	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 14 A, V _{GS} = 0 V,	_	52	_	ns
Reverse recovery charge	Q _{rr}	-dI _{DR} /dt = 100 A/µs	_	80	_	nC

Note 4: Ensure that the channel temperature does not exceed 175 °C.

7. Marking (Note)

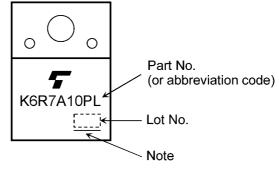


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

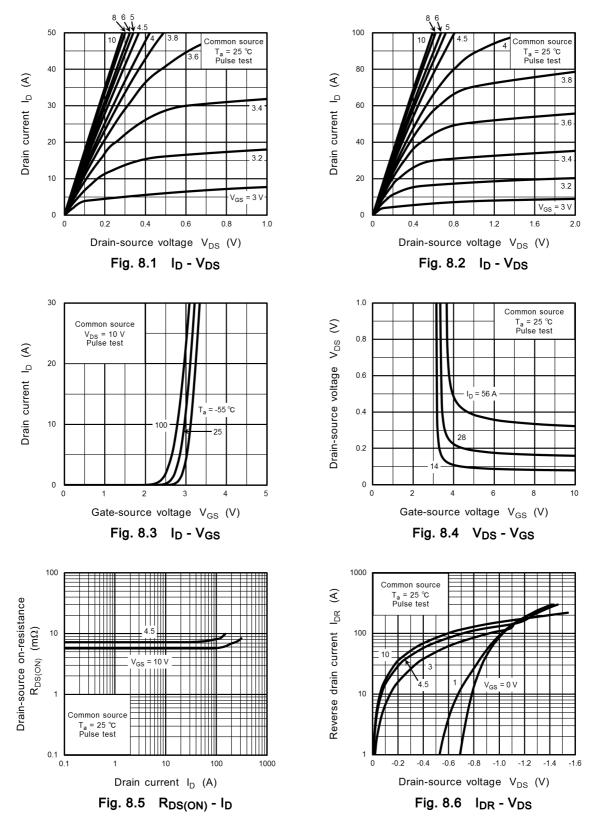
Not underlined: [[Pb]]/INCLUDES > MCV

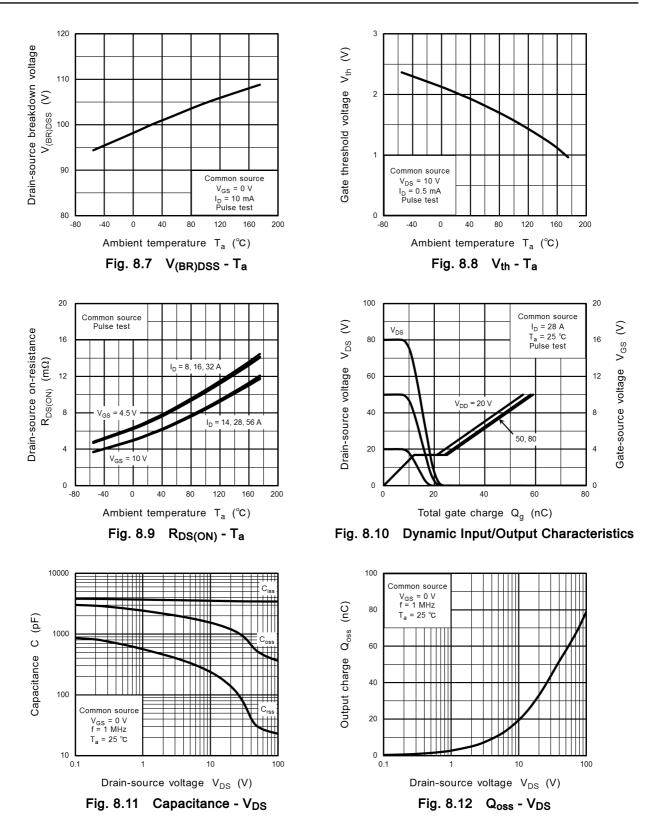
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

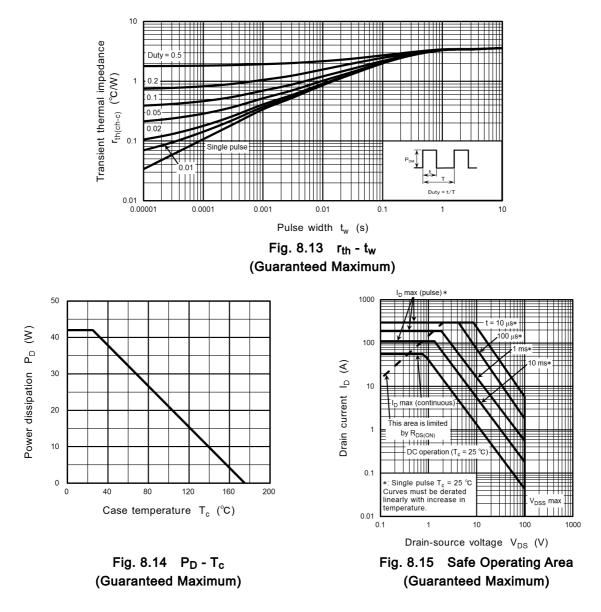
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8. Characteristics Curves (Note)





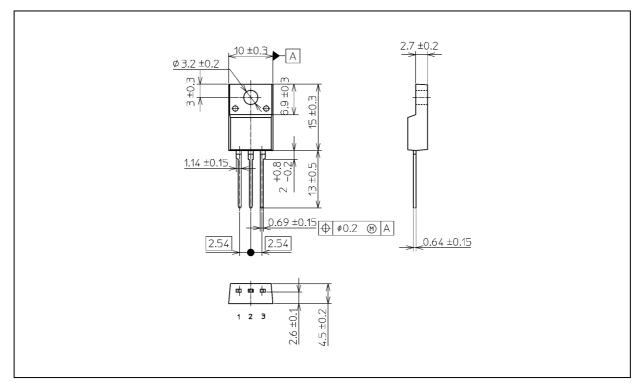


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

TK6R7A10PL

Package Dimensions

Unit: mm



Weight: 1.56 g (typ.)

Package Name(s)			
TOSHIBA: 2-10U1S			
Nickname: TO-220SIS			

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