TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK1828

High Speed Switching Applications Unit: mm Analog Switch Applications +0.2 2.5 V gate drive Low threshold voltage: $V_{th} = 0.5$ to 1.5 V High speed Enhancement-mode Small package 1. GATE **Equivalent Circuit** 2. SOURCE Marking 3. DRAIN S-MINI ΚI JEDEC TO-236MOD JEITA SC-59 TOSHIBA 2-3F1F Weight: 0.012 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V DS	20	v
Gate-source voltage	VGSS <	10	V
DC drain current	ID	50	mA
Drain power dissipation	PD	200	mW
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

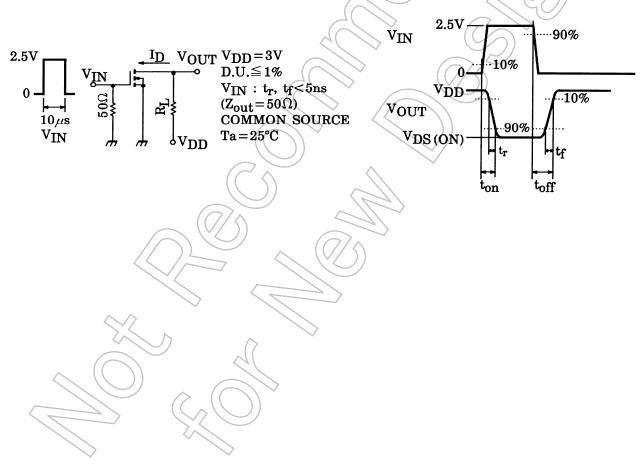
Note: This transistor is electrostatic sensitive device. Please handle with caution.

> Start of commercial production 1991-02

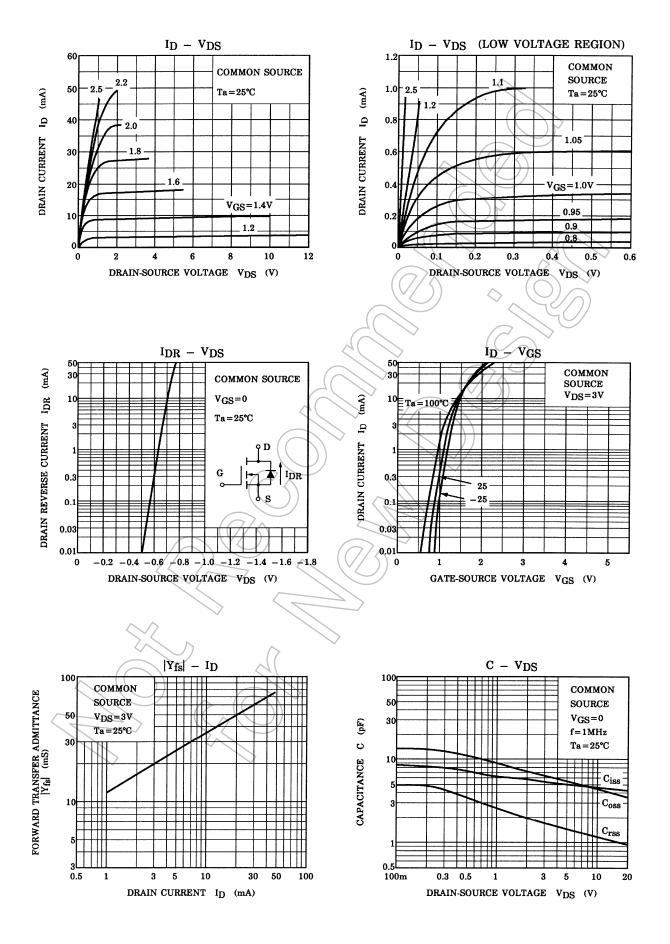
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = 10 \text{ V}, V_{DS} = 0$			1	μA
Drain-source breat	kdown voltage	V (BR) DSS	$I_D = 100 \ \mu A, \ V_{GS} = 0$	20	_	_	V
Drain cut-off current	nt	I _{DSS}	$V_{DS} = 20 V, V_{GS} = 0$	X		1	μA
Gate threshold vol	tage	V _{th}	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 0.1 \text{ mA}$	0.5		1.5	V
Forward transfer a	dmittance	Y _{fs}	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 10 \text{ mA}$	20)}		mS
Drain-source ON r	esistance	R _{DS (ON)}	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	77	20	40	Ω
Input capacitance		C _{iss}	$V_{DS} = 3 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	$\mathcal{O}\mathcal{H}$	5.5		pF
Reverse transfer c	apacitance	C _{rss}	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		1.6	_	pF
Output capacitanc	е	C _{oss}	$V_{DS} = 3 V$, $V_{GS} = 0$, f = 1 MHz	_	6.5	_	pF
Switching time	Turn-on time	t _{on}	V_{DD} = 3 V, I_D = 10 mA, V_{GS} = 0 to 2.5 V		0.14	_	μs
	Turn-off time	t _{off}	V_{DD} = 3 V, I_D = 10 mA, V_{GS} = 0 to 2.5 V	_	0.14	\searrow	

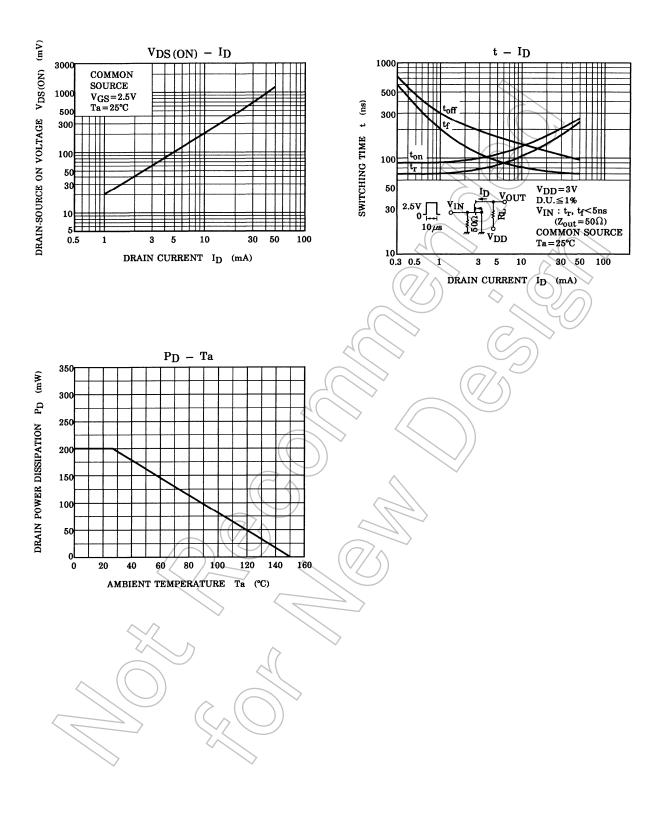
Switching Time Test Circuit



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