CMOS Digital Integrated Circuits Silicon Monolithic

TC7SBL66CFU,TC7SBL384CFU

1. Functional Description

- Low-Capacitance Single Bus Switch (analog)

2. General

The TC7SBL66CFU and TC7SBL384CFU are a Low Voltage / Low Capacitance CMOS single Bus Switch. The low On-resistance of the switch allows connections to be made with minimal propagation delay time.

The TC7SBL66CFU requires the output enable (OE) input to be set low to place the output into the high impedance state,whereas the TC7SBL384CFU requires the output enable ($\overline{OE}$) input to be set high to place the output into the high impedance.

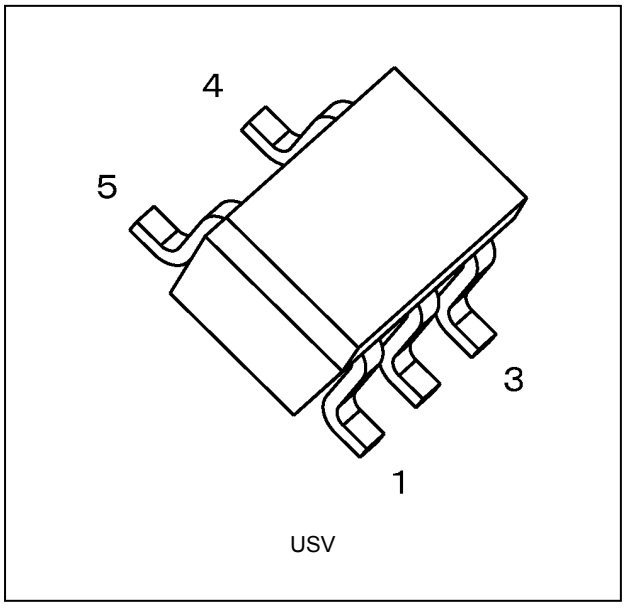
All inputs are equipped with protection circuits against static discharge.

3. Features

(1) Wide operating temperature range: T_{opr} = -40 to 125 °C (Note 1)
(2) Operating voltage: V_{CC} = 1.65 to 3.6 V
(3) ON capacitance: $C_{I/O}$ = 7 pF Switch On (typ.) @V_{CC} = 3.0 V
(4) ON resistance: R_{ON} = 5.5 Ω (typ.) @V_{CC} = 3 V, V_{IS} = 0 V
(5) Power-down protection for inputs (OE and $\overline{OE}$, I/O)
(6) Package: USV

Note 1: For devices with the ordering part number ending in (CT. T_{opr} = -40 to 85 °C for the other devices.

4. Packaging

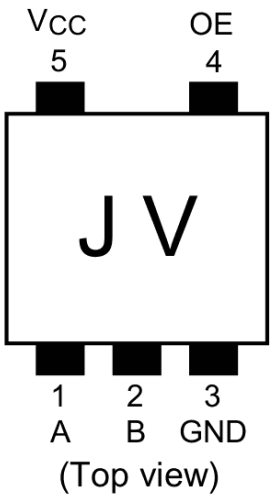


USV

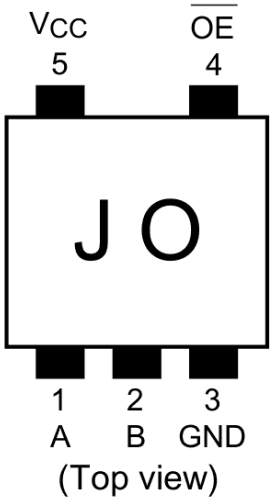
Start of commercial production
2020-12

5. Pin Assignment

TC7SBL66CFU

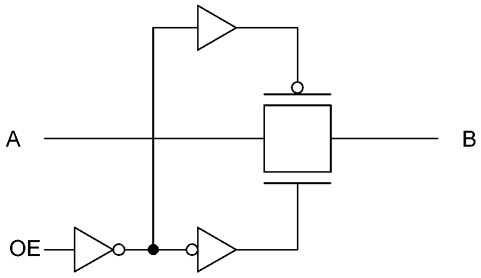


TC7SBL384CFU

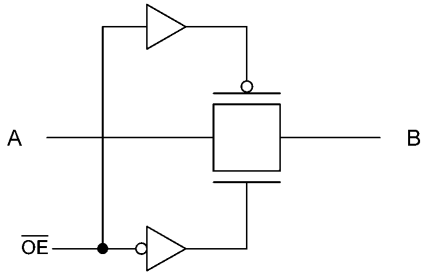


6. Block Diagram

TC7SBL66CFU



TC7SBL384CFU



7. Principle of Operation

7.1. Truth Table

Inputs OE (TC7SBL66CFU)	Inputs $\overline{OE}$ (TC7SBL384CFU)	Function
H	L	A port = B port
L	H	Disconnect

8. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics		Symbol	Note	Rating	Unit
Supply voltage		V_{CC}		-0.5 to 4.6	V
Input voltage (OE, $\overline{OE}$)		V_{IN}		-0.5 to 4.6	V
Switch I/O voltage	V_{CC} = 0 V or Switch = Off	V_S		-0.5 to 4.6	V
	Switch = On			-0.5 to V_{CC} +0.5	
Clamp diode current		I_{IK}		-50	mA
Switch I/O current		I_S		50	mA
Power dissipation		P_D		200	mW
V_{CC}/ground current		I_{CC}/I_{GND}		±100	mA
Storage temperature		T_{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

9. Operating Ranges (Note)

Characteristics		Symbol	Note	Rating	Unit
Supply voltage		V_{CC}		1.65 to 3.6	V
Input voltage (OE, $\overline{OE}$)		V_{IN}		0 to 3.6	V
Switch I/O voltage	V_{CC} = 0 V or Switch = Off	V_S		0 to 3.6	V
	Switch = On			0 to V_{CC}	
Operating temperature		T_{opr}	(Note 1)	-40 to 125	°C
			(Note 2)	-40 to 85	
Input rise time		dt/dv		0 to 10	ns/V
Input fall time				0 to 10	

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused control inputs must be tied to either V_{CC} or GND.
Note 1: For devices with the ordering part number ending in (CT.
Note 2: For devices except those with the ordering part number ending in (CT.

10. Electrical Characteristics

10.1. DC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V_{CC} (V)	Min	Typ.	Max	Unit
High-level input voltage (OE, $\overline{OE}$)	V_{IH}		—	1.65 to 3.6	0.7 × V_{CC}	—	—	V
Low-level input voltage (OE, $\overline{OE}$)	V_{IL}		—	1.65 to 3.6	—	—	0.3 × V_{CC}	V
Input leakage current (OE, $\overline{OE}$)	I_{IN}		V_{IN} = 0 to 3.6 V	1.65 to 3.6	—	—	±1.0	μA
Power-OFF leakage current	I_{OFF}		OE, $\overline{OE}$, A, B = 0 to 3.6 V	0	—	—	10	μA
Switch OFF-state leakage current	I_{SZ}		A, B = 0 to V_{CC}, OE = GND(TC7SBL66CFU), $\overline{OE}$ = V_{CC}(TC7SBL384CFU)	1.65 to 3.6	—	—	±1.0	μA
ON-resistance	R_{ON}	(Note 1), (Note 2)	V_{IS} = 0 V, I_{IS} = 30 mA	3.0	—	5.5	10	Ω
			V_{IS} = 3.0 V, I_{IS} = 30 mA	3.0	—	10	16	
			V_{IS} = 2.4 V, I_{IS} = 15 mA	3.0	—	12	18	
			V_{IS} = 0 V, I_{IS} = 24 mA	2.3	—	6	10	
			V_{IS} = 2.3 V, I_{IS} = 24 mA	2.3	—	13	20	
			V_{IS} = 2.0 V, I_{IS} = 15 mA	2.3	—	15	21	
			V_{IS} = 0 V, I_{IS} = 4 mA	1.65	—	7	13	
			V_{IS} = 1.65 V, I_{IS} = 4 mA	1.65	—	18	27	
Quiescent supply current	I_{CC}		V_{IN} = V_{CC} or GND, I_{OUT} = 0 A	3.6	—	—	10	μA

Note 1: All typical values are at T_a = 25 °C.

Note 2: Measured by the voltage drop between A and B pins at the indicated current through the switch. On-resistance is determined by the lower of the voltages on the two (A or B) pins.

10.2. DC Characteristics (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Note	Test Condition	V_{CCB} (V)	Min	Max	Unit
High-level input voltage (OE, $\overline{OE}$)	V_{IH}		—	1.65 to 3.6	0.7 × V_{CC}	—	V
Low-level input voltage (OE, $\overline{OE}$)	V_{IL}		—	1.65 to 3.6	—	0.3 × V_{CC}	V
Input leakage current (OE, $\overline{OE}$)	I_{IN}		V_{IN} = 0 to 3.6 V	1.65 to 3.6	—	±10.0	μA
Power-OFF leakage current	I_{OFF}		OE, $\overline{OE}$, A, B = 0 to 3.6 V	0	—	40	μA
Switch OFF-state leakage current	I_{SZ}		A, B = 0 to V_{CC}, $\overline{OE}$ = GND(TC7SBL66CFU), $\overline{OE}$ = V_{CC}(TC7SBL384CFU)	1.65 to 3.6	—	±10.0	μA
ON-resistance	R_{ON}	(Note 1), (Note 2)	V_{IS} = 0 V, I_{IS} = 30 mA	3.0	—	13	Ω
			V_{IS} = 3.0 V, I_{IS} = 30 mA	3.0	—	19	
			V_{IS} = 2.4 V, I_{IS} = 15 mA	3.0	—	21	
			V_{IS} = 0 V, I_{IS} = 24 mA	2.3	—	13	
			V_{IS} = 2.3 V, I_{IS} = 24 mA	2.3	—	23	
			V_{IS} = 2.0 V, I_{IS} = 15 mA	2.3	—	24	
			V_{IS} = 0 V, I_{IS} = 4 mA	1.65	—	16	
			V_{IS} = 1.65 V, I_{IS} = 4 mA	1.65	—	30	
Quiescent supply current	I_{CC}		V_{IN} = V_{CC} or GND, I_{OUT} = 0 A	3.6	—	40	μA

Note 1: All typical values are at T_a = 25 °C.

Note 2: Measured by the voltage drop between A and B pins at the indicated current through the switch. On-resistance is determined by the lower of the voltages on the two (A or B) pins.

10.3. AC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Test Condition	V_{CC} (V)	Min	Max	Unit
Output enable time	t_{PZL},t_{PZH}	See Fig. 11.1, 11.2, Table 11.1.1	3.3 ± 0.3	—	6	ns
			2.5 ± 0.2	—	7	
			1.8 ± 0.15	—	11	
Output disable time	t_{PLZ},t_{PHZ}	See Fig. 11.1, 11.2, Table 11.1.1	3.3 ± 0.3	—	6	ns
			2.5 ± 0.2	—	7	
			1.8 ± 0.15	—	11	

10.4. AC Characteristics (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition	V_{CC} (V)	Min	Max	Unit
Output enable time	t_{PZL},t_{PZH}	See Fig. 11.1, 11.2, Table 11.1.1	3.3 ± 0.3	—	8	ns
			2.5 ± 0.2	—	9	
			1.8 ± 0.15	—	13	
Output disable time	t_{PLZ},t_{PHZ}	See Fig. 11.1, 11.2, Table 11.1.1	3.3 ± 0.3	—	8	ns
			2.5 ± 0.2	—	9	
			1.8 ± 0.15	—	13	

10.5. Capacitive Characteristics (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Test Condition	V_{CC} (V)	Typ.	Unit
Input capacitance (OE, $\overline{OE}$)	C_{IN}	V_{IN} = 0 V	3.0	4	pF
Switch terminal OFF-capacitance	$C_{I/O}$	OE = GND(TC7SBL66CFU), $\overline{OE}$ = V_{CC}(TC7SBL384CFU), V_{IS} = 0 V	3.0	3.5	pF
Switch terminal ON-capacitance	$C_{I/O}$	OE = V_{CC}(TC7SBL66CFU), $\overline{OE}$ = GND(TC7SBL384CFU), V_{IS} = 0 V	3.0	7	pF

Note: Parameter guaranteed by design.

11. AC Electrical Test Circuit

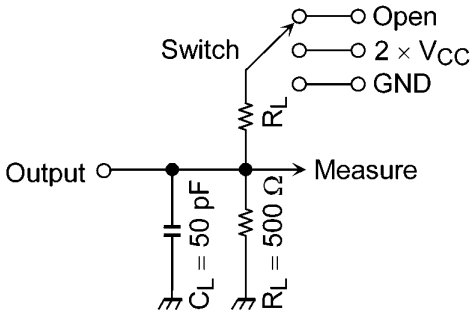


Fig. 11.1 AC Test Circuit

Table 11.1.1 Parameter for AC Test Circuit

Parameter	Switch
t_{PLZ}, t_{PZL}	2 × V_{CC}
t_{PHZ}, t_{PZH}	GND

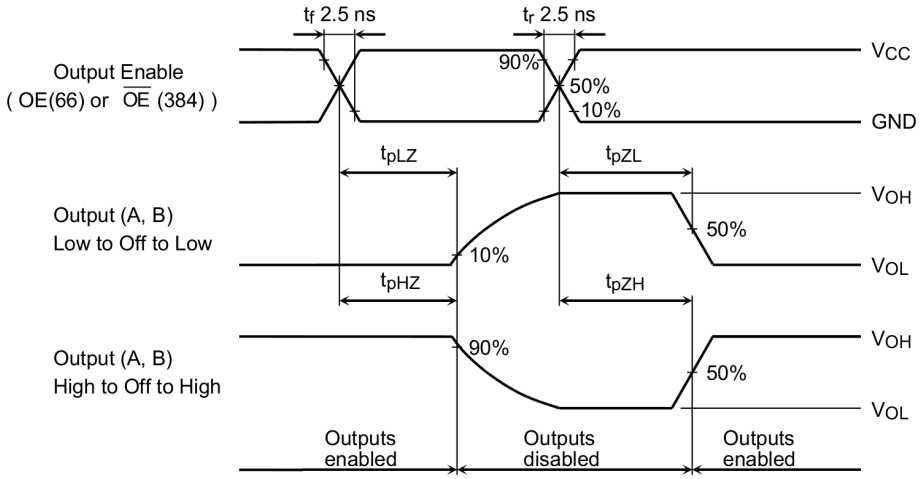


Fig. 11.2 AC Waveform t_{PLZ}, t_{PHZ}, t_{PZL}, t_{PZH}

12. Rise and Fall Time (t_r/t_f)

The $t_{r(out)}$ and $t_{f(out)}$ values of the output signals are affected by the CR time constant of the input, which consists of the switch terminal capacitance ($C_{I/O}$) and the on-resistance (R_{ON}) of the input.

In practice, the $t_{r(out)}$ and $t_{f(out)}$ values are also affected by the circuit's capacitance and resistance components other than the capacitance of TC7SBL66CFU, TC7SBL384CFU

The $t_r/t_{f(out)}$ values can be approximated as follows.

(Figure 12.1, Table 12.1 shows the test circuit.)

$t_r/t_{f(out)}$ (approx) = - ($C_{I/O}$ + C_L) · (R_{DRIVE} + R_{ON}) · ln (((V_{OH} - V_{OL}) - V_M) / (V_{OH} - V_{OL}))

Where, R_{DRIVE} is the output impedance of the previous-stage circuit.

Calculation example:

$t_r/t_{f(out)}$ (approx) = - (7 + 15) E - 12 · (120 + 5.5) · ln ((3.0 - 0) - 1.5) / (3.0 - 0)) = ≈1.9 ns

Calculation conditions:

V_{CC} = 3.0 V, C_L = 15 pF, R_{DRIVE} = 120 Ω (output impedance of the previous IC), V_M = 1.5 V (V_{CC}/2)

Output of the previous IC = digital (i.e., high-level voltage = V_{CC}, low-level voltage = GND)

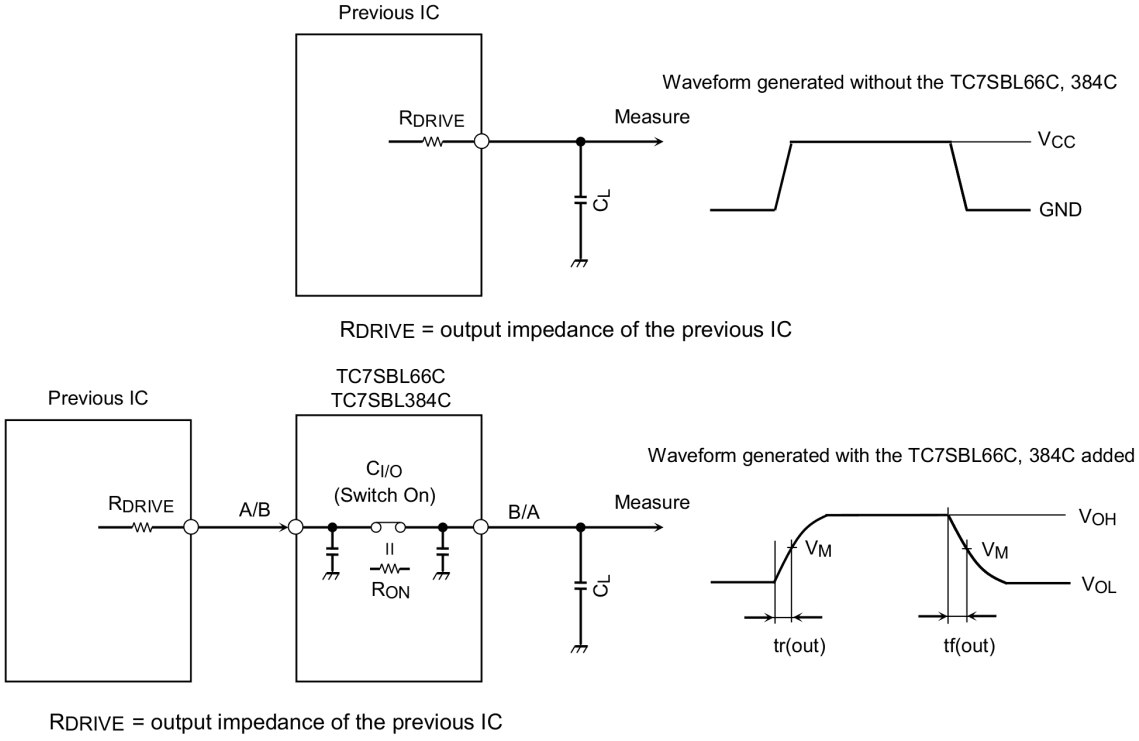


Fig. 12.1 Calculation Circuit

Table 12.1 Calculation Circuit

Characteristics	V_{CC} = 3.3 ± 0.3 V	V_{CC} = 2.5 ± 0.2 V	V_{CC} = 1.8 ± 0.15 V
V_M	V_{CC}/2	V_{CC}/2	V_{CC}/2

13. Characteristic (Note)

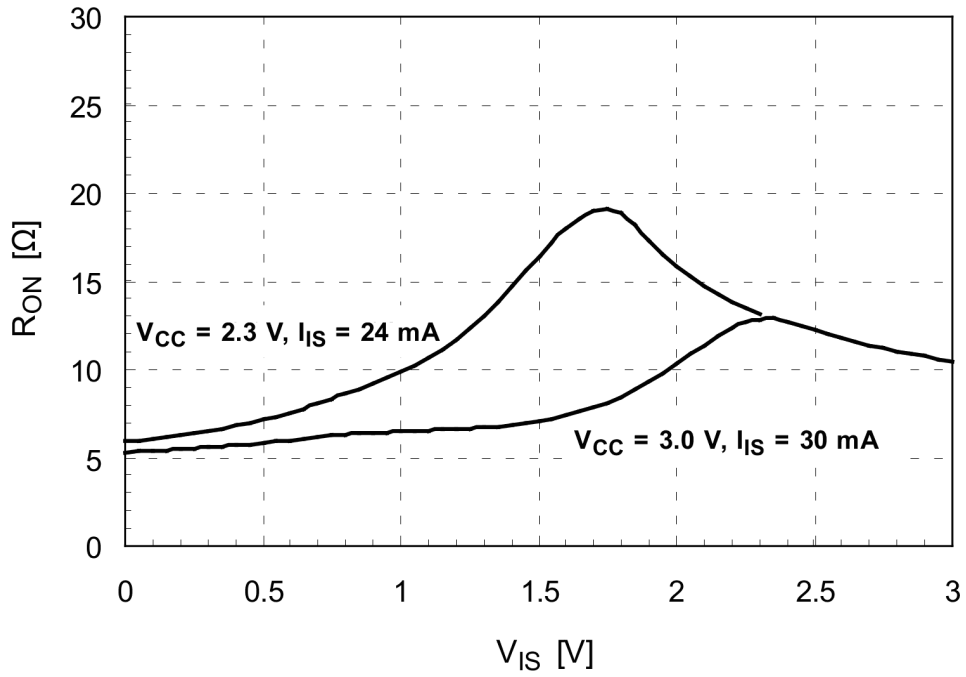
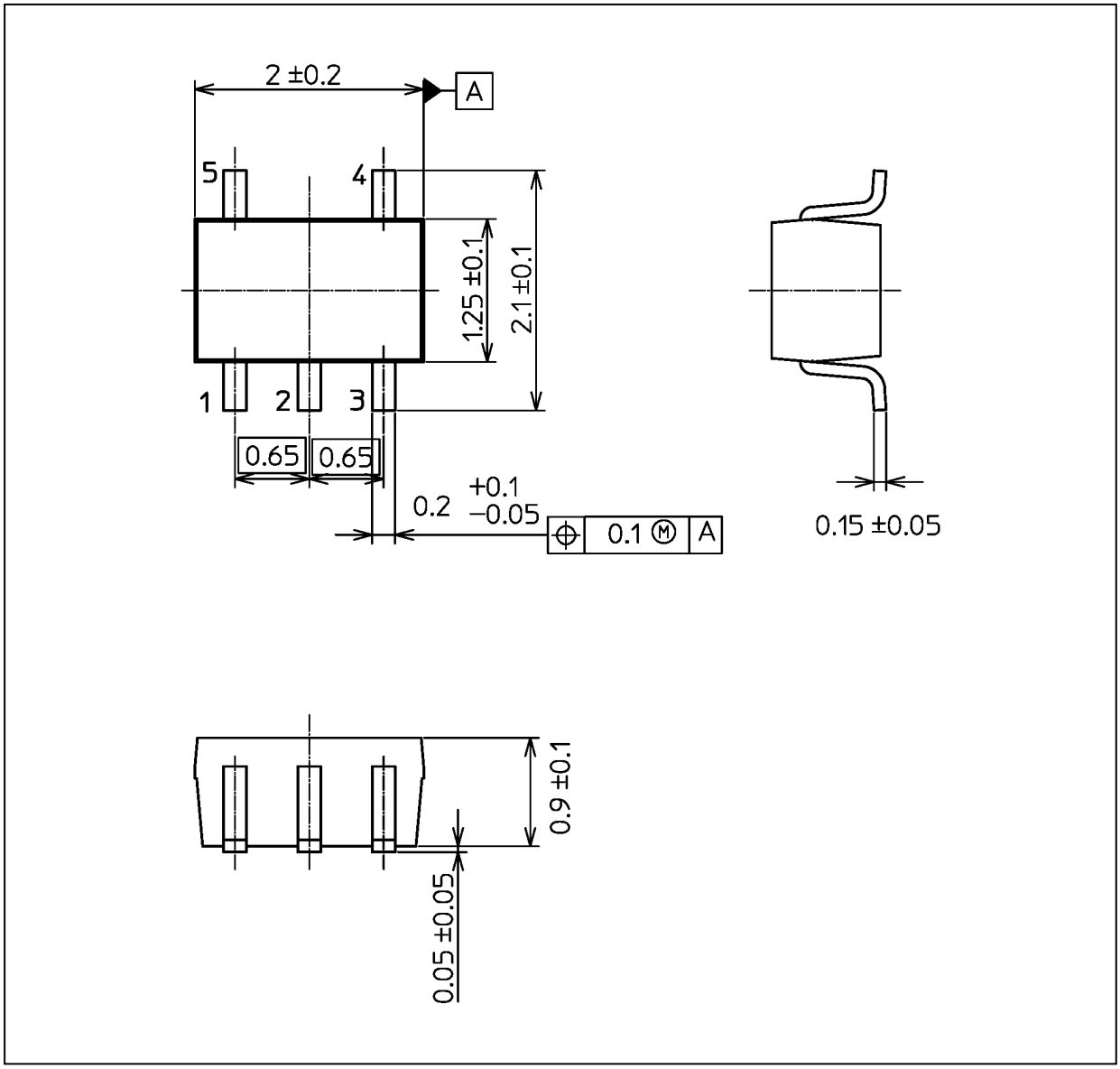


Fig. 13.1 R_{ON} - V_{IS} (Typ.) (T_a = 25°C)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

Unit: mm



Weight: 0.006 g (typ.)

Package Name(s)
JEDEC: SOT-353
Nickname: USV

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