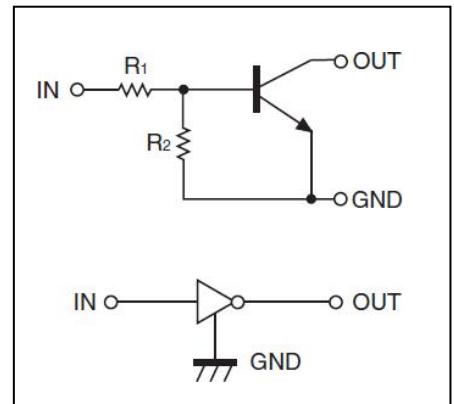


## **DTC123JE**

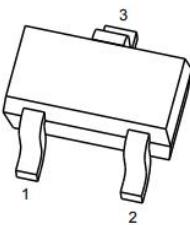
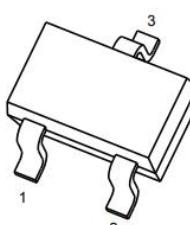
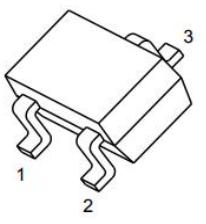
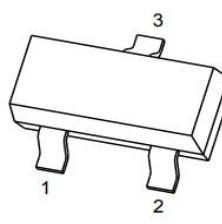
## **NPN Silicon Epitaxial Planar Digital Transistor**

### **FEATURES**

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy



### **PIN CONNECTIONS and MARKING**

<b>DTC123JE</b>		<b>SOT-523</b>	<b>DTC123JUA</b>		<b>SOT-323</b>
<b>DTC123JKA</b>		<b>SOT-23-3L</b>	<b>DTC123JCA</b>		<b>SOT-23</b>

### **ORDERING INFORMATION**

Part Number	MARKING	Package	Packing Method	Pack Quantity
DTC123JE	<b>E42</b>	SOT-523	Reel	3000pcs/Reel
DTC123JUA	<b>E42</b>	SOT-323	Reel	3000pcs/Reel
DTC123JKA	<b>E42</b>	SOT-23-3L	Reel	3000pcs/Reel
DTC123JCA	<b>E42</b>	SOT-23	Reel	3000pcs/Reel

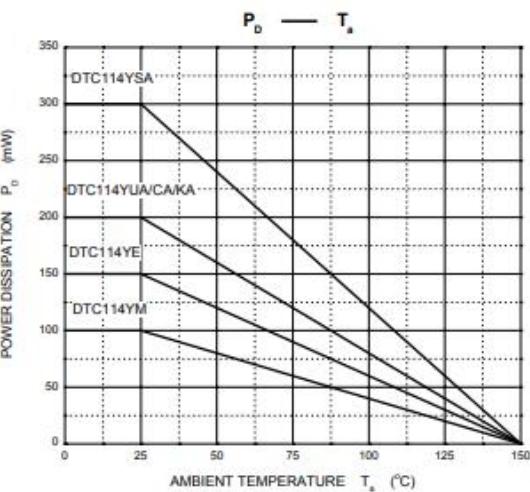
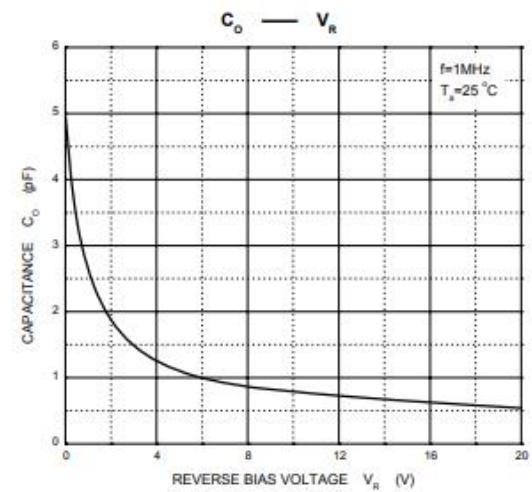
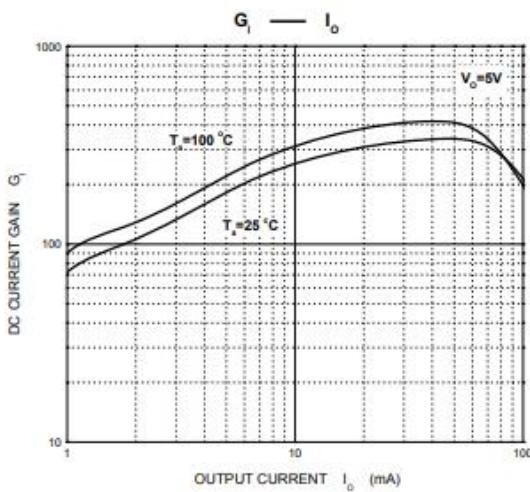
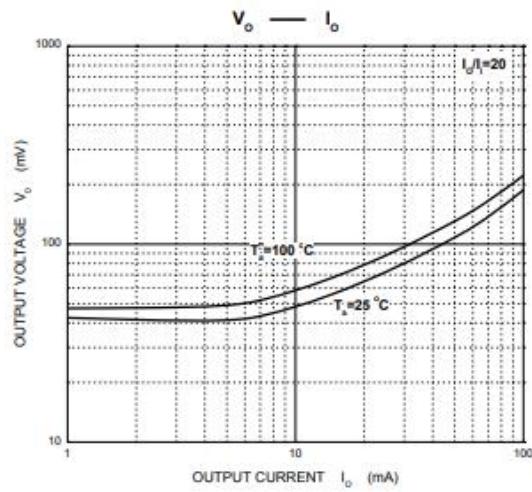
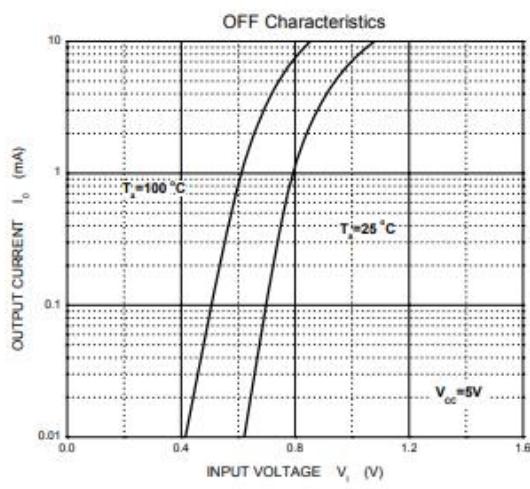
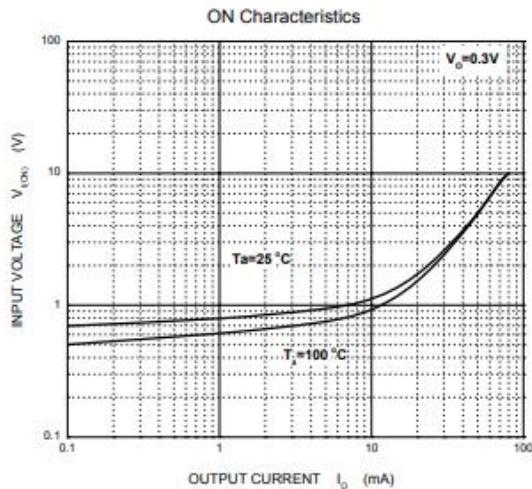
**MAXIMUM RATINGS(Ta=25°C unless otherwise noted)**

Symbol	Parameter	Limits(DTC123J□)						Unit
		M	E	UA	KA	CA	SA	
V <sub>CC</sub>	Supply Voltage	50						V
V <sub>IN</sub>	Input Voltage	-5~+12						V
I <sub>O</sub>	Output Current	100						mA
P <sub>D</sub>	Power Dissipation	100	150	200	200	200	300	mW
T <sub>J</sub> ,T <sub>stg</sub>	Operation Junction and Storage Temperature Range	-55~+150						°C

**ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
<b>Input voltage</b>	V <sub>I(off)</sub>	V <sub>CC</sub> =5V,I <sub>O</sub> =100μA	0.5			V
	V <sub>I(on)</sub>	V <sub>O</sub> =0.3V,I <sub>O</sub> =5mA			1.1	V
<b>Output voltage</b>	V <sub>O(on)</sub>	I <sub>O</sub> /I <sub>I</sub> =5mA/0.25mA		0.1	0.3	V
<b>Input current</b>	I <sub>I</sub>	V <sub>I</sub> =5V			3.6	mA
<b>Output current</b>	I <sub>O(off)</sub>	V <sub>CC</sub> =50V,V <sub>I</sub> =0			0.5	μA
<b>DC current gain</b>	G <sub>I</sub>	V <sub>O</sub> =5V,I <sub>O</sub> =10mA	80			
<b>Input resistance</b>	R <sub>1</sub>		1.54	2.2	2.86	kΩ
<b>Resistance ratio</b>	R <sub>2</sub> /R <sub>1</sub>		17	21	26	
<b>Transition frequency</b>	f <sub>T</sub>	V <sub>O</sub> =10V,I <sub>O</sub> =5mA,f=100MHz		250		MHz

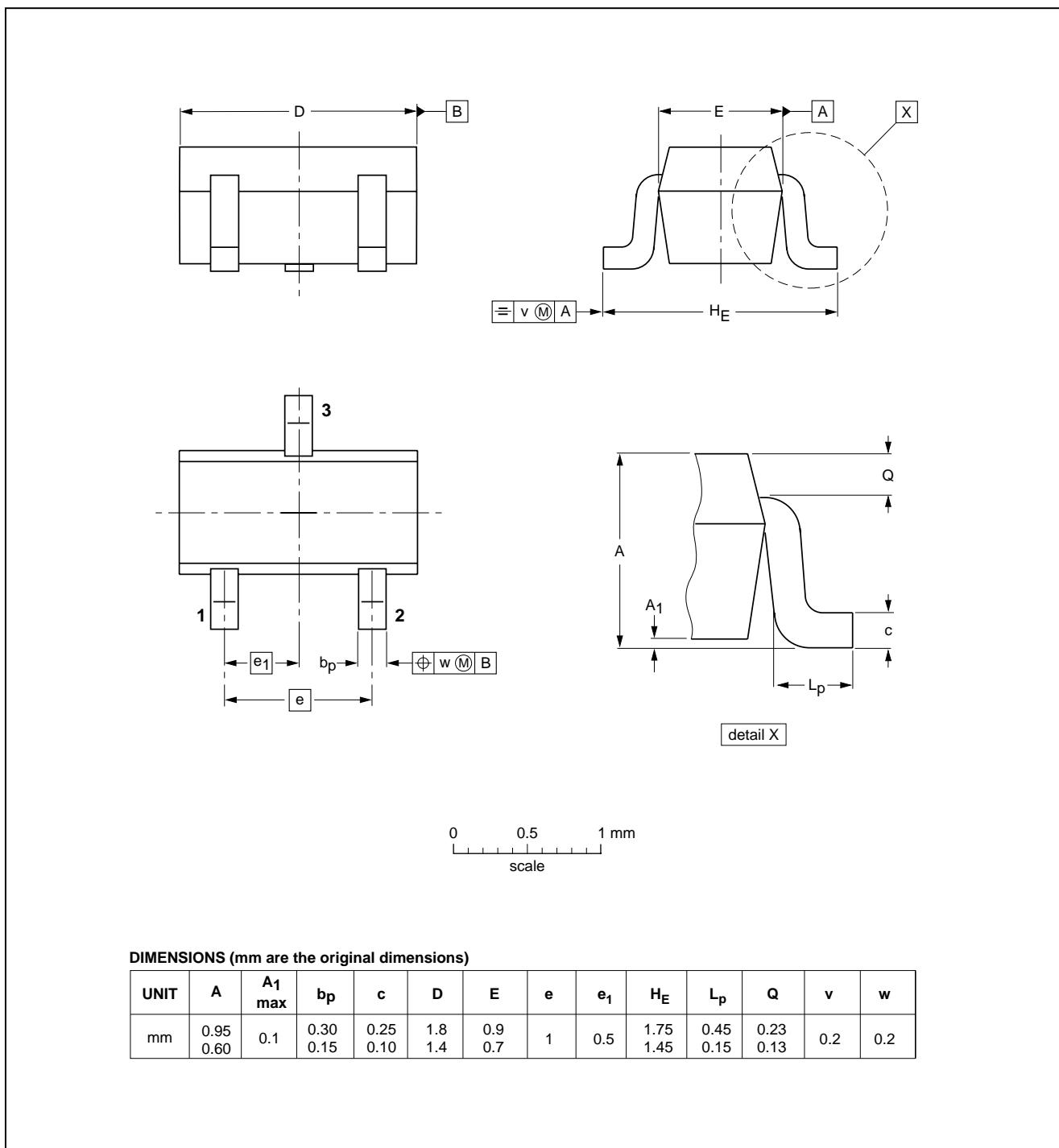
## Typical Characteristics



## PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-523



单击下面可查看定价，库存，交付和生命周期等信息

[>>TWGMC\(台湾迪嘉\)](#)