

General Description

SN74LVC1G14 is A non-gate integrated circuit with Schmitt-triggered function, which can achieve Y=A mathematical mathematical logic operation. Advanced CMOS process design, with low power consumption and high output driving capability, the power supply voltage VCC between 1.65V and 5.5V chip can work normally. 74LVC1G14 has a variety of small encapsulation shapes, which can be widely used in high-end precision instruments, miniaturized and low-power handheld devices, as well as artificial intelligence and other fields.

Features

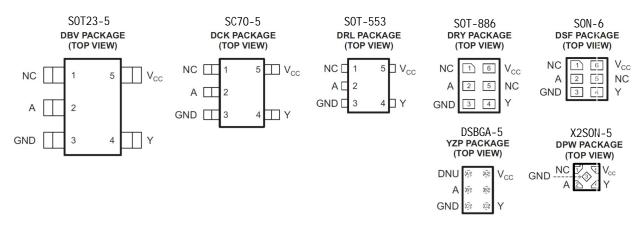
- Low input current.
- Low static power consumption.lcc=0.1uA.
- High output drive.VCC=4.5V.
- Wide operating voltage range.1.65V-5.5V
- Packaging form:DBV/DRL/YZP/DCK DRP/DSF/DPW

Applications

- Portable audio interface
- Blu-ray players and home theaters
- Solid state drives

- Digital TV
- Wireless headphones, smart watches, etc
- Smart wearable Devices

Pinning and Package



Pin Functions

		PIN				
NAME	DBV, DCK, DRL, DPW	DRY, DSF	YZP	YZV	1/0	DESCRIPTION
Α	2	2	B1	A1	ı	Signal Input
GND	3	3	C1	B1	_	Ground
N.C.	1	1, 5	_	_	_	No internal connection ⁽¹⁾
DNU	_	_	A1	_	_	Do not use ⁽²⁾
V _{CC}	5	6	A2	A2	_	Positive Supply
Υ	4	4	C2	B2	0	Signal Output

⁽¹⁾ Pins labeled N.C. can be connected to any signal or voltage source, including ground. They should always be soldered to the board.

⁽²⁾ Pins labeled DNU should not be connected to any signal or voltage source, including ground. They should always be soldered to the board.



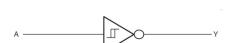
Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
V_{CC}	Supply voltage			6.5	V
VI	Input voltage (2)		-0.5	6.5	V
Vo	Voltage range applied to any output in the high-impedance or power-off state (2)			6.5	V
Vo	Voltage range applied to any output in the high or lo	ow state ^{(2) (3)}	-0.5	V _{CC} + 0.5	V
I _{IK}	Input clamp current	V _I < 0		-50	mA
I _{OK}	Output clamp current	V _O < 0		-50	mA
Io	Continuous output current	Continuous output current			mA
	Continuous current through V _{CC} or GND			±100	mA
Tj	Maximum junction temperature			150	°C
T _{stg}	Storage temperature		-65	150	°C

⁽¹⁾ Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Functional Block Diagram



Device Functional Modes

Inputs	Output
Α	Y
L	Н
Н	L

Recommended Operating Conditions

			MIN	MAX	UNIT	
V	Supply voltage	Operating	1.65	5.5	V	
V _{CC}	Supply voltage	Data retention only	1.5		V	
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	0.65 × V _{CC}			
V High love	Lligh level input veltage	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.7		V	
V _{IH}	High-level input voltage	$V_{CC} = 3 \text{ V to } 55 \text{V}$	0.7 × V _{CC}			
		V _{CC} = 1.65 V to 1.95 V		0.35 × V _{CC}		
V_{IL}	Low-level input voltage	V _{CC} = 2.3 V to 2.7 V		0.7	V	
		$V_{CC} = 3 V \text{ to } 5.5 V$		0.3 × V _{CC}		
VI	Input voltage		0	5.5	٧	
Vo	Output voltage		0	V _{CC}	V	
		V _{CC} = 1.65 V		-4		
I _{OH}	High-level output current	V _{CC} = 2.3 V		-8		
ЮП	riigir lovel eatpat eatrone	$V_{CC} = 3 V$		-16	16 mA	
		V _{CC} = 4.5 V		-32		
		V _{CC} = 1.65 V		4		
1	Low-level output current	V _{CC} = 2.3 V		8	_	
l _{OL}	Low-level output current	V _{CC} = 3 V		16	+	
		V _{CC} = 4.5 V		32		

⁽²⁾ The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

⁽³⁾ The value of V_{CC} is provided in the recommended operating conditions table.



Electrical Characteristics

PARAMETER		TEST CONDITIONS	Vcc	TYP	MAX	UNIT	
V _{T+}			1.65V	1	_	V	
			2. 3V	1.35	_		
		_	3V	1.7	_		
			4. 5V	2. 5	_		
			5. 5V	3	_		
			1.65V	0.5	_		
			2. 3V	0.7	_		
V _T -	-	_	3V	1.1	_	V	
			4.5V	1.65	_		
			5. 5V	1.9	_		
			1.65V	0.5	_		
Δ V	<i></i>		2. 3V	0.65	_		
		_		0.6	_	V	
$(V_{T+} - V_{T-})$			4.5V	0.85	_		
			5. 5V	1.1	_		
		I _{OH} =-100uA	1.65V~5.5V	1.64	_		
		$I_{OH} = -4 \text{ mA}$	1.65V	1.47	_		
Vol	Н	$I_{OH} = -8 \text{ mA}$	2. 3V	2. 15	_	V	
		I _{OH} =-16 mA	3V	2.73	_		
		I_{OH} =-32 mA	4. 5V 4. 0				
		I _{OH} =100uA	1.65V~5.5V	0.01	_		
		I_{OH} =4 mA	1.65V	0.11	_		
Voi	L	$I_{OH} = 8 \text{ mA}$	2. 3V	0.11	_	V	
		I_{OH} =16 mA	3V	0.2	_		
		I_{OH} =32 mA	4. 5V	0.35	_		
$I_{\mathbf{I}}$	A	$V_{I} = 5.5 V \text{ or GND}$	0~5.5V	0.01	±5	uA	
T	VI	V _I =5. 5V	0	0.01	±10	11 /	
Ioff	Vo	V _O =5. 5V	0	0.01	±10	uA	
Ico		$V_{I} = 5.5V, I_{O} = 0$	1. 65V~5. 5V	0.01	10	11Λ	
100		$V_{I} = GND$, $I_{O} = 0$	0.01		10	uA	
ΔI_{C}	CC	A=V _{CC} -0.6V	3V~5.5V	25	_	uA	

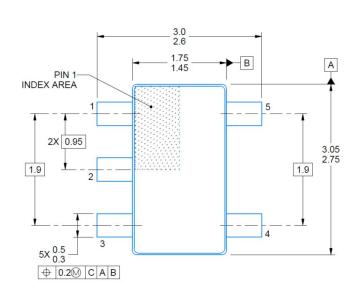
Switching Characteristics: -40°C to 85°C

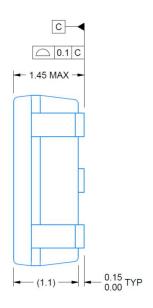
over recommended operating free-air temperature range, (-40 C to 85°C unless otherwise noted) (see)

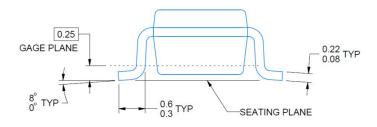
PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC}	C _L = 15 pF		C _L = 30 pF or 50 pF		UNIT
				MIN	MAX	MIN	MAX	
		V	1.8 V ± 0.15 V	2.8	9.9	3.8	11	
			2.5 V ± 0.2 V	1.6	5.5	2	6.5	
t _{pd}	Α	Y	3.3 V ± 0.3 V	1.5	4.6	1.8	5.5	ns
			5 V ± 0.5 V	0.9	4.4	1.2	5	İ



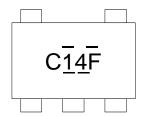
DBV (SOT23-5)





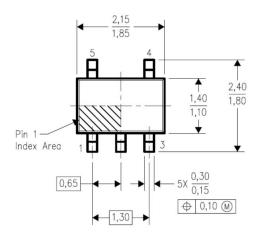


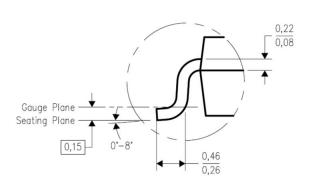
Marking

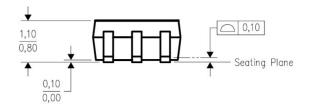


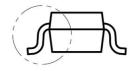


DCK (SC70-5)

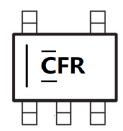






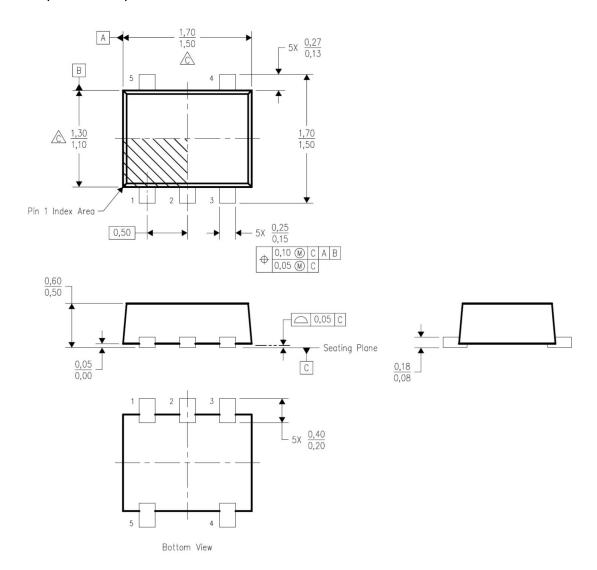


Marking



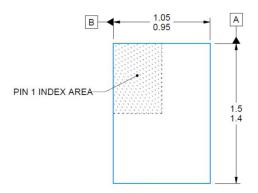


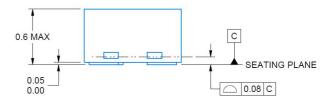
DRL (SOT-553)

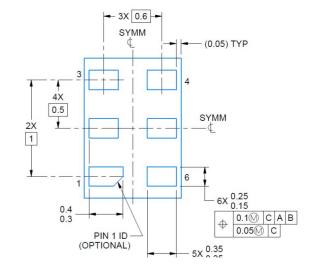


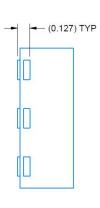


DRY (SOT-886)



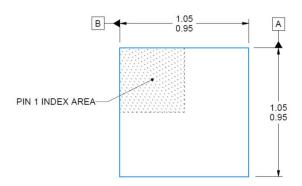


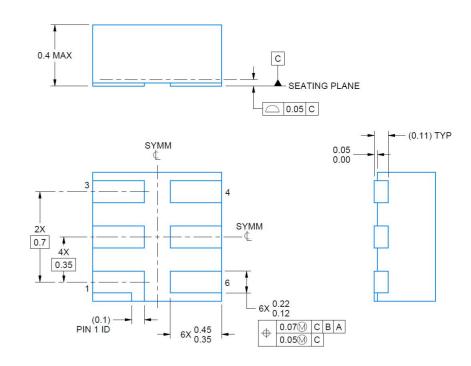






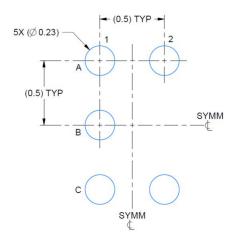
DSF (SON-6)



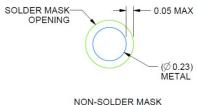




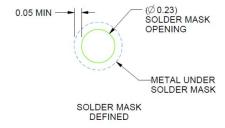
YZP (DSBGA-5)



LAND PATTERN EXAMPLE SCALE:40X

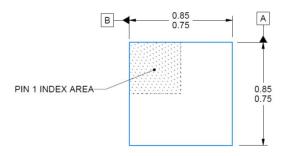


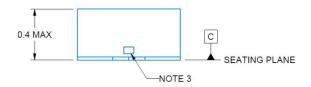
NON-SOLDER MASK DEFINED (PREFERRED)

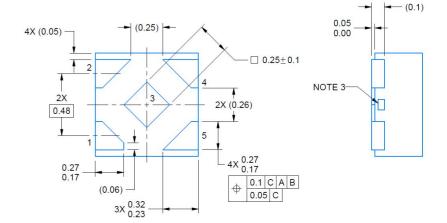




DPW (X2SON-5)







单击下面可查看定价,库存,交付和生命周期等信息

>>TWGMC(台湾迪嘉)