

# Low-Power, Slew-Rate-Limited RS-485/RS-422 Transceivers

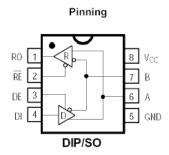
#### **General Description**

The SP485 is low-power transceivers for RS-485 and RS-422 communication. IC contains one driver and one receiver. The driver slew rates of the SP485 is not limited, allowing them to transmit up to 2.5Mbps.

These transceivers draw between  $120\mu A$  and  $500\mu A$  of supply current when unloaded or fully loaded with disabled drivers. All parts operate from a single 5V supply. Drivers are short-circuit current limited and are protected against excessive power dissipation by thermal shutdown circuitry that places the driver outputs into a high-impedance state. The receiver input has a fail-safe feature that guarantees a logic-high output if the input is open circuit. The SP485 is designed for half-duplex applications.

#### **Features**

- Low Quiescent Current: 300µA
- -7V to +12V Common-Mode Input Voltage Range
- Three-State Outputs
- 30ns Propagation Delays, 5ns Skew
- Full-Duplex and Half-Duplex Versions Available
- Operate from a Single 5V Supply
- Allows up to 32 Transceivers on the Bus
- Data rate: 2,5 Mbps
- Current-Limiting and Thermal Shutdown for Driver Overload Protection





#### **ABSOLUTE MAXIMUM RATINGS**

Supply Voltage ( $V_{CC}$ ) 12V Control Input Voltage -0.5V to ( $V_{CC}$  + 0.5V)

Driver Input Voltage (DI) -0.5V to (V<sub>CC</sub>+ 0.5V)

Driver Output Voltage (A, B) -8V to +12.5V Receiver Input Voltage (A, B) -8V to +12.5V Receiver Output Voltage (RO) -0.5V to (V<sub>cc</sub>+0.5V)

Continuous Power Dissipation (T<sub>A</sub>= +70°C) 8-Pin Plastic DIP (derate 9.09mW/°C above +70°C) 727mW

8-Pin SO (derate 5.88mW/°C above +70°C) 471mW

Operating Temperature Ranges0°C to +70°C Storage Temperature Range -65°C to +160°C Lead Temperature (soldering, 10sec) +300°C

#### DC ELECTRICAL CHARACTERISTICS

 $(V_{CC} = 5V \pm 5\%, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted.})$  (Notes 1, 2)

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	
Differential Driver Output (no load)	V <sub>OD1</sub>				5	V	
Differential Driver Output	V <sub>OD2</sub>	$R = 50\Omega (RS-422)$		2			V
(with load)		$R = 27\Omega (RS-485), F$	igure 4	1.5		5	
Change in Magnitude of Driver Differential Output Voltage for Complementary Output States	ΔVod	R = 27 $\Omega$ or 50 $\Omega$ , Figu			0.2	V	
Driver Common-Mode Output Voltage	Voc	R = 27 $\Omega$ or 50 $\Omega$ , Figu			3	V	
Change in Magnitude of Driver Common-Mode Output Voltage for Complementary Output States	ΔVod	R = 27 $\Omega$ or 50 $\Omega$ , Figu			0.2	<	
Input High Voltage	VIH	DE, DI, RE	2.0			V	
Input Low Voltage	VIL	DE, DI, RE				0.8	V
Input Current	l <sub>IN1</sub>	DE, DI, RE				±2	μA
Input Current	I <sub>IN2</sub>	DE = 0V; V <sub>IN</sub> = 12V				1.0	mA
(A, B)		Vcc = 0V or 5.25V, V <sub>IN</sub> = -7V				-0.8	1
Receiver Differential Threshold Voltage	Vтн	$-7V \le V_{CM} \le 12V$		-0.2		0.2	V
Receiver Input Hysteresis	$\Delta V$ TH	V <sub>CM</sub> = 0V			70		mV
Receiver Output High Voltage	Vон	Io = -4mA, VID = 200mV		3.5			V
Receiver Output Low Voltage	Vol	Io = 4mA, VID = -200mV				0.4	V
Three-State (high impedance) Output Current at Receiver	lozr	0.4V ≤ Vo ≤ 2.4V			±1	μA	
Receiver Input Resistance	RIN	$-7V \le V_{CM} \le 12V$				kΩ	



## DC ELECTRICAL CHARACTERISTICS (continued) (Vcc = 5V ±5%, Ta = Tmin to Tmax, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
No-Load Supply Current	Icc	DE = V <sub>CC</sub>		500	900	
(Note 3)		RE = 0V or Vcc		300	500	μA
		DE = 0V				
Driver Short-Circuit Current,						
	losd1	-7V ≤ Vo ≤ 12V (Note 4)	35		250	mA
Vo = High						
Driver Short-Circuit Current,						
	losd2	-7V ≤ Vo ≤12V (Note 4)	35		250	mA
Vo = Low						
Receiver Short-Circuit Current	Iosr	0V ≤ Vo ≤ Vcc	7		95	mA

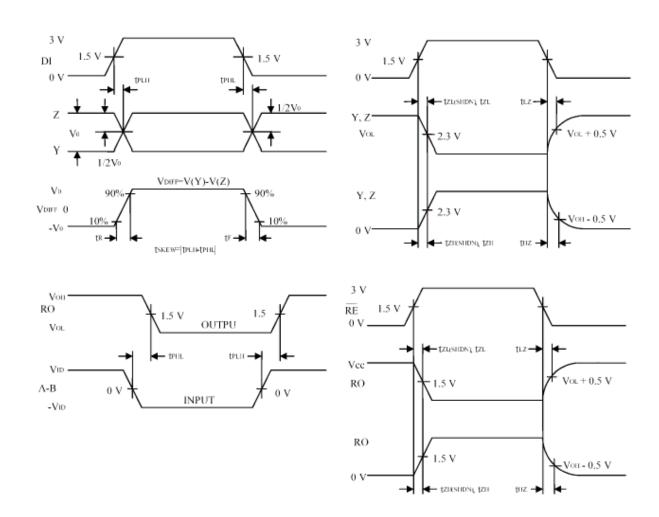
#### **SWITCHING CHARACTERISTICS**

(Vcc = 5V ±5%, T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Input to Output	<b>t</b> PLH	RDIFF = $54\Omega$	10	30	60	ns
	<b>t</b> PHL	C <sub>L1</sub> = C <sub>L2</sub> = 100pF	10	30	60	
Driver Output Skew to Output	<b>t</b> skew	RDIFF = $54\Omega$ , CL1 = CL2 = $100$ pF		5	10	ns
Driver Enable to Output High	tzн	C <sub>L</sub> = 100pF, S2 closed		40	70	ns
Driver Enable to Output Low	<b>t</b> zL	C <sub>L</sub> = 100pF, S1 closed		40	70	ns
Driver Disable Time from Low	<b>t</b> LZ	C <sub>L</sub> = 15pF, S1 closed		40	70	ns
Driver Disable Time from High	<b>t</b> HZ	C <sub>L</sub> = 15pF, S2 closed		40	70	ns
tPLH - tPHL   Differential	<b>t</b> skd	RDIFF = $54\Omega$		13		ns
Receiver Skew		C <sub>L1</sub> = C <sub>L2</sub> = 100pF				
Receiver Enable to Output Low	<b>t</b> zL	C <sub>RL</sub> = 15pF, S1 closed		20	50	ns
Receiver Enable to Output High	tzн	C <sub>RL</sub> = 15pF, S2 closed		20	50	ns
Receiver Disable Time from	<b>t</b> LZ	C <sub>RL</sub> = 15pF, S1 closed		20	50	ns
Low						
Receiver Disable Time from	<b>t</b> HZ	C <sub>RL</sub> = 15pF, S2 closed		20	50	ns
High						
Maximum Data Rate	fmax		2.5			Mbps



### Operation timing diagrams of SP485



#### **Table of SP485 operation**

Transmission				Receipt				
	Inputs		Outp	uts X	Inputs			Outputs
RE	DE	DI	Z	Υ	RE	DE	A-B	RO
Х	1	1	0	1	0	0	+0.2V	1
Х	1	0	1	0	0	0	-0.2V	0
0	0	Х	Z	Z	0	0	open	1
1	0	Χ	Z	Z	1	0	Х	Z

X-don't care

Z-high resistance

## 单击下面可查看定价,库存,交付和生命周期等信息

## >>UMW(友台半导体)