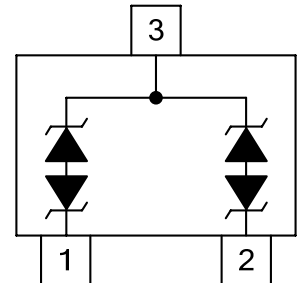


Description

SDT23C24L02 component is designed to protect sensitive electronics from damage or latch-up due to ESD and other voltage induced transient events. It is designed for use in applications where board space is at a premium. The device will protect up to two lines.

It is bidirectional devices and may be used on lines where the signal polarities are above ground. TVS diodes are solid state device designed specifically for transient suppression. It feature large cross-sectional area junctions for conduction high transient currents. It offer desirable characteristics for board level protection including fast response time, low and clamping voltage, and no device degradation.



Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOT-23 surface mount package
- Protects bidirectional two I/O lines
- Peak power dissipation of 350W under 8/20μs waveform
- Working voltage: 24V
- Low leakage current
- Low operating and clamping voltages
- Solder reflow temperature: Pure Tin-Sn, 260~270°C

Applications

- RS-232 and RS-422 data lines
- Microprocessor based equipment
- LAN/WAN equipment
- Desktops PC and servers
- Notebook, Laptop and Palmtop computers
- Set Top Box
- Peripherals
- Serial and Parallel ports

Maximum Ratings

Rating	Symbol	Value	Unit
Peak pulse power (tp=8/20μs waveform)	P _{PP}	350	W
ESD voltage (Contact discharge)	V _{ESD}	±8	kV
ESD voltage (Air discharge)		±15	
Storage & operating temperature range	T _{STG} , T _J	-55~+150	°C

Electrical Characteristics (T_J=25°C)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V _{RWM}				24	V
Reverse breakdown voltage	V _{BR}	I _{BR} =1mA	26.7			V
Reverse leakage current	I _R	V _R =24V Each I/O pin			1	μA
Clamping voltage (tp=8/20μs)	V _C	I _{PP} =1A			43	V
Clamping voltage (tp=8/20μs)	V _C	I _{PP} =5A			52	V
Off state junction capacitance	C _J	0Vdc, f=1MHz Between I/O pins and GND		40		pF

Typical Characteristics Curves

Figure 1. Power Derating Curve

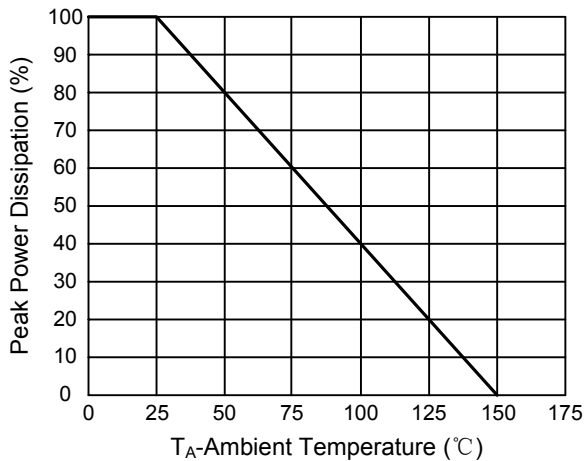


Figure 3. Non-Repetitive Peak Pulse vs. Pulse Time

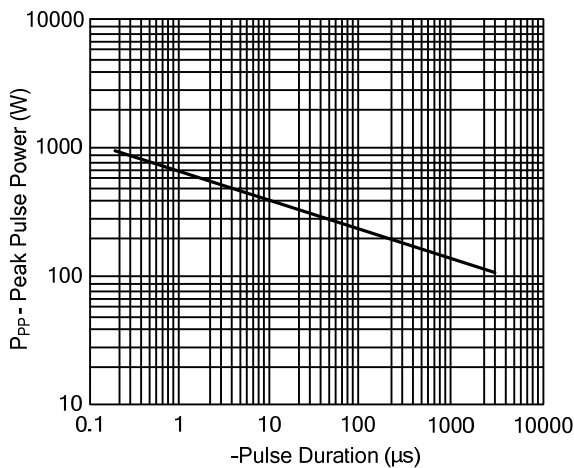


Figure 2. Pulse Waveforms

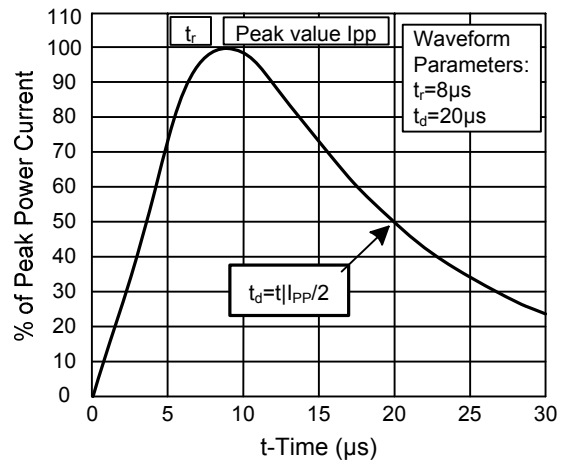
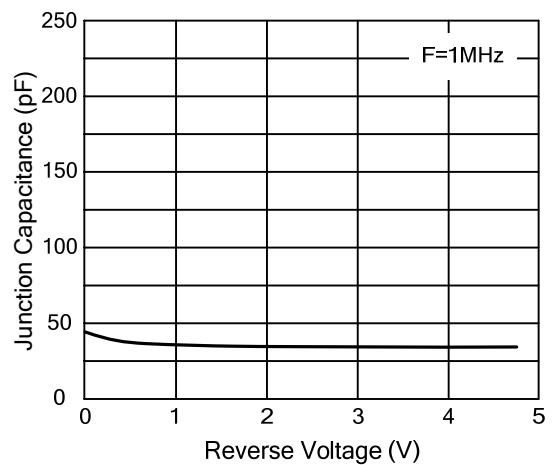
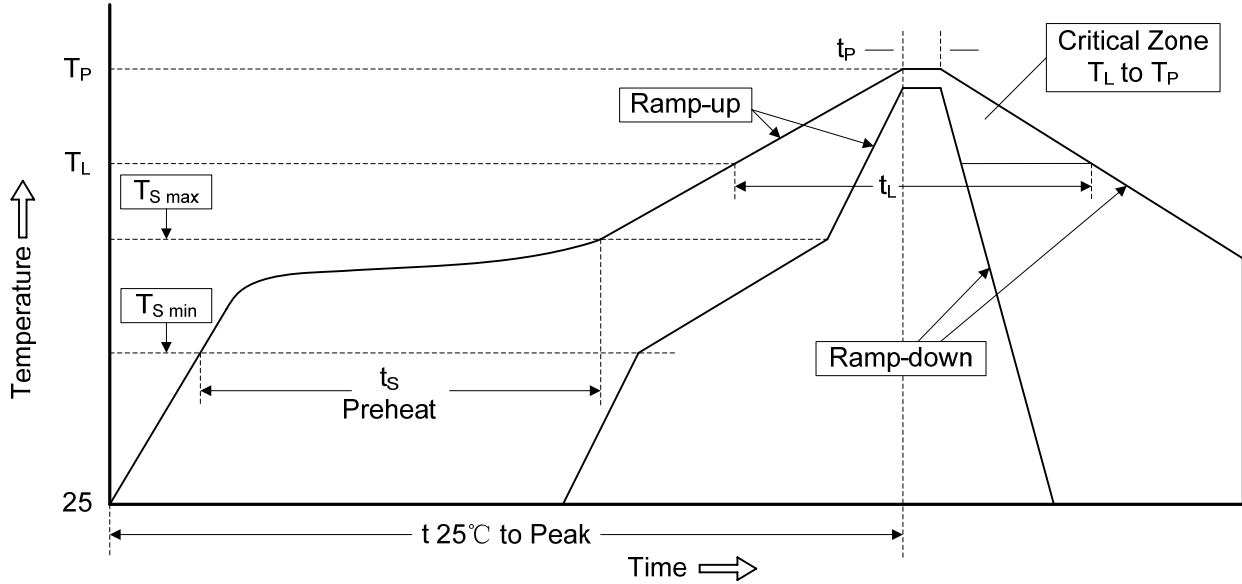


Figure 4. Normalized Capacitance vs. Reverse Voltage



Recommended Soldering Conditions

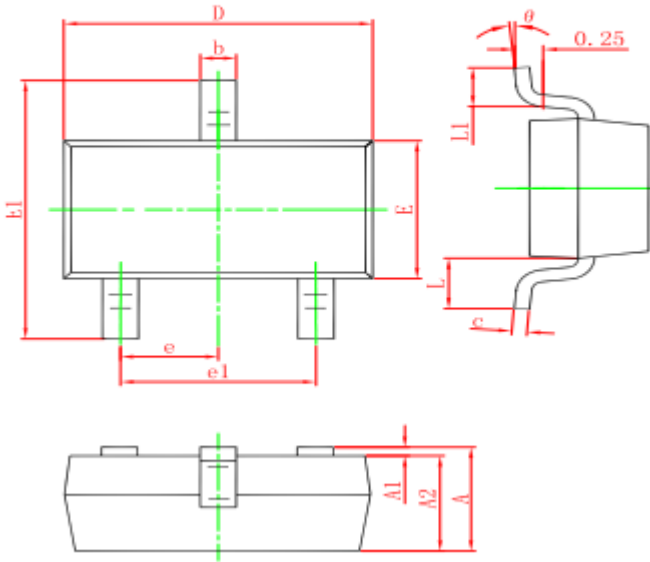
Reflow Soldering



Recommended Conditions

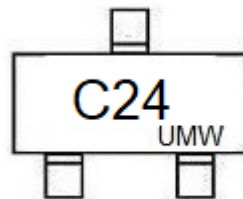
Profile Feature	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.
Preheat -Temperature Min ($T_{S\ min}$) -Temperature Max ($T_{S\ max}$) -Time (min to max) (t_s)	150°C 200°C 60-180 seconds
$T_{S\ max}$ to T_L -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature (T_L) -Time (t_L)	217°C 60-150 seconds
Peak Temperature (T_P)	260°C
Time within 5°C of actual Peak Temperature (t_p)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

SOT-23 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

Marking



Ordering information

Order code	Package	Baseqty	Deliverymode
UMW SDT23C24L02	SOT-23	3000	Tape and reel

单击下面可查看定价，库存，交付和生命周期等信息

[>>UMW\(友台半导体\)](#)