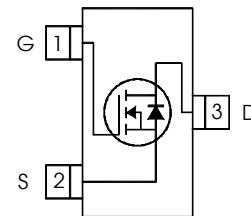
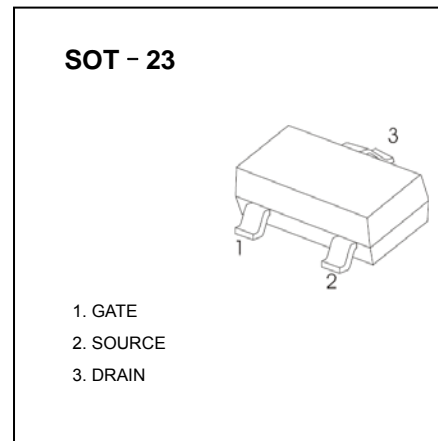


Features

- $V_{DS} (V) = 30V$
- $R_{DS(ON)} < 27m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 40m\Omega$ ($V_{GS} = 4.5V$)

Benefits

- Lower switching losses
- Multi-vendor compatibility
- Easier manufacturing
- Environmentally friendly
- Increased reliability



Absolute Maximum Ratings

Symbol	Parameter	Max.	Units
V_{DS}	Drain-Source Voltage	30	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	5.3	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	4.3	
I_{DM}	Pulsed Drain Current	21	
$P_D @ T_A = 25^\circ C$	Maximum Power Dissipation	1.3	W
$P_D @ T_A = 70^\circ C$	Maximum Power Dissipation	0.8	
	Linear Derating Factor	0.01	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient ^③	—	100	°C/W
$R_{\theta JA}$	Junction-to-Ambient (t<10s) ^④	—	99	

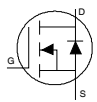
Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width $\leq 400\mu s$; duty cycle $\leq 2\%$.
- ③ Surface mounted on 1 in square Cu board

Electric Characteristics @ T_J = 25°C (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	30	—	—	V	V _{GS} = 0V, I _D = 250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	0.02	—	V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	33	40	mΩ	V _{GS} = 4.5V, I _D = 4.2A ②
		—	22	27		V _{GS} = 10V, I _D = 5.2A ②
V _{GS(th)}	Gate Threshold Voltage	1.3	1.7	2.3	V	V _{DS} = V _{GS} , I _D = 25μA
I _{DSS}	Drain-to-Source Leakage Current	—	—	1	μA	V _{DS} = 24V, V _{GS} = 0V
		—	—	150		V _{DS} = 24V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V _{GS} = 20V
	Gate-to-Source Reverse Leakage	—	—	-100		V _{GS} = -20V
R _G	Internal Gate Resistance	—	2.3	—	Ω	
g _{fs}	Forward Transconductance	9.5	—	—	S	V _{DS} = 10V, I _D = 5.2A
Q _g	Total Gate Charge	—	2.6	—	nC	I _D = 5.2A
Q _{gs}	Gate-to-Source Charge	—	0.8	—		V _{DS} = 15V
Q _{gd}	Gate-to-Drain ("Miller") Charge	—	1.1	—		V _{GS} = 4.5V ②
t _{d(on)}	Turn-On Delay Time	—	5.2	—	ns	V _{DD} = 15V ②
t _r	Rise Time	—	4.4	—		I _D = 1.0A
t _{d(off)}	Turn-Off Delay Time	—	7.4	—		R _G = 6.8Ω
t _f	Fall Time	—	4.4	—		V _{GS} = 4.5V
C _{iss}	Input Capacitance	—	382	—	pF	V _{GS} = 0V
C _{oss}	Output Capacitance	—	84	—		V _{DS} = 15V
C _{rss}	Reverse Transfer Capacitance	—	39	—		f = 1.0MHz

Source - Drain Ratings and Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	1.6	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	21		
V _{SD}	Diode Forward Voltage	—	—	1.0	V	T _J = 25°C, I _S = 1.6A, V _{GS} = 0V ②
t _{rr}	Reverse Recovery Time	—	11	17	ns	T _J = 25°C, V _R = 15V, I _F = 1.6A
Q _{rr}	Reverse Recovery Charge	—	4.0	6.0	nC	di/dt = 100A/μs ②

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

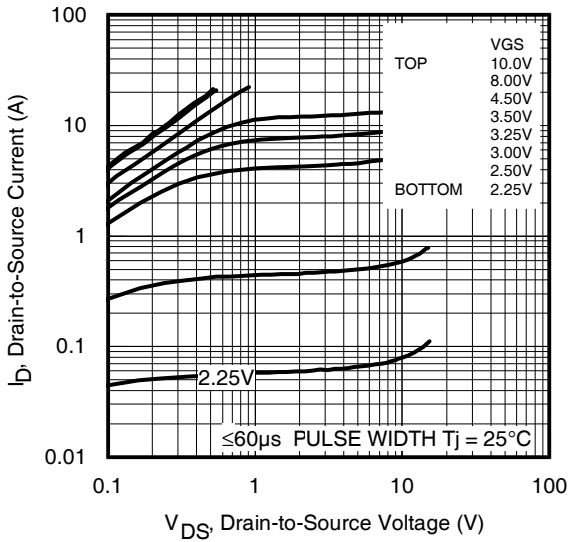


Fig 1. Typical Output Characteristics

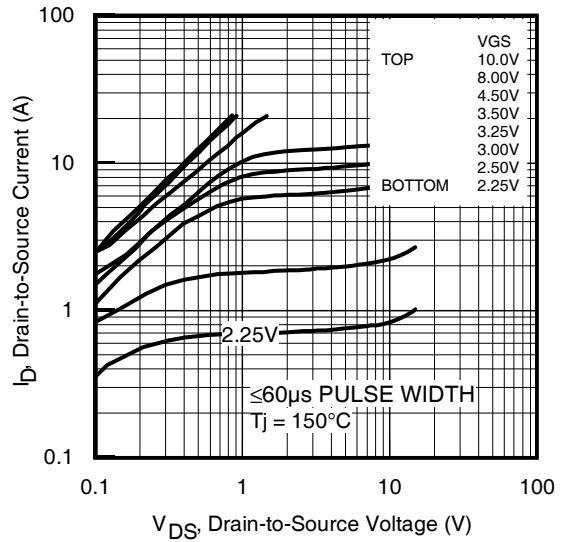


Fig 2. Typical Output Characteristics

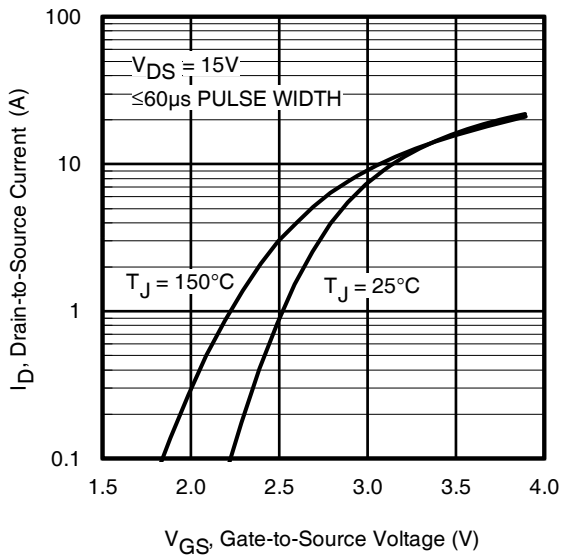


Fig 3. Typical Transfer Characteristics

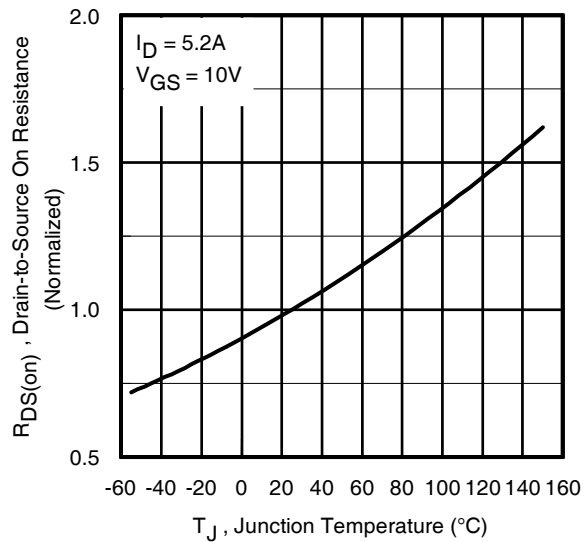


Fig 4. Normalized On-Resistance Vs. Temperature

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

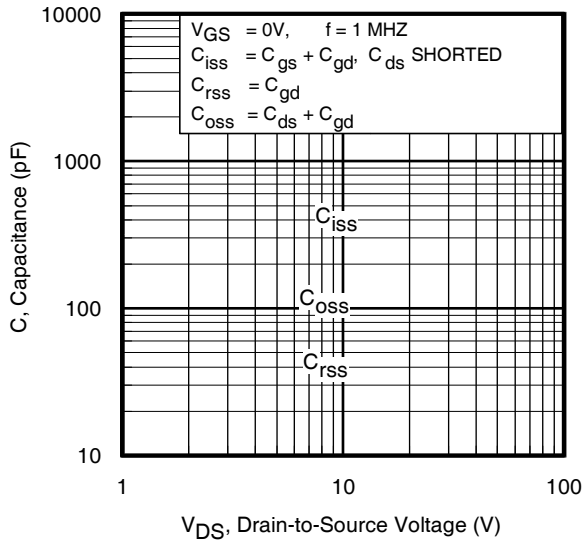


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

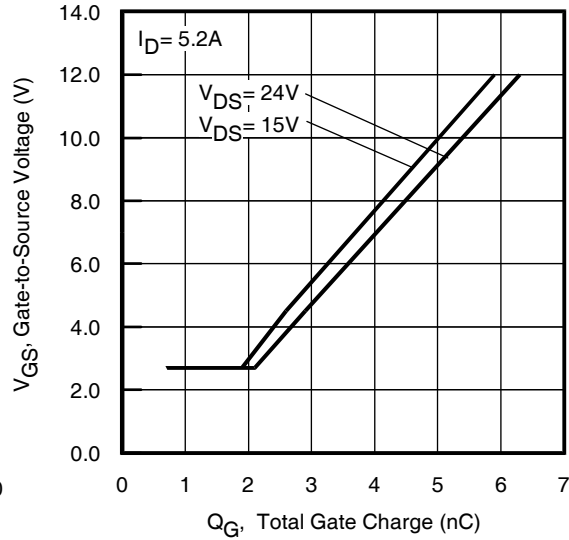


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

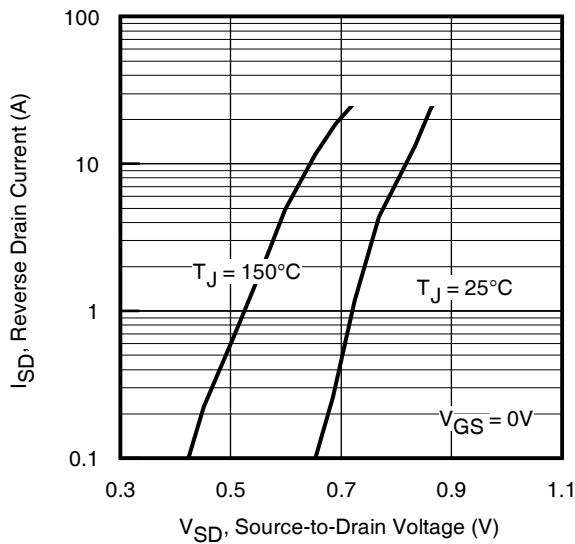


Fig 7. Typical Source-Drain Diode Forward Voltage

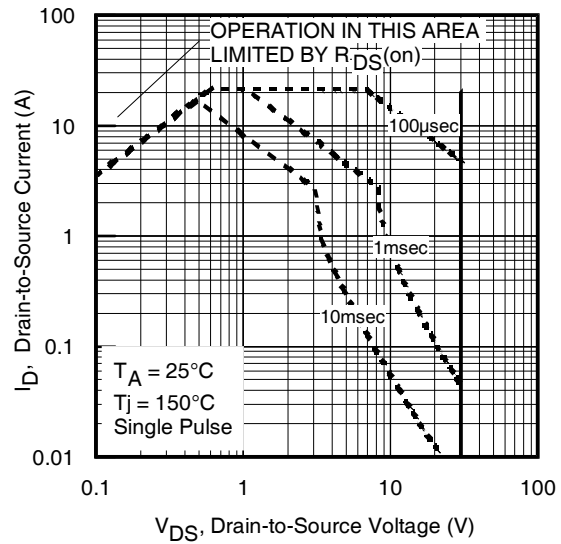


Fig 8. Maximum Safe Operating Area

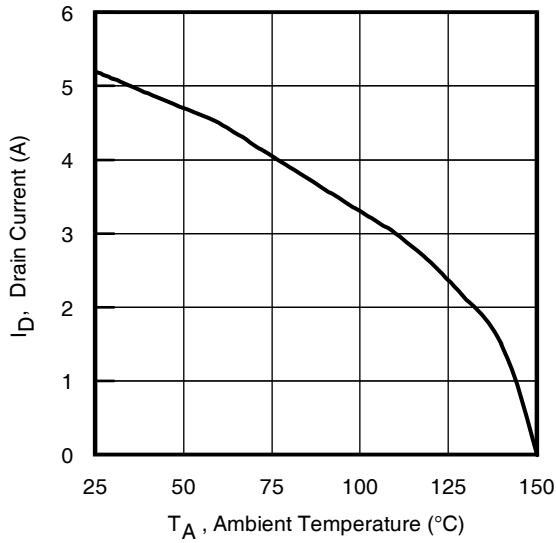


Fig 9. Maximum Drain Current Vs. Ambient Temperature

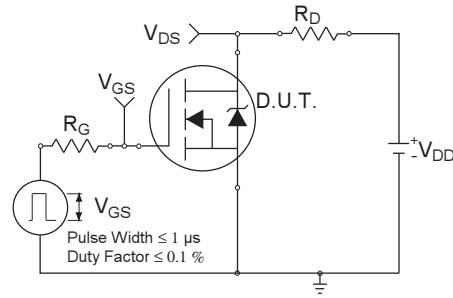


Fig 10a. Switching Time Test Circuit

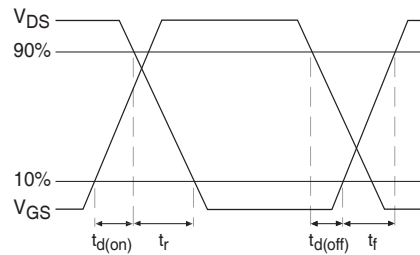


Fig 10b. Switching Time Waveforms

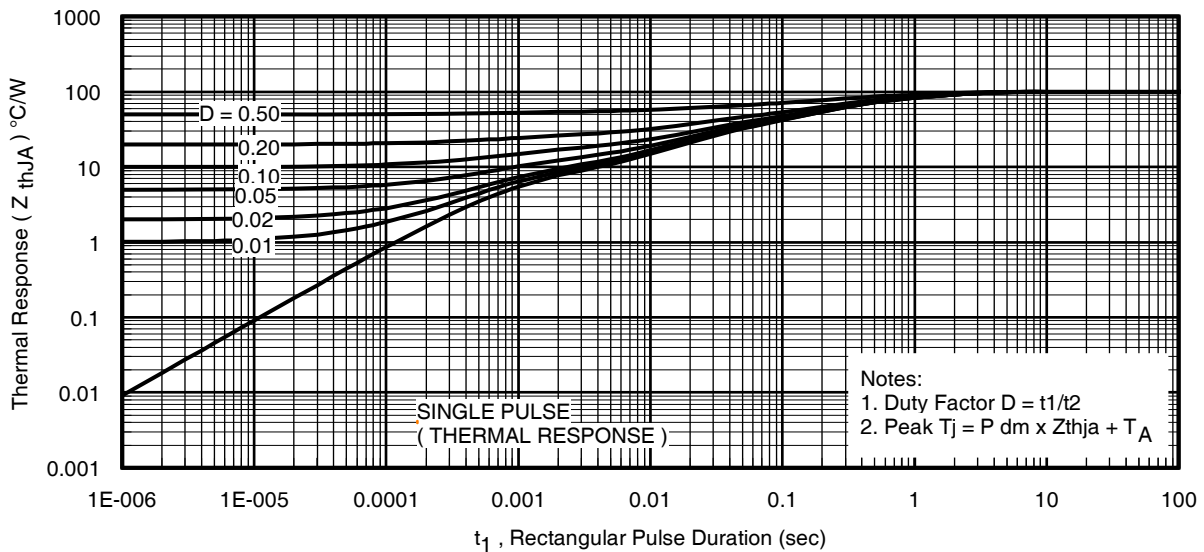


Fig 11. Typical Effective Transient Thermal Impedance, Junction-to-Ambient

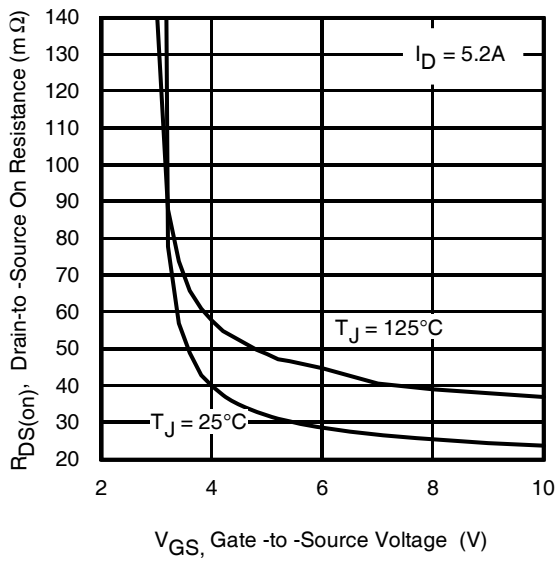


Fig 12. Typical On-Resistance Vs. Gate Voltage

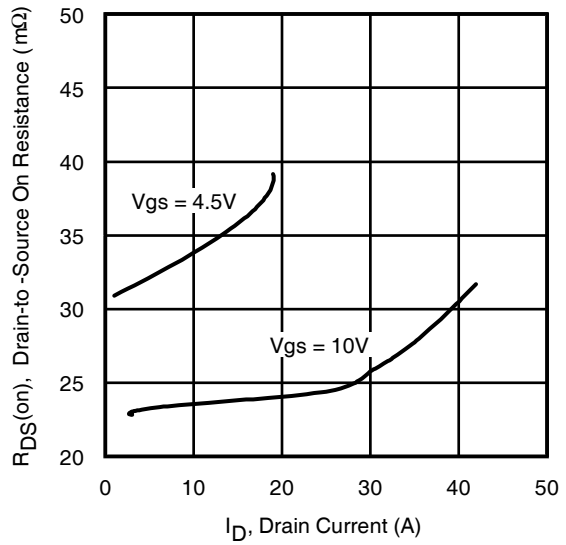


Fig 13. Typical On-Resistance Vs. Drain Current

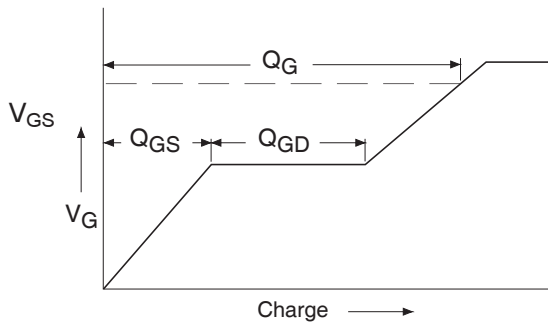


Fig 14a. Basic Gate Charge Waveform

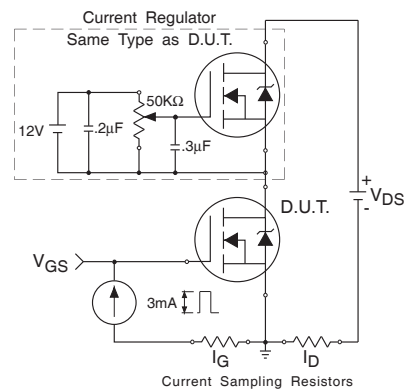


Fig 14b. Gate Charge Test Circuit

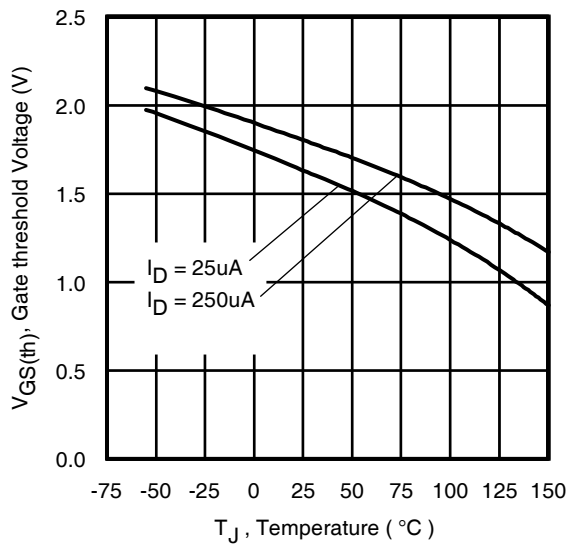


Fig 15. Typical Threshold Voltage Vs. Junction Temperature

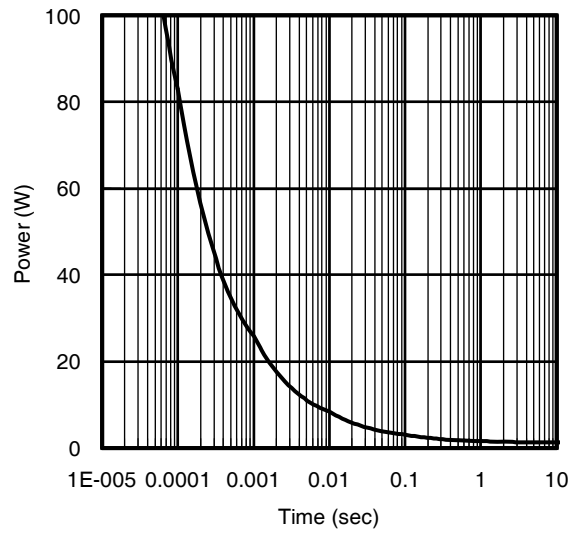
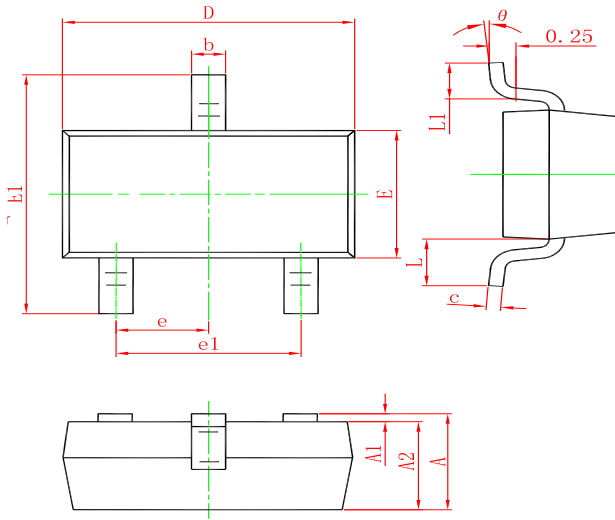


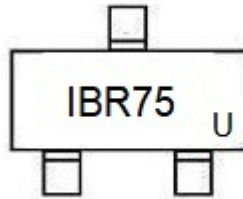
Fig 16. Typical Power Vs. Time

SOT-23 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
theta	0°	8°	0°	8°

Marking



Ordering information

Order code	Package	Baseqty	Deliverymode
UMW IRLML0030TR	SOT-23	3000	Tape and reel

单击下面可查看定价，库存，交付和生命周期等信息

[>>UMW\(友台半导体\)](#)