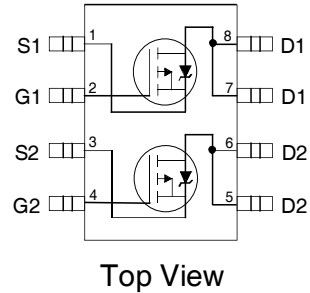


Features

- $V_{DS} (V) = -20V$
- $R_{DS(ON)} < 18\ m\ \Omega$ ($V_{GS} = -4.5V$)
- $R_{DS(ON)} < 26\ m\ \Omega$ ($V_{GS} = -2.5V$)
- Trench Technology
- Ultra Low On-Resistance
- Low Profile (<1.1mm)
- Available in Tape & Reel
- 2.5V Rated
- Lead-Free



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain-Source Voltage	-20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	-9.0	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	-7.1	
I_{DM}	Pulsed Drain Current ^①	-71	
$P_D @ T_A = 25^\circ C$	Maximum Power Dissipation ^③	2.0	W
$P_D @ T_A = 70^\circ C$	Maximum Power Dissipation ^③	1.3	W
	Linear Derating Factor	16	mW/ $^\circ C$
V_{GS}	Gate-to-Source Voltage	± 12	V
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	$^\circ C$

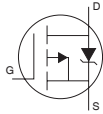
Thermal Resistance

	Parameter	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ^③	62.5	$^\circ C/W$

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	-20			V	$V_{GS} = 0V, I_D = -250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		-0.02		V/°C	Reference to $25^\circ\text{C}, I_D = -1\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance			18	mΩ	$V_{GS} = -4.5V, I_D = -9.0A$ ②
				26		$V_{GS} = -2.5V, I_D = -7.7A$ ②
$V_{GS(th)}$	Gate Threshold Voltage	-0.45		-1.0	V	$V_{DS} = V_{GS}, I_D = -250\mu A$
g_{fs}	Forward Transconductance	19			S	$V_{DS} = -10V, I_D = -9.0A$
I_{DSS}	Drain-to-Source Leakage Current			-1.0	μA	$V_{DS} = -16V, V_{GS} = 0V$
				-25		$V_{DS} = -16V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage			-100	nA	$V_{GS} = -12V$
	Gate-to-Source Reverse Leakage			100		$V_{GS} = 12V$
Q_g	Total Gate Charge		42	63	nC	$I_D = -9.0A$
Q_{gs}	Gate-to-Source Charge		7.1	11		$V_{DS} = -16V$
Q_{gd}	Gate-to-Drain ("Miller") Charge		12	18		$V_{GS} = -5.0V$
$t_{d(on)}$	Turn-On Delay Time		17		ns	$V_{DD} = -10V$
t_r	Rise Time		36			$I_D = -1.0A$
$t_{d(off)}$	Turn-Off Delay Time		170			$R_G = 6.0\Omega$
t_f	Fall Time		190			$R_D = 10\Omega$ ②
C_{iss}	Input Capacitance		2940		pF	$V_{GS} = 0V$
C_{oss}	Output Capacitance		630			$V_{DS} = -15V$
C_{rss}	Reverse Transfer Capacitance		420			$f = 1.0\text{MHz}$

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)			-2.0	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①			-71		
V_{SD}	Diode Forward Voltage			-1.2	V	$T_J = 25^\circ\text{C}, I_S = -2.0A, V_{GS} = 0V$ ②
t_{rr}	Reverse Recovery Time		180	270	ns	$T_J = 25^\circ\text{C}, I_F = -2.0A$
Q_{rr}	Reverse Recovery Charge		300	450	nC	$di/dt = -100A/\mu s$ ②

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width $\leq 300\mu s$; duty cycle $\leq 2\%$.
- ③ Surface mounted on FR-4 board, $t \leq 10\text{sec}$.

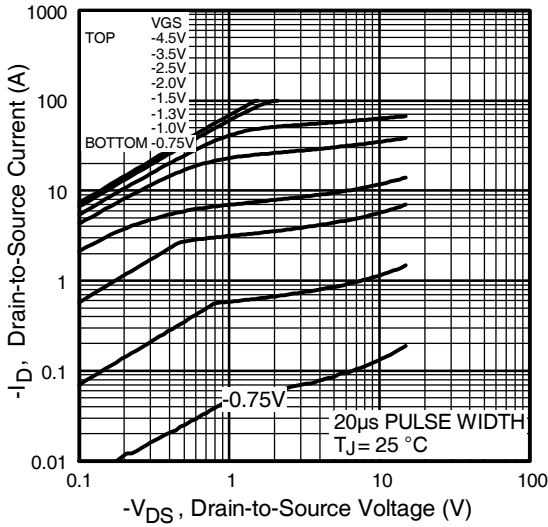


Fig 1. Typical Output Characteristics

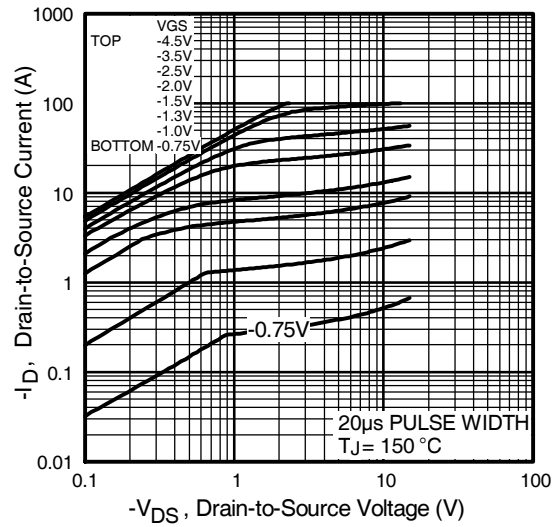


Fig 2. Typical Output Characteristics

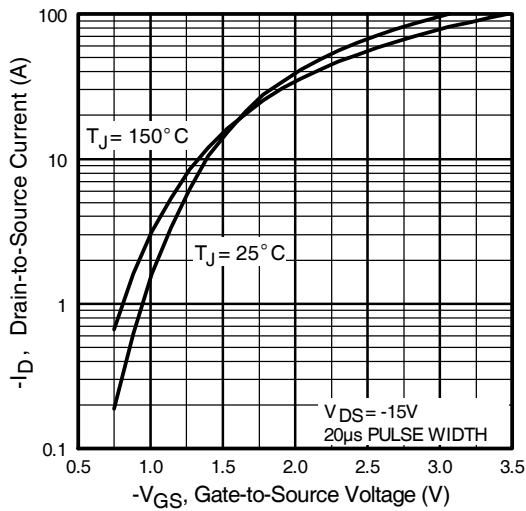


Fig 3. Typical Transfer Characteristics

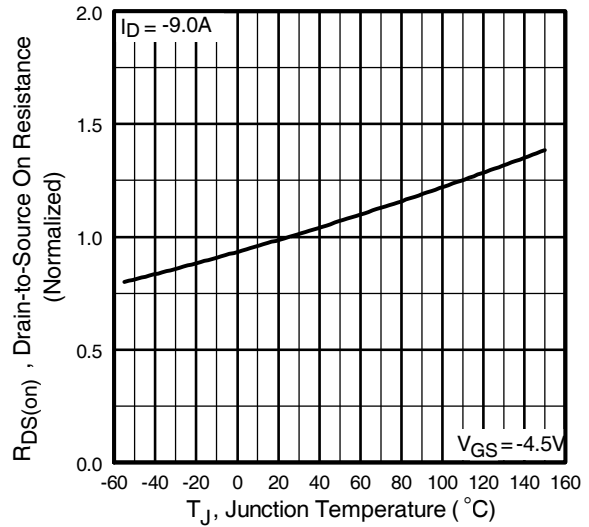


Fig 4. Normalized On-Resistance Vs. Temperature

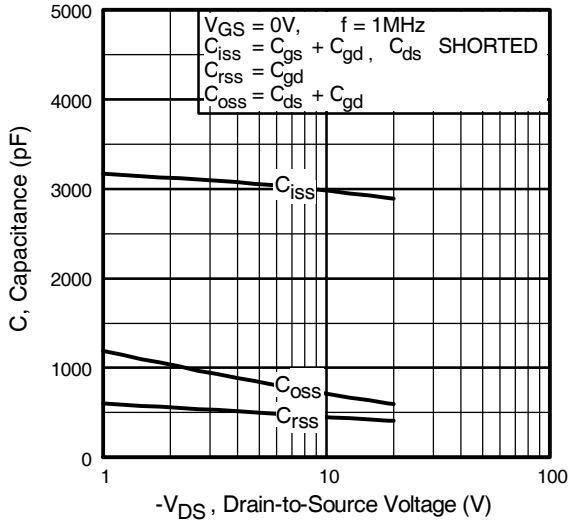


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

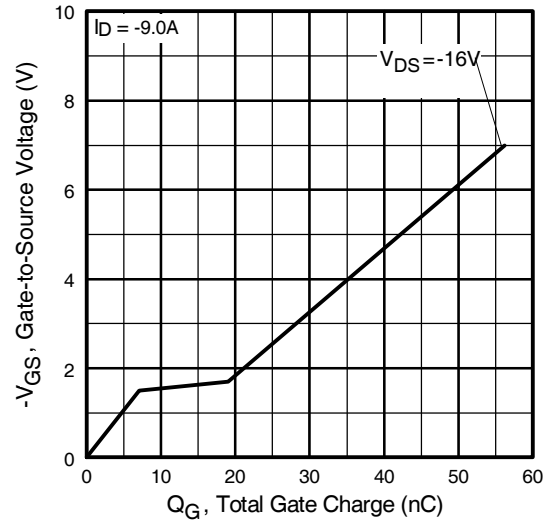


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

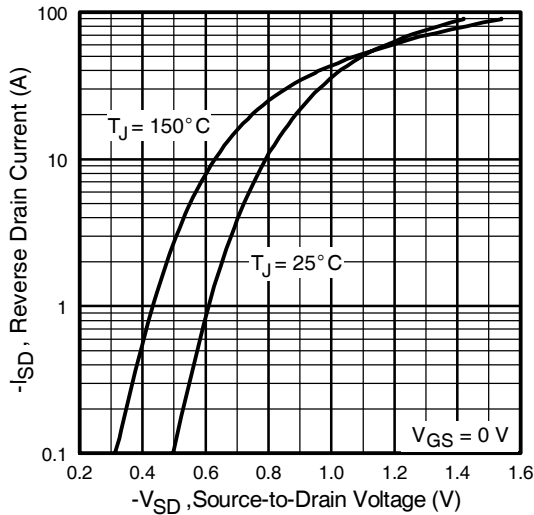


Fig 7. Typical Source-Drain Diode Forward Voltage

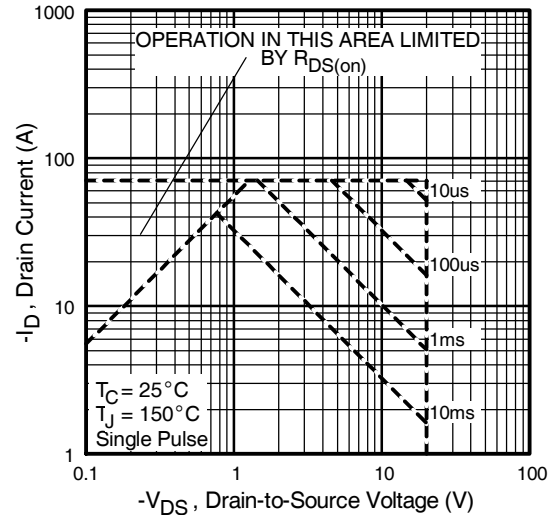


Fig 8. Maximum Safe Operating Area

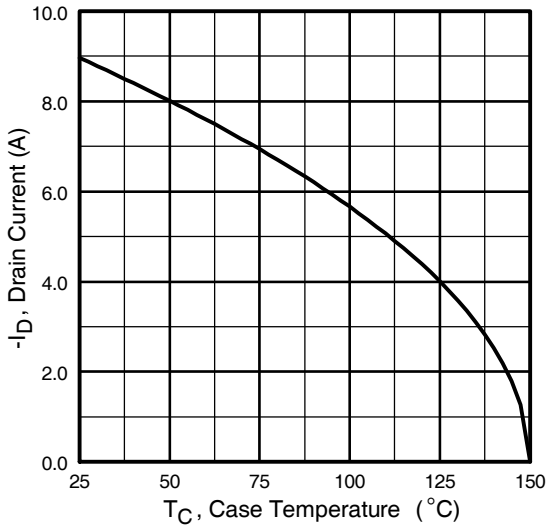


Fig 9. Maximum Drain Current Vs. Case Temperature

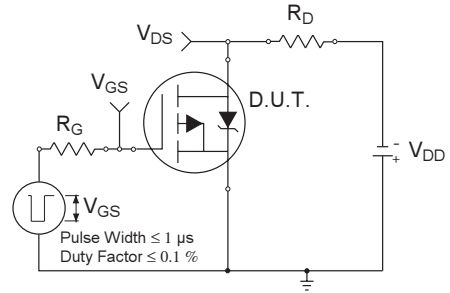


Fig 10a. Switching Time Test Circuit

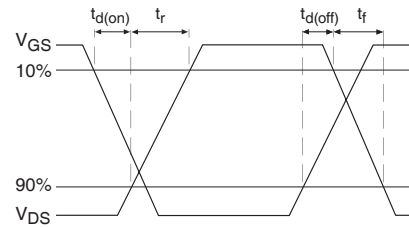


Fig 10b. Switching Time Waveforms

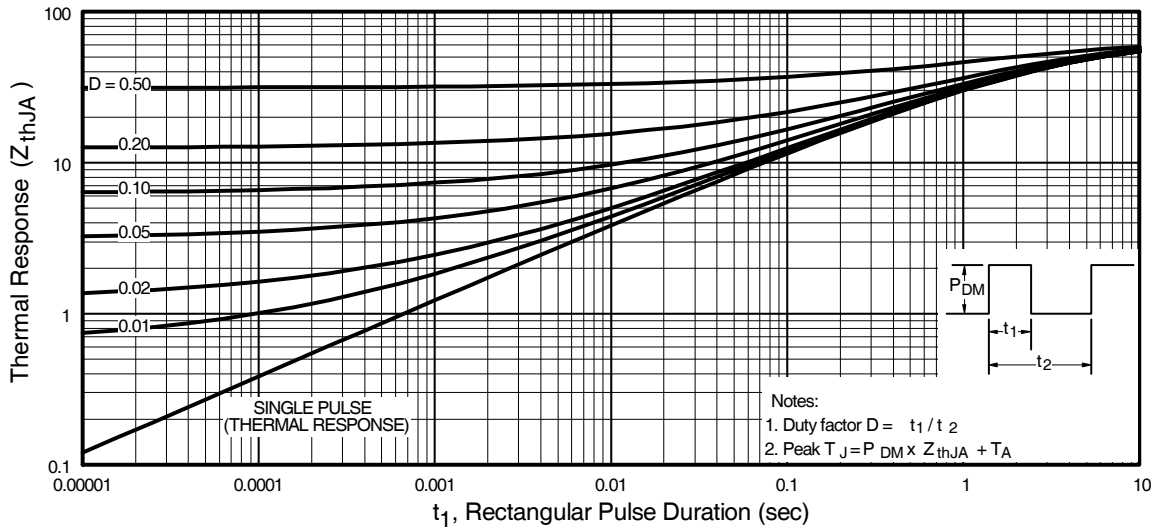


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

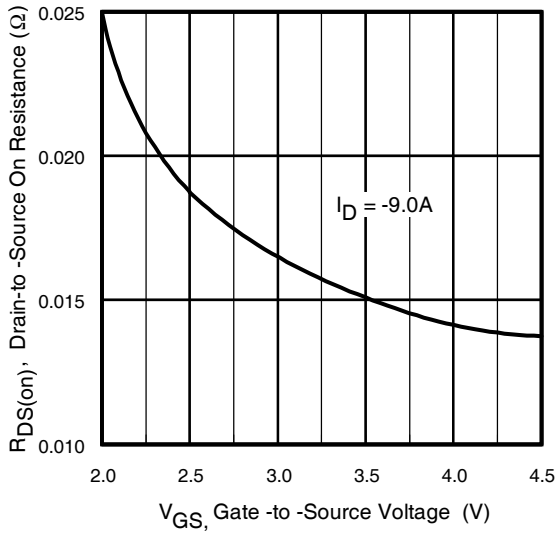


Fig 12. Typical On-Resistance Vs. Gate Voltage

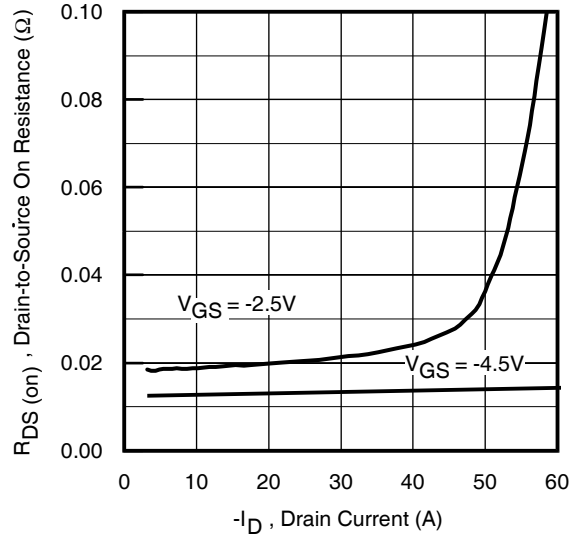


Fig 13. Typical On-Resistance Vs. Drain Current

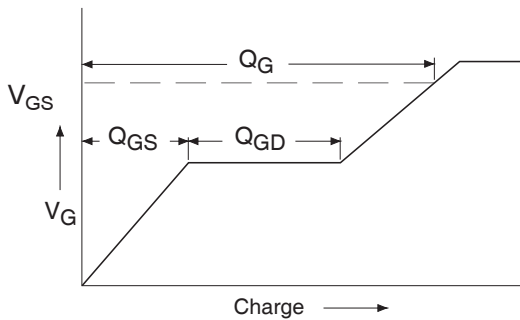


Fig 14a. Basic Gate Charge Waveform

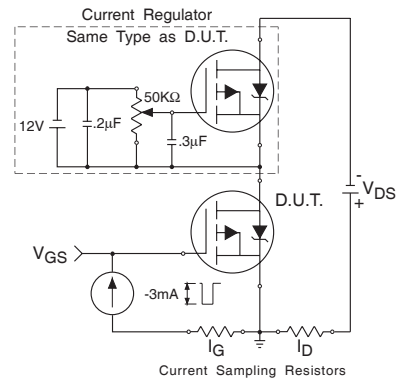
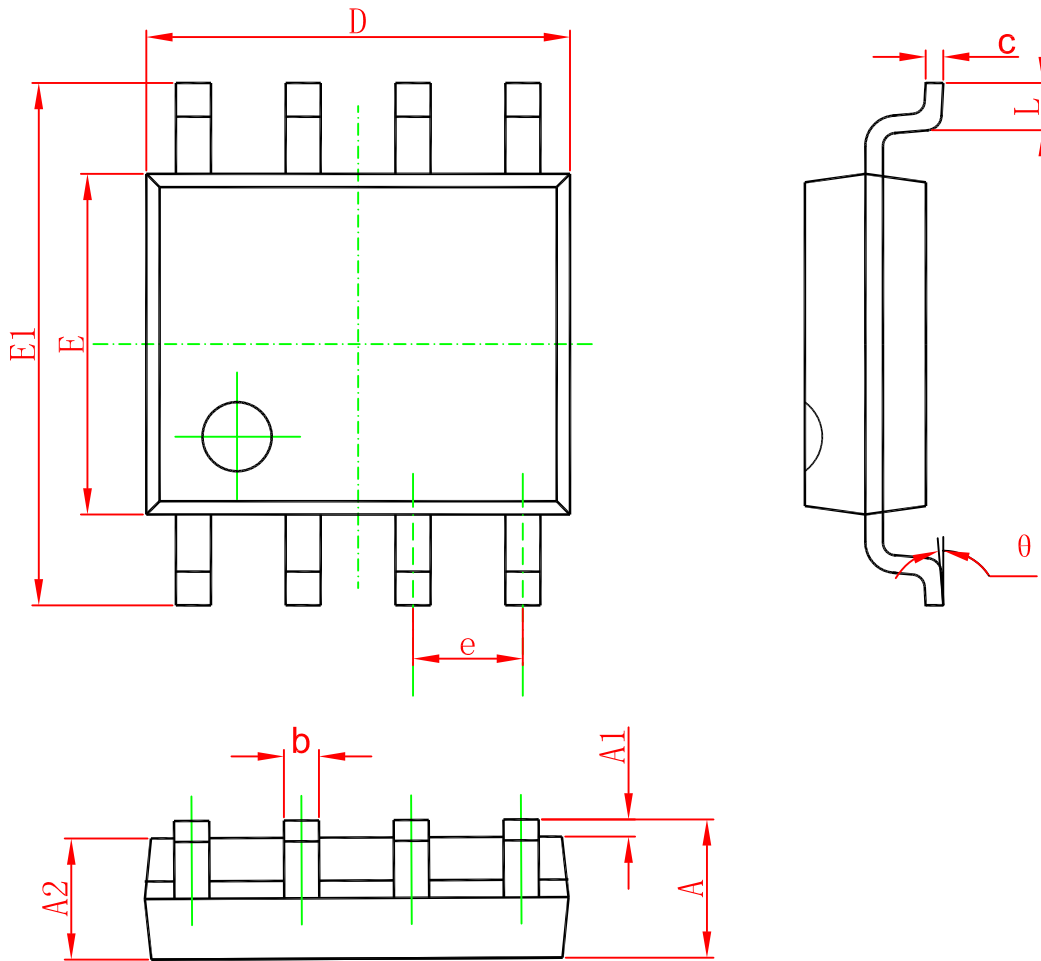


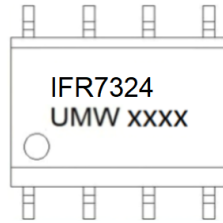
Fig 14b. Gate Charge Test Circuit

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°

Marking



Ordering information

Order code	Package	Baseqty	Deliverymode
UMW IRF7324TR	SOP-8	3000	Tape and reel

单击下面可查看定价，库存，交付和生命周期等信息

[>>UMW\(友台半导体\)](#)