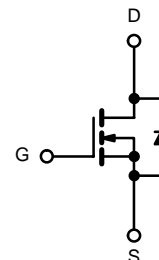


Description

The AO4404 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



N-Channel MOSFET

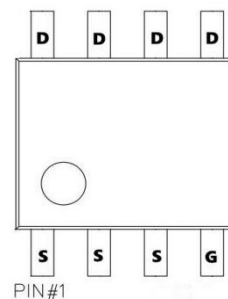
General Features

$V_{DS} = 30V$ $I_D = 8.5 A$

$R_{DS(ON)} < 20m\Omega$ @ $V_{GS}=10V$

Application

- Battery protection
- Load switch
- Uninterruptible power supply



Absolute Maximum Ratings $T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 16	
Continuous Drain Current ($T_J = 150\text{ }^\circ\text{C}$)	I_D	$T_C = 25\text{ }^\circ\text{C}$	12
		$T_C = 70\text{ }^\circ\text{C}$	11
		$T_A = 25\text{ }^\circ\text{C}$	$10^{b,c}$
		$T_A = 70\text{ }^\circ\text{C}$	$8^{b,c}$
Pulsed Drain Current	I_{DM}	47	A
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$	
		$T_A = 25\text{ }^\circ\text{C}$	$2.0^{b,c}$
Single Pulse Avalanche Current	I_{AS}	20	mJ
Avalanche Energy	E_{AS}	21	
Maximum Power Dissipation	P_D	$T_C = 25\text{ }^\circ\text{C}$	4.1
		$T_C = 70\text{ }^\circ\text{C}$	2.5
		$T_A = 25\text{ }^\circ\text{C}$	$2.2^{b,c}$
		$T_A = 70\text{ }^\circ\text{C}$	$1.3^{b,c}$
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$

Thermal resistance rating

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R_{thJA}	39	55	$^\circ\text{C/W}$
Maximum Junction-to-Foot (Drain)	R_{thJF}	25	29	

Notes:

- a. Base on $T_C = 25\text{ }^\circ\text{C}$.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. $t = 10\text{ s}$.
- d. Maximum under Steady State conditions is $85\text{ }^\circ\text{C/W}$.

Specifications $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted

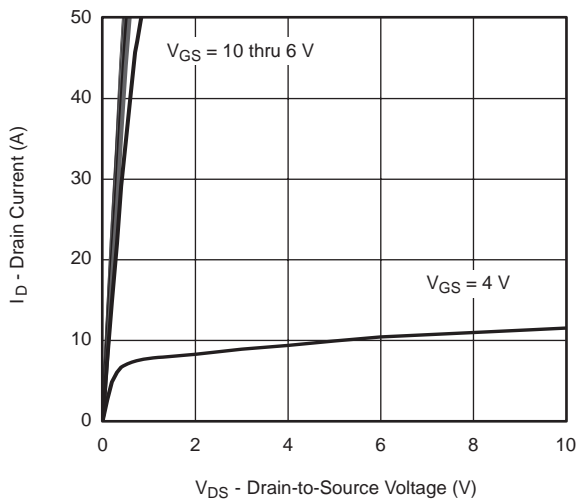
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	20			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		26		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			-6		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.0		3.0	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	20			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 8\text{ A}$		13	20	$\text{M}\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 3\text{ A}$		19	30	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\text{ V}, I_D = 10\text{ A}$		50		S
Input Capacitance	C_{iss}	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		800		pF
Output Capacitance	C_{oss}			165		
Reverse Transfer Capacitance	C_{rss}			73		
Total Gate Charge	Q_g	$V_{DS} = 10\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		15	23	nC
		$V_{DS} = 10\text{ V}, V_{GS} = 5\text{ V}, I_D = 10\text{ A}$		6.8	10.2	
Gate-Source Charge	Q_{gs}	$V_{DS} = 10\text{ V}, V_{GS} = 5\text{ V}, I_D = 10\text{ A}$		2.5		
Gate-Drain Charge	Q_{gd}			2.3		
Gate Resistance	R_g	$f = 1\text{ MHz}$	0.36	1.8	3.6	Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}, R_L = 1.4\text{ }\Omega$ $I_D \cong 9\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$		16	23	ns
Rise Time	t_r			12	16	
Turn-Off Delay Time	$t_{d(off)}$			16	22	
Fall Time	t_f			10	18	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}, R_L = 1.4\text{ }\Omega$ $I_D \cong 9\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		8	16	
Rise Time	t_r			10	20	
Turn-Off Delay Time	$t_{d(off)}$			16	22	
Fall Time	t_f			8	15	
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			10	A
Pulse Diode Forward Current ^a	I_{SM}				50	
Body Diode Voltage	V_{SD}	$I_S = 9\text{ A}$		0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 9\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		15	30	ns
Body Diode Reverse Recovery Charge	Q_{rr}			6	12	nC
Reverse Recovery Fall Time	t_a			8		ns
Reverse Recovery Rise Time	t_b			7		

Notes:

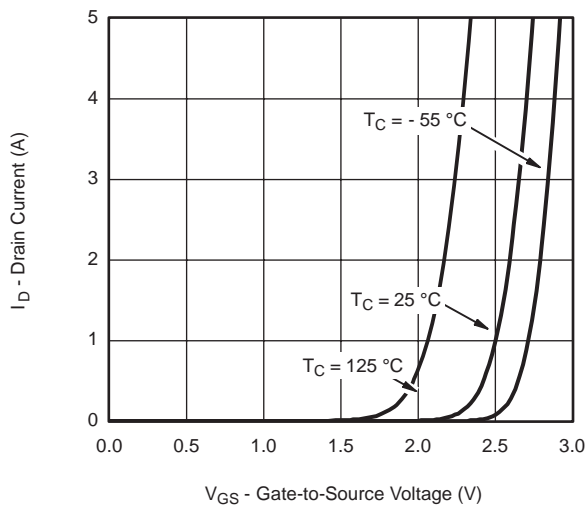
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

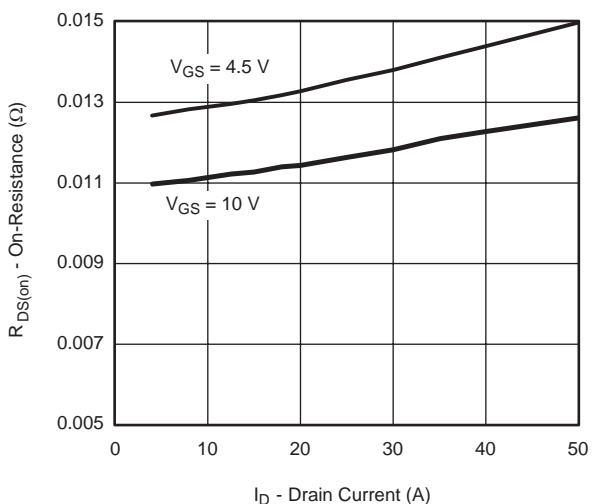
Typical characteristic 25 °C, unless otherwise noted



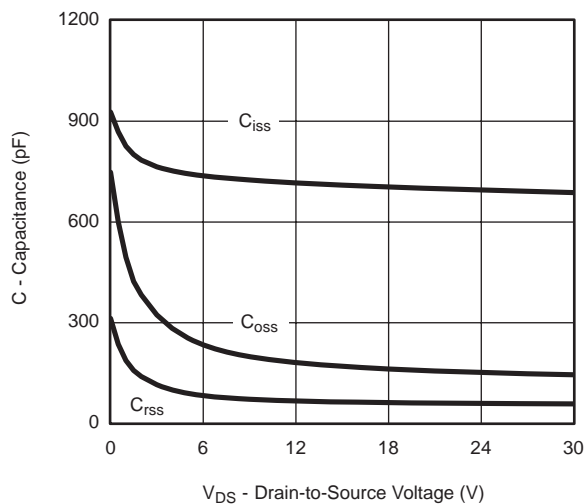
Output Characteristics



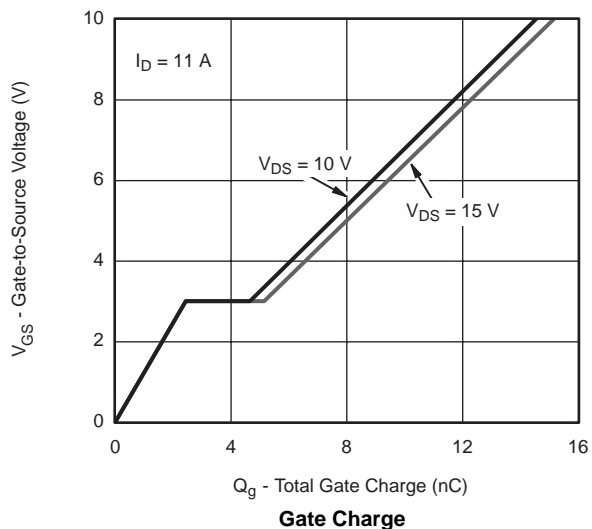
Transfer Characteristics



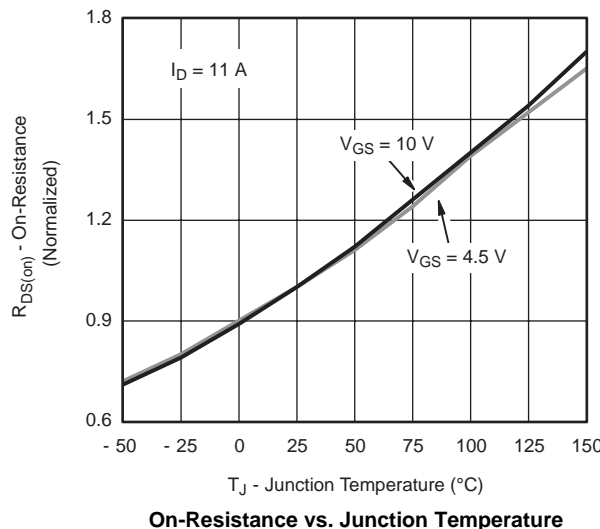
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

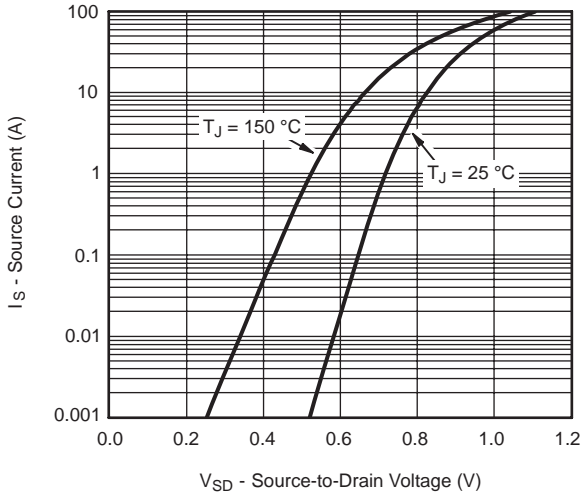


Gate Charge

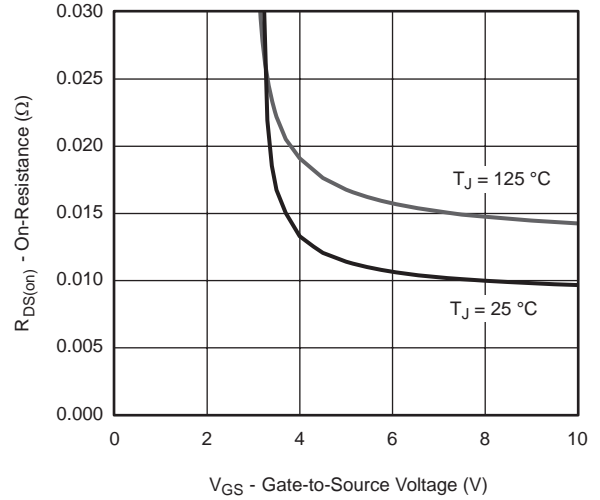


On-Resistance vs. Junction Temperature

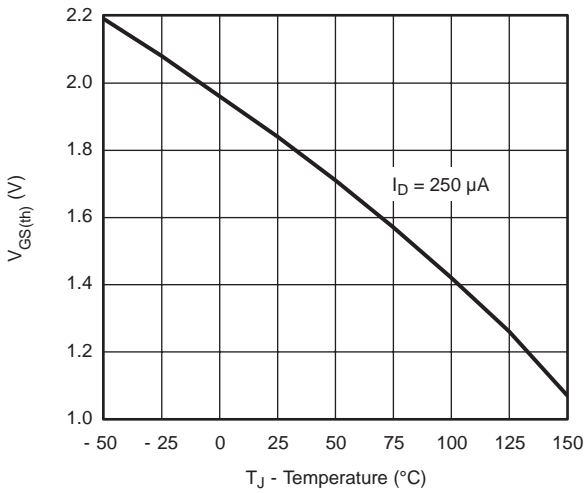
Typical characteristic 25 °C, unless otherwise noted



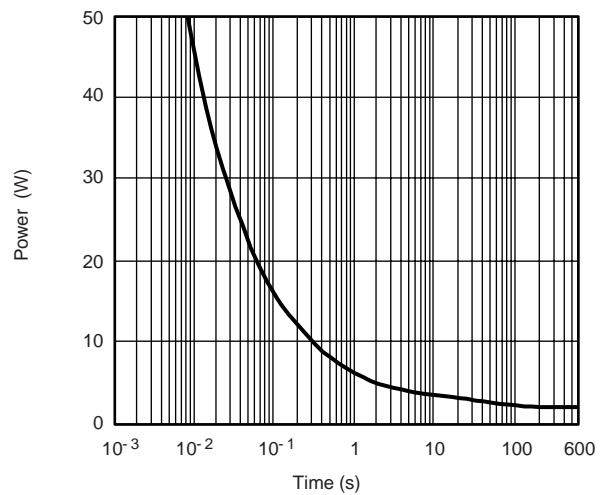
Source-Drain Diode Forward Voltage



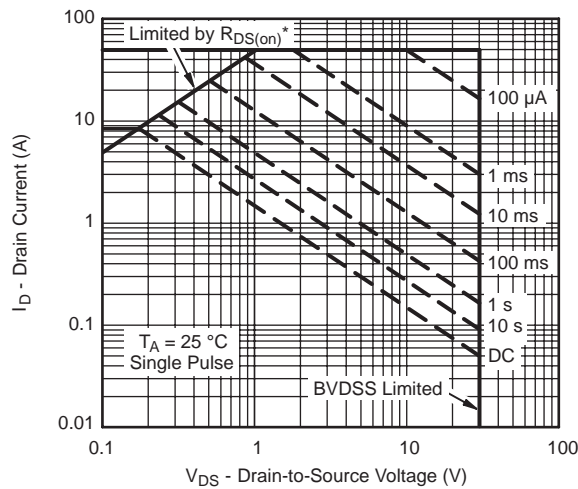
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



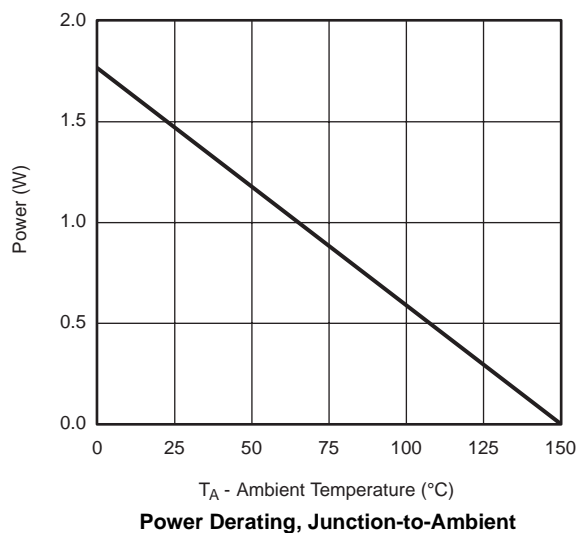
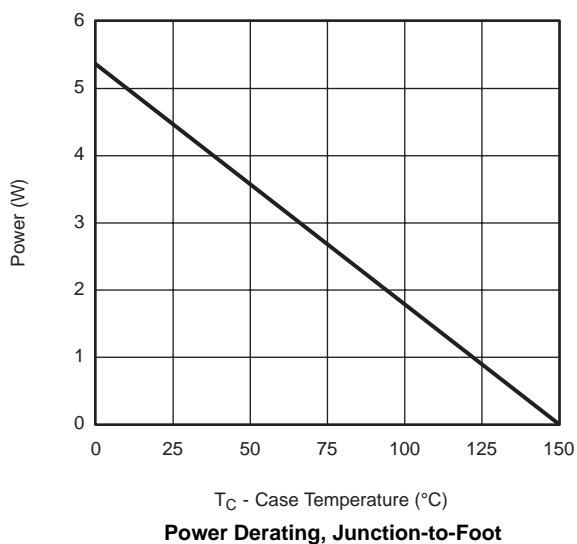
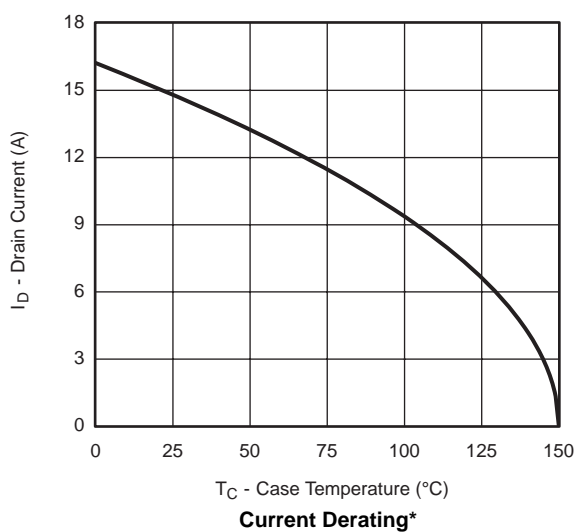
Single Pulse Power, Junction-to-Ambient



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

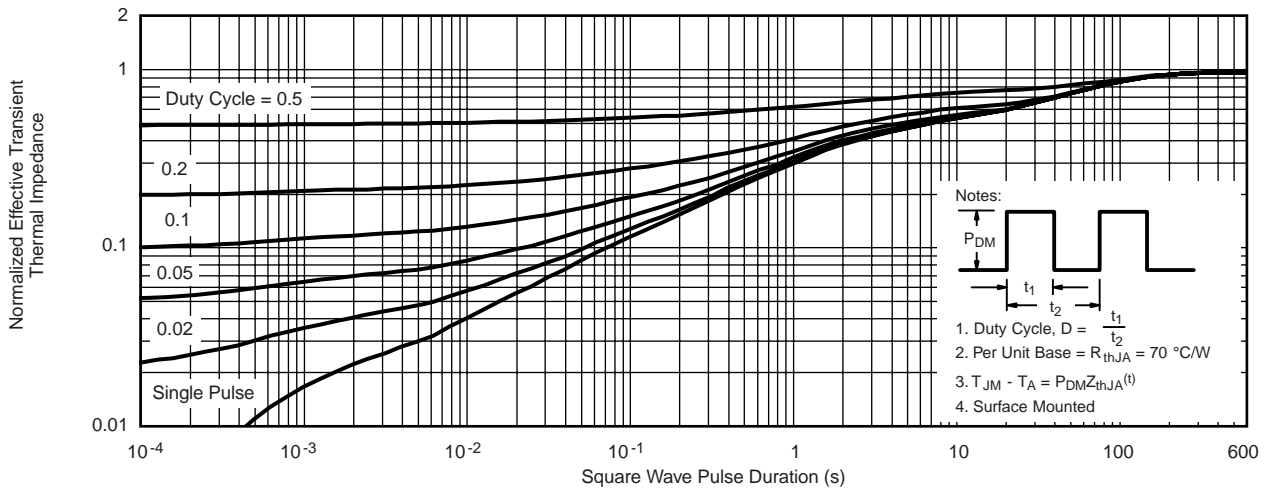
Safe Operating Area, Junction-to-Ambient

Typical characteristic 25 °C, unless otherwise noted

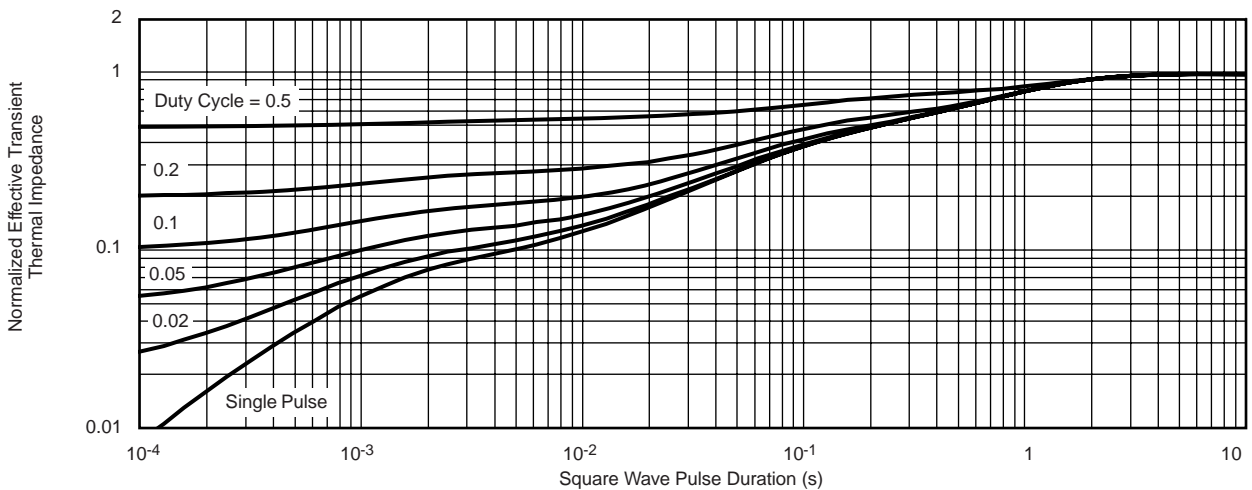


* The power dissipation P_D is based on $T_{J(max)} = 150\text{ °C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

Typical characteristic 25 °C, unless otherwise noted



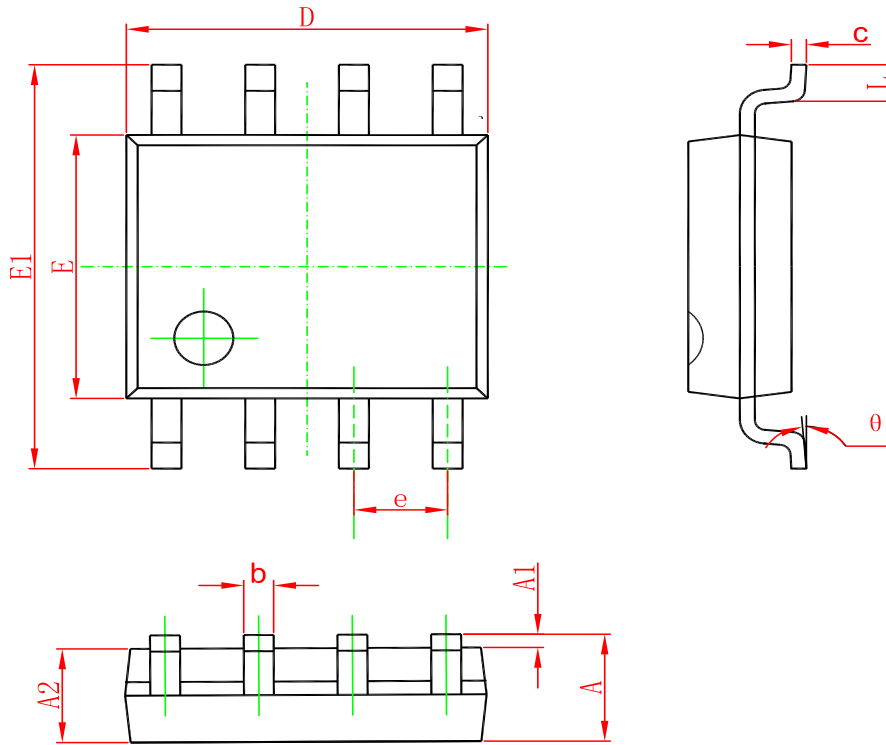
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

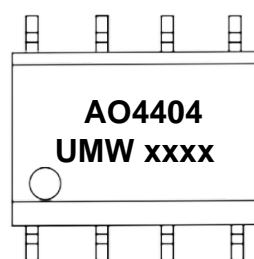
PACKAGE OUTLINE DIMENSIONS

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

Marking



("xxxx"代表年份周期)

Ordering information

Order code	Package	Baseqty	Deliverymode
UMW AO4404	SOP-8	3000	Tape and reel

单击下面可查看定价，库存，交付和生命周期等信息

[>>UMW\(友台半导体\)](#)